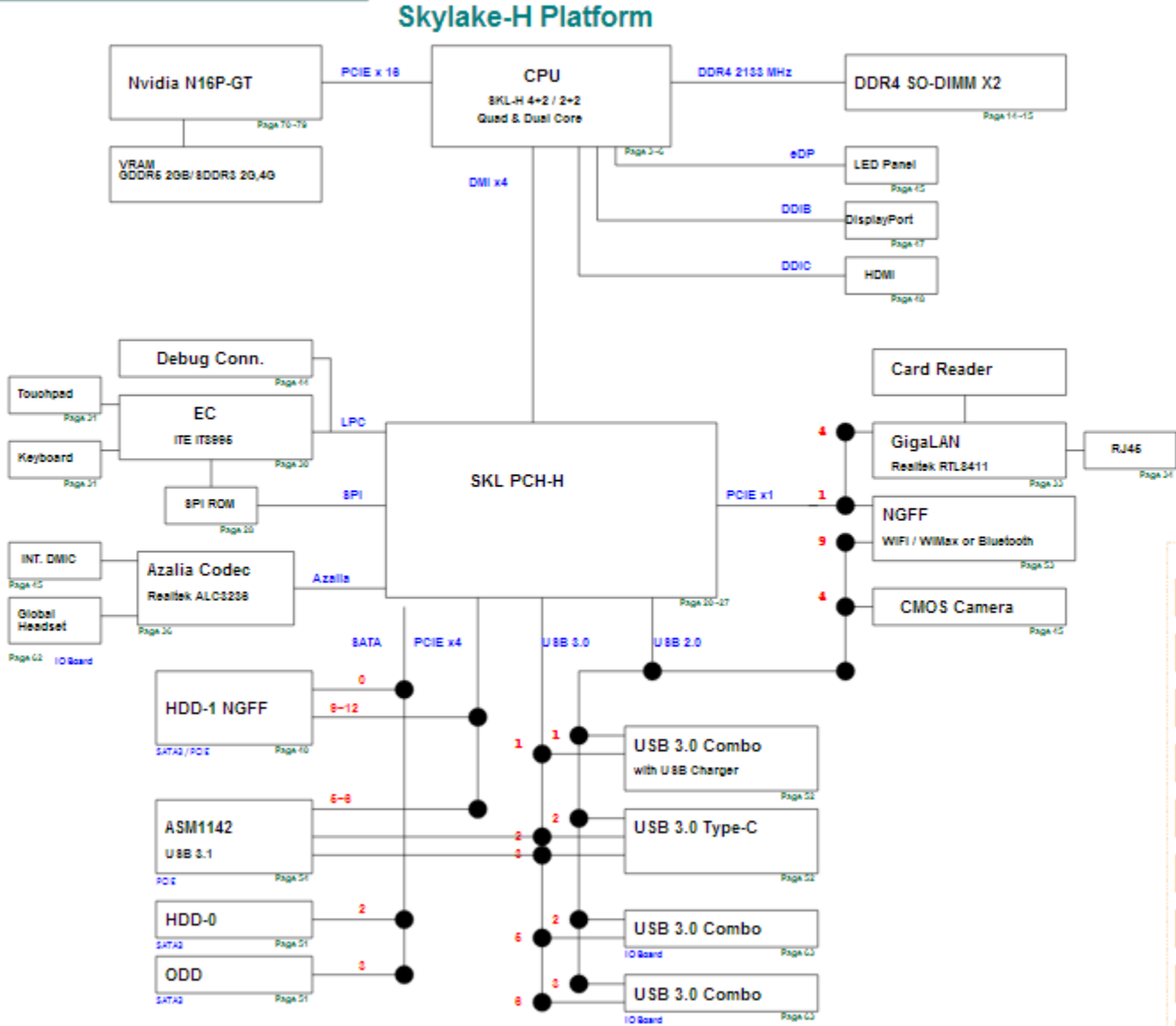


# N752VX Repair Guide

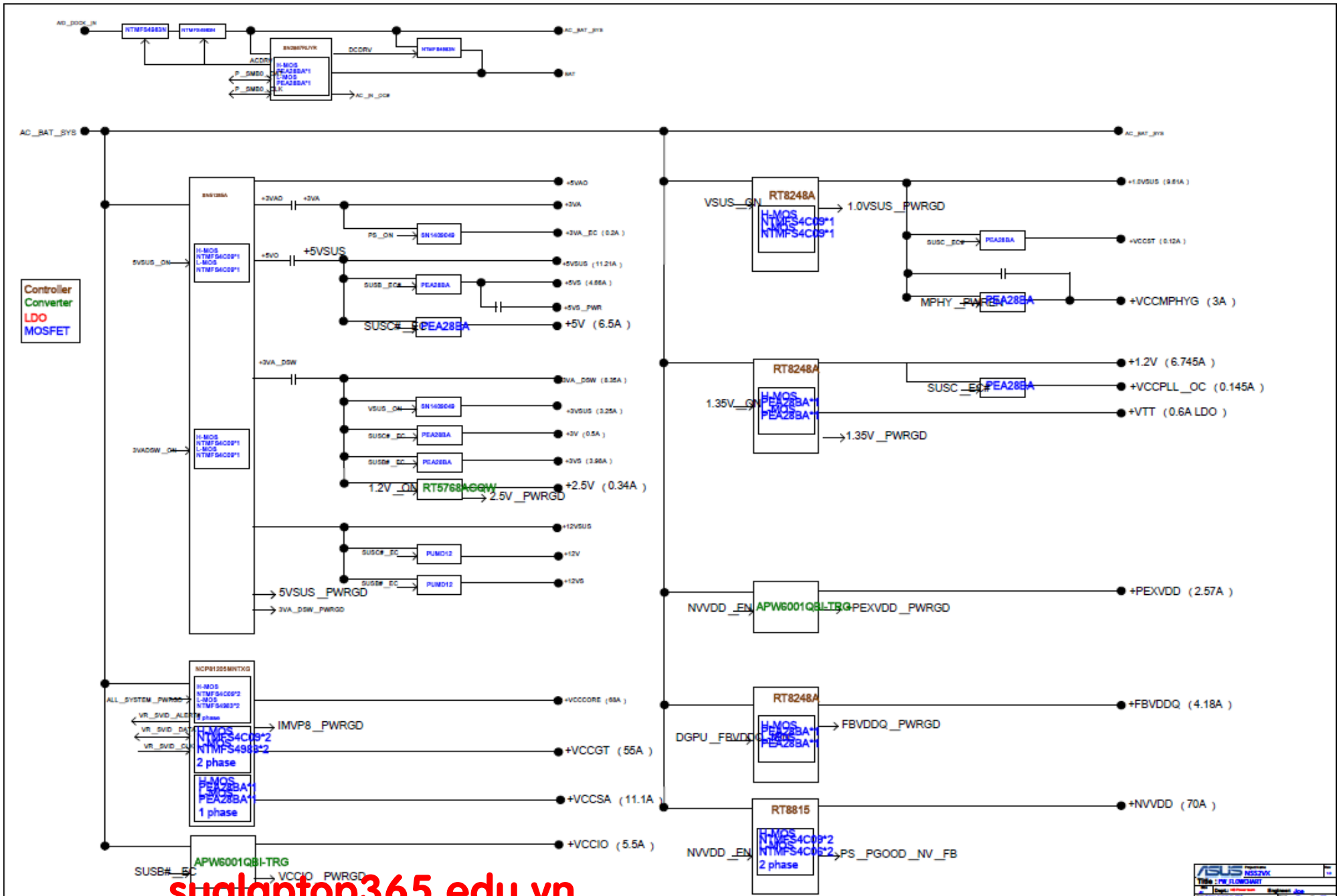
## BLOCK DIAGRAM

N752VX Block Diagram



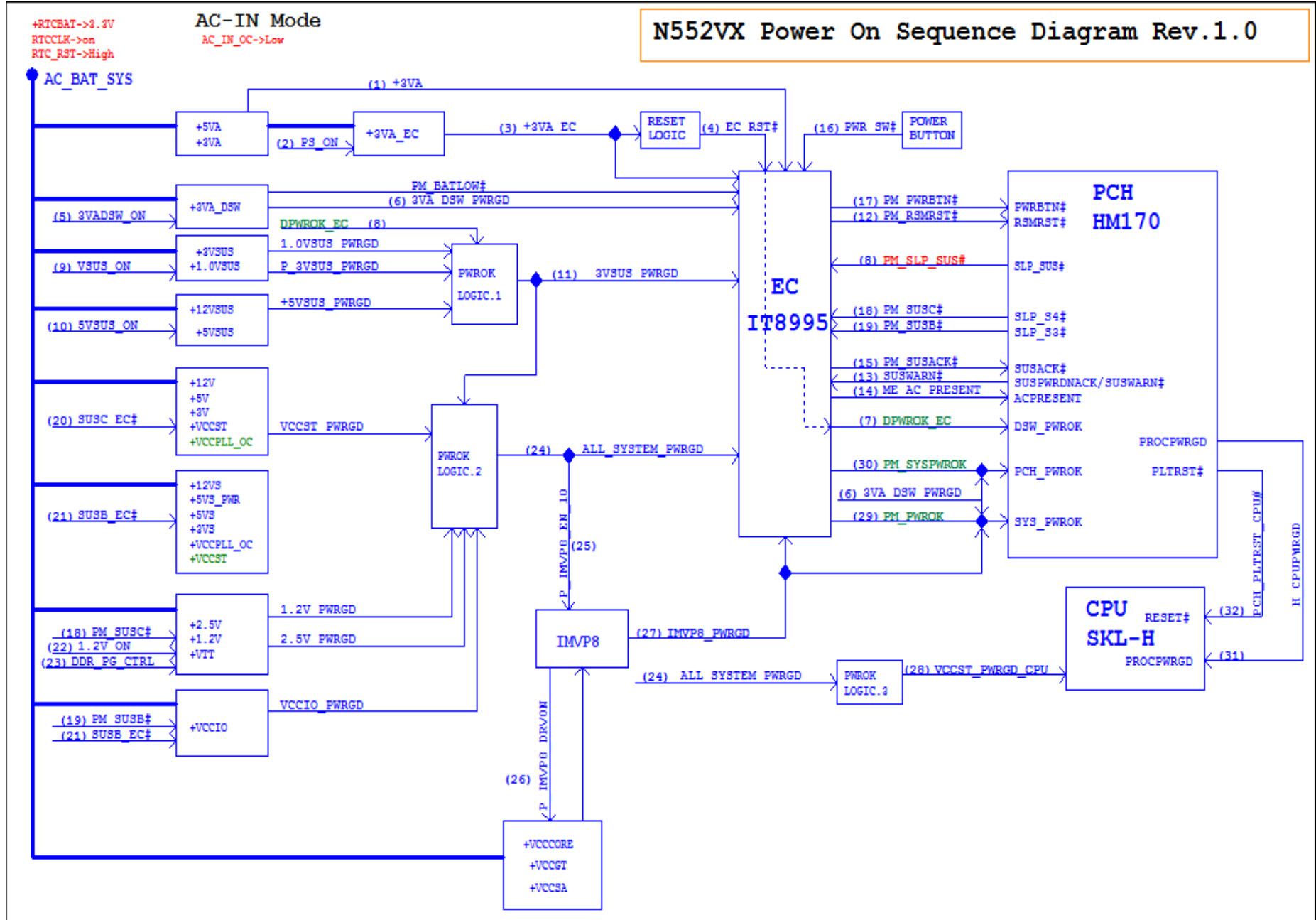
Power	
Power Skylake	Page 00-02
+1.0V 8U 8	Page 03
+VCCIO	Page 04
+1.2V(+VTTI)+2.6V	Page 05
+3VAD 8WI+6V 8U 8	Page 07
Load Switch	Page 08
Charger	Page 09
Protection	Page 10
+NVVDD	Page 11

# POWER FLOW

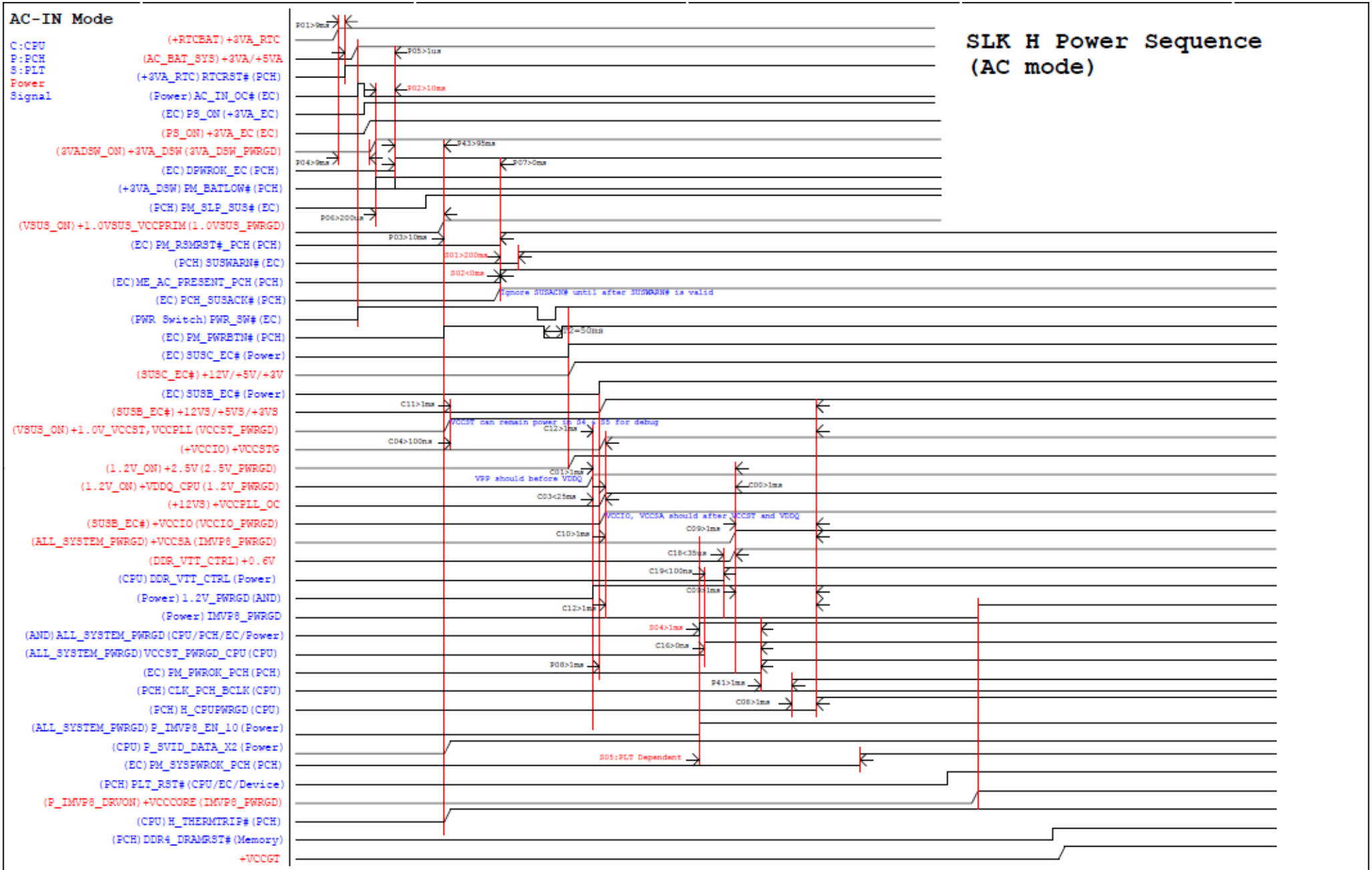


# POWER ON SEQUENCE

N552VX Power On Sequence Diagram Rev.1.0



# AC\_IN POWER ON SEQUENCE

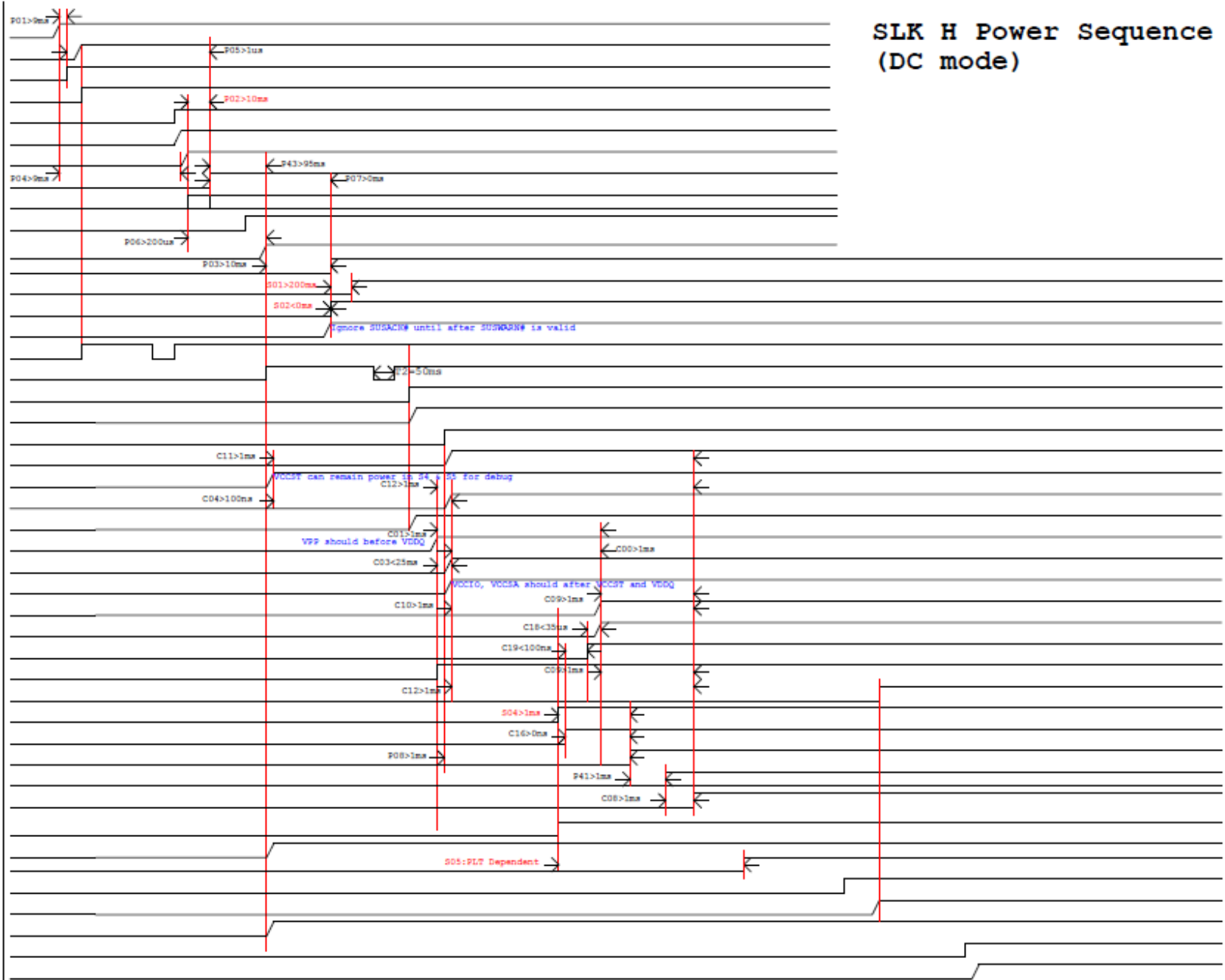


# DC\_IN POWER ON SEQUENCE

## DC-IN Mode

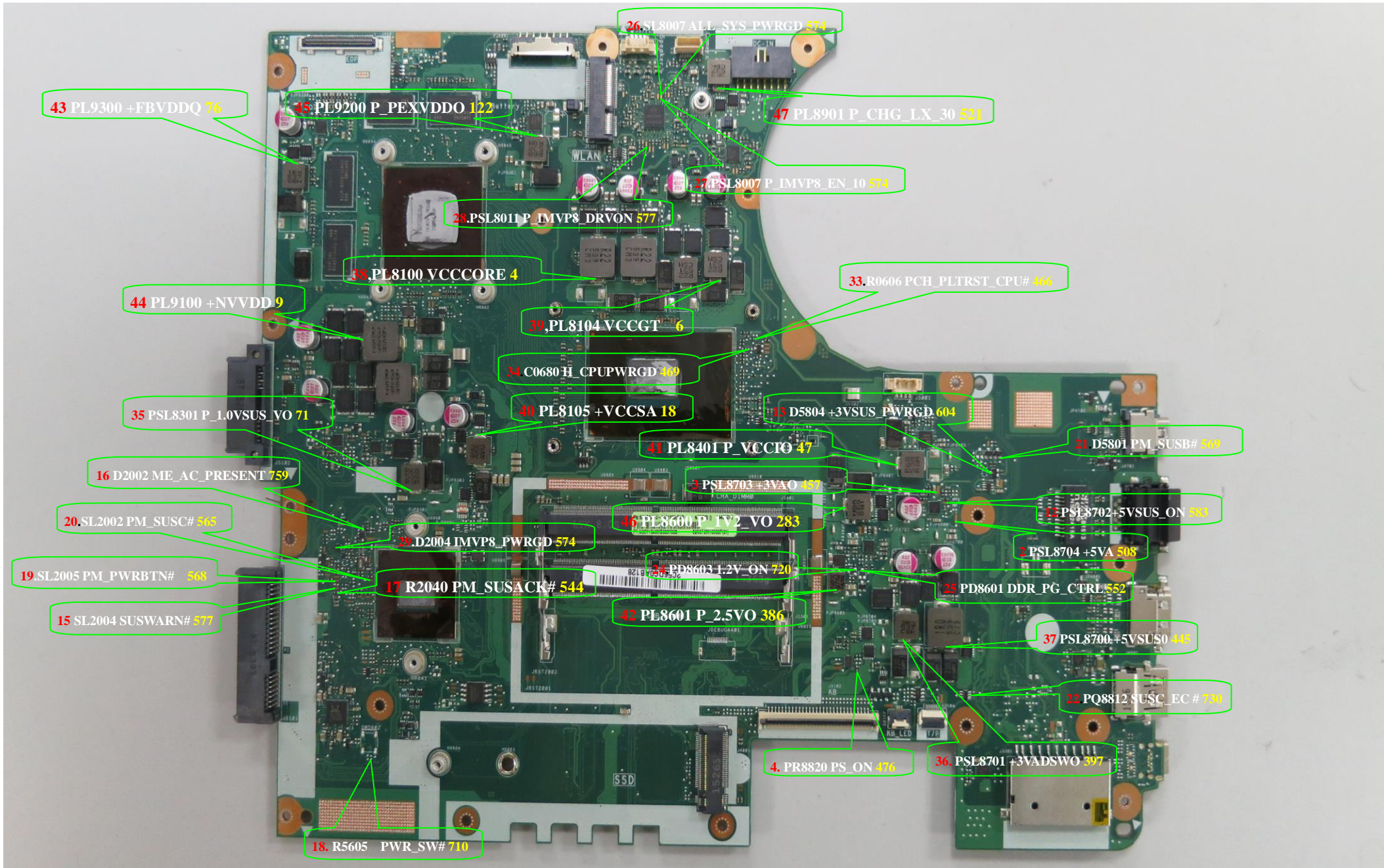
**C:** CPU  
**P:** PCH  
**S:** PLT  
**Power**  
**Signal**

(+RTCBAT) +3VA\_RTC  
 (AC\_BAT\_SYS) +3VA/+5VA  
 (+3VA\_RTC) RTCRST# (PCH)  
 (Power) AC\_IN\_OC# (EC)  
 (EC) PS\_ON (+3VA\_EC)  
 (PS\_ON) +3VA\_EC (EC)  
 (3VADSW\_ON) +3VA\_DSW (3VA\_DSW\_PWRGD)  
 (EC) DPWROK\_EC (PCH)  
 (+3VA\_DSW) PM\_BATLOW# (PCH)  
 (PCH) PM\_SLP\_SUS# (EC)  
 (VSUS\_ON) +1.0VSUS\_VCCPRIM (1.0VSUS\_PWRGD)  
 (EC) PM\_RST#\_PCH (PCH)  
 (PCH) SUSWARN# (EC)  
 (EC) ME\_AC\_PRESENT\_PCH (PCH)  
 (EC) PCH\_SUSACK# (PCH)  
 (PWR Switch) PWR\_SW# (EC)  
 (EC) PM\_PWRBTN# (PCH)  
 (EC) SUSC\_EC# (Power)  
 (SUSC\_EC#) +12V/+5V/+3V  
 (EC) SUSB\_EC# (Power)  
 (SUSB\_EC#) +12VS/+5VS/+3VS  
 (VSUS\_ON) +1.0V\_VCCST,VCCPLL (VCCST\_PWRGD)  
 (+VCCIO) +VCCSTG  
 (1.2V\_ON) +2.5V (2.5V\_PWRGD)  
 (1.2V\_ON) +VDDQ\_CPU (1.2V\_PWRGD)  
 (+12VS) +VCCPLL\_OC  
 (SUSB\_EC#) +VCCIO (VCCIO\_PWRGD)  
 (ALL\_SYSTEM\_PWRGD) +VCCSA (IMVP8\_PWRGD)  
 (DDR\_VTT\_CTRL) +0.6V  
 (CPU) DDR\_VTT\_CTRL (Power)  
 (Power) 1.2V\_PWRGD (AND)  
 (Power) IMVP8\_PWRGD  
 (AND) ALL\_SYSTEM\_PWRGD (CPU/PCH/EC/Power)  
 (ALL\_SYSTEM\_PWRGD) VCCST\_PWRGD\_CPU (CPU)  
 (EC) EM\_PWROK\_PCH (PCH)  
 (PCH) CLK\_PCH\_BCLK (CPU)  
 (PCH) H\_CPUPWRGD (CPU)  
 (ALL\_SYSTEM\_PWRGD) P\_IMVP8\_EN\_10 (Power)  
 (CPU) P\_SVID\_DATA\_X2 (Power)  
 (EC) PM\_SYSPWROK\_PCH (PCH)  
 (PCH) PLT\_RST# (CPU/EC/Device)  
 (P\_IMVP8\_DRVON) +VCCCORE (IMVP8\_PWRGD)  
 (CPU) H\_THERMTRIP# (PCH)  
 (PCH) DDR4\_DRAMRST# (Memory)  
 +VCCGT



## SLK H Power Sequence (DC mode)

# Signal Measure Point-Bottom



# Signal Measure Point-Top

