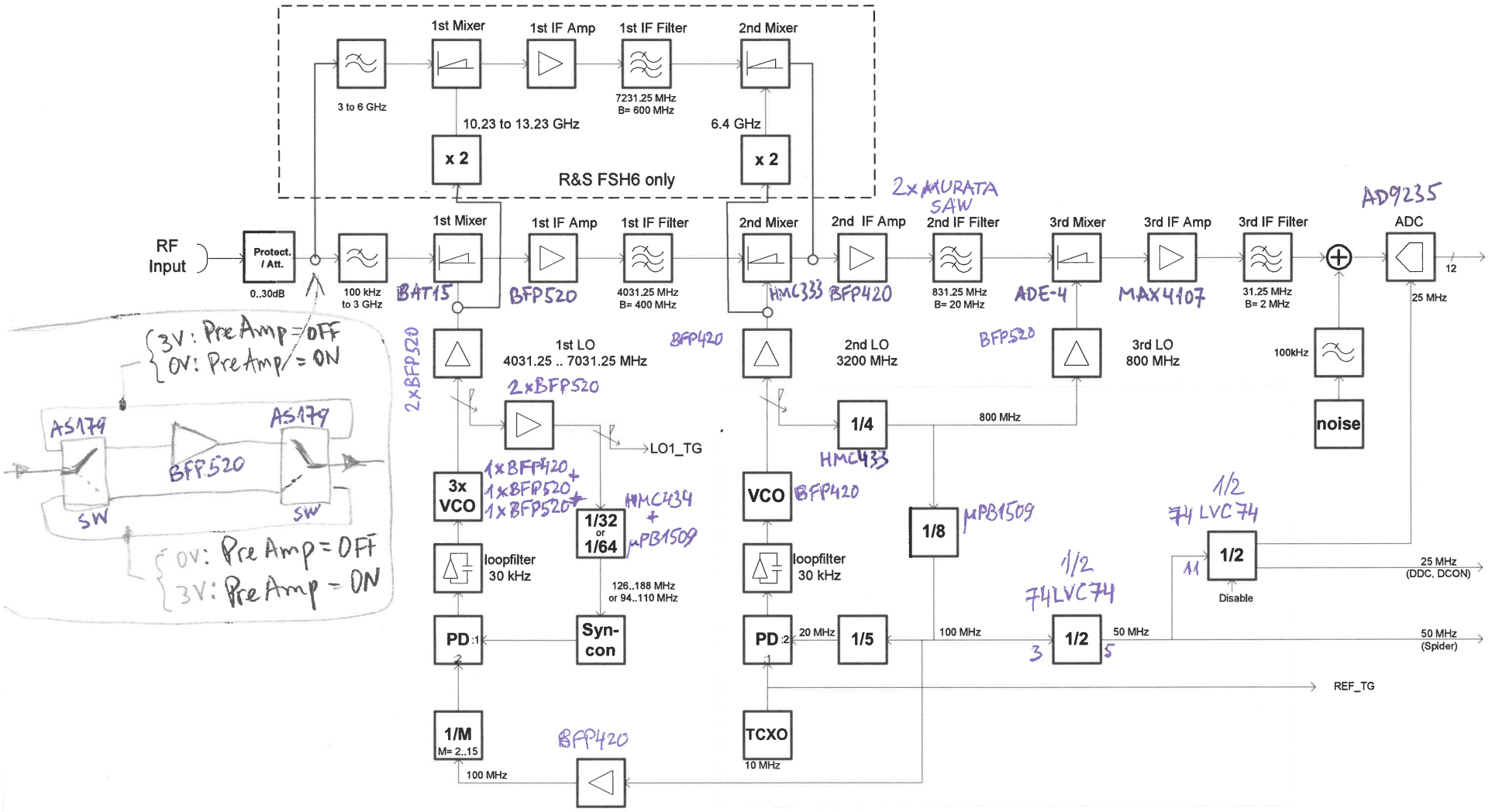

Fig. 3-2 Block Diagram: RF to IF Conversion and Synthesizer