

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Dez	Hex	Active Feature	Config Register	
32768	16384	8192	4096	2048	1024	512	256	128	64	32	16	8	4	2	1					
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	OperationalStatus Deviceis currentlyperforminga conversion
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	32768	8000	1	32768	OperationalStatus Deviceis not currentlyperforminga conversion
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	AINP= AIN0and AINN= AIN1(default)
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	4096	1000	0	0	AINP= AIN0and AINN= AIN3
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	8192	2000	0	0	AINP= AIN1and AINN= AIN3
0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	12288	3000	0	0	AINP= AIN2and AINN= AIN3
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	16384	4000	1	16384	AINP= AIN0and AINN= GND
0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	20480	5000	0	0	AINP= AIN1and AINN= GND
0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	24576	6000	0	0	AINP= AIN2and AINN= GND
0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	28672	7000	0	0	AINP= AIN3and AINN= GND
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	FSR= ±6.144V
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	512	200	0	0	FSR= ±4.096V(1
0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1024	400	0	0	FSR= ±2.048V (default)
0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	1536	600	0	0	FSR= ±1.024V
0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2048	800	0	0	FSR= ±0.5
0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	2560	A00	0	0	FSR= ±0.256V
0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	3072	C00	0	0	FSR= ±0.256V
0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	3584	E00	1	3584	FSR= ±0.256V
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Continuous-conversionmode
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	256	100	1	256	Single-shotmodeor power-downstate(default)
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	8 SPS
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	32	20	0	0	16 SPS
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	64	40	0	0	32 SPS
0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	96	60	0	0	64 SPS
0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	128	80	0	0	128 SPS
0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	160	A0	0	0	250 SPS
0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	192	C0	0	0	475 SPS
0	0	0	0	0	0	0	0	1	1	1	0	0	0	0	0	224	E0	0	0	860 SPS
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	Comparatormode Traditionalcomparator(default)
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	16	10	0	0	Comparatormode Window Comparator
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	Comparatorpolarity Activelow (default)
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	8	8	0	0	Comparatorpolarity Activehigh
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Latchingcomparator Nonlatchingcomparator.
0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	4	4	1	4	Latchingcomparator Latchingcomparator
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	Assertafterone conversion
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	Assertaftertwo conversions
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	2	2	0	0	Assertafterfour conversions
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	3	3	0	0	Disablecomparatorand set ALERT/RDYpin to high-impedance(default)

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Ergebnis: CF04 (Config Register Value)