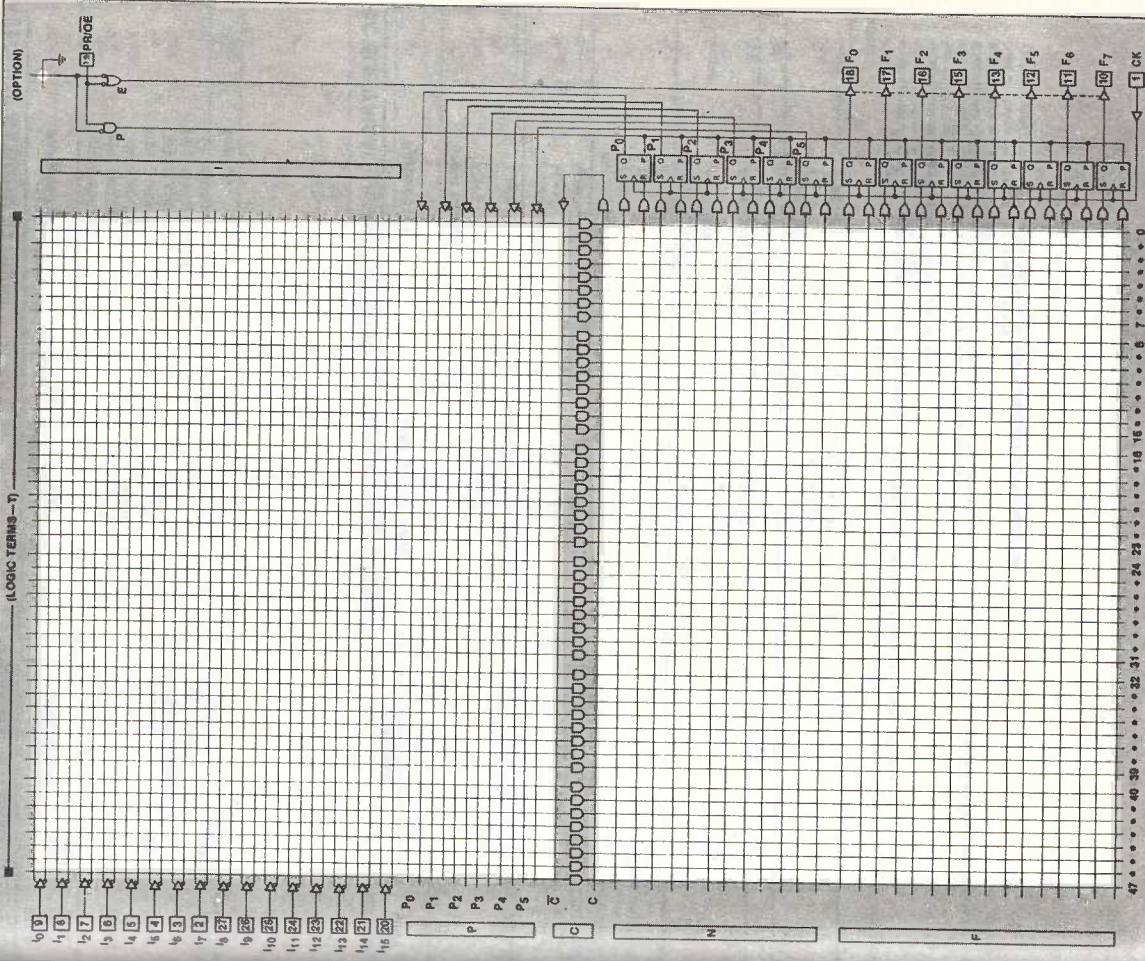


FIELD PROGRAMMABLE LOGIC SEQUENCER (16 x 48 x 8)

82S105A (T.S.)

INTEGRATED FUSE LOGIC SERIES 28

FPLS LOGIC DIAGRAM



- NOTES
1. All AND gate inputs with a blown link float to a logic '1'.
 2. All OR gate inputs with a blown link float to a logic '0'.
 3. Programmable connection.