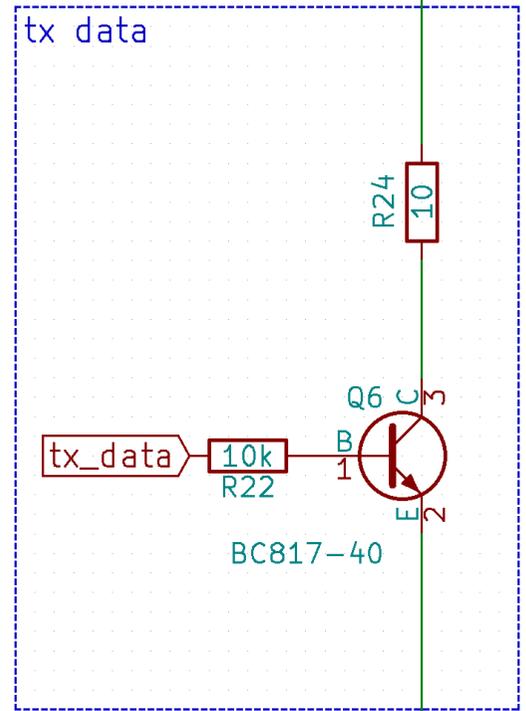Initial scheme did not work well at my system

interface to siedle "in-home-bus"

P1
CONN_01X02

2 bus(Ta)+
1 bus(Tb)−

BUS+

rx data

target threshold: 5V
=> 5V−0.7=4.3V
=> 4.3V/5V=(R10+R11)/(R10+R11+R9)
=> 4.3/5=0.86

VCC

BC857
Q2

rx_data

VCC

C5

100n

GND

R9
1k5

R10
8k2

R8
10k

R11
560

tx start

R17
100

R18
100

Q4

tx_start

R15
10k

BC817−40

tx data

R24
10

Q6

tx_data

R22
10k

BC817−40
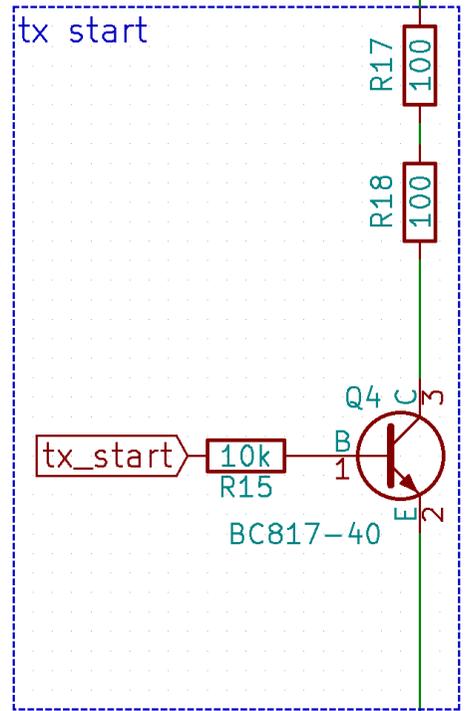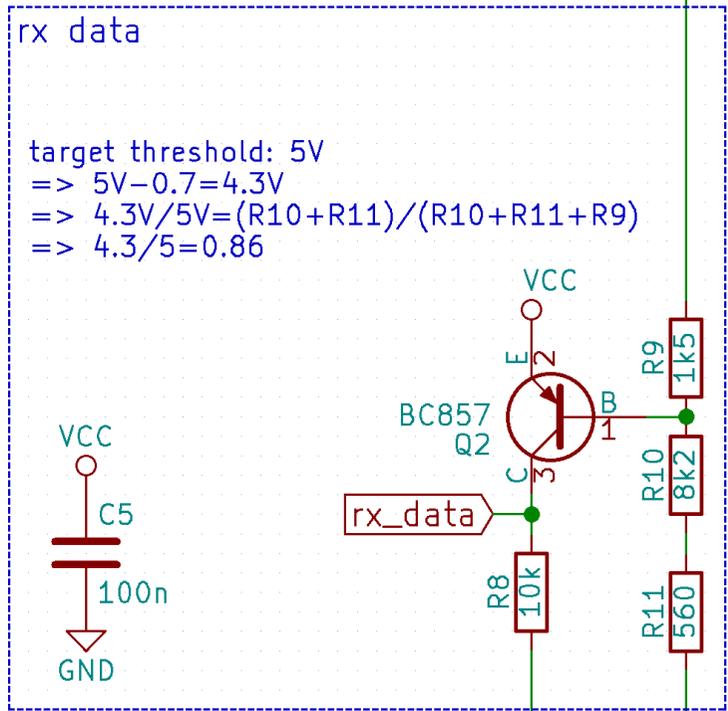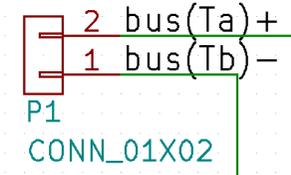
GND

I has changed it as below.

interface to siedle "in-home-bus"

2 bus(Ta)+
1 bus(Tb)−

BUS+

P1
CONN_01X02

rx data

target threshold: 5V
=> 5V−0.7=4.3V
=> 4.3V/5V=(R10+R11)/(R10+R11+R9)
=> 4.3/5=0.86

VCC

VCC

C5

100n

GND

BC857
Q2

E 2
B 1
C 3

rx_data

R8 10k

R9 1k5

R10 8k2

R11 560

tx start

R17 100

R18 100

tx_start

Q4
B 1
C 3
E 2

BC817−40

R15 10k

tx data

R24 10

tx_data

Q6
B 1
C 3
E 2

BC817−40

R22 10k

GND