

PRODUCT: TFT TOUCH MODULE

MODULE NO.: WKS116FHD001-WCT

SUPPLIER: WKS Technology Co.,LTD

DATE: Oct 15, 2018

SPECIFICATION

Revision: 0.0

WKS116FHD001-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

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REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2018-10-15	First release	Preliminary

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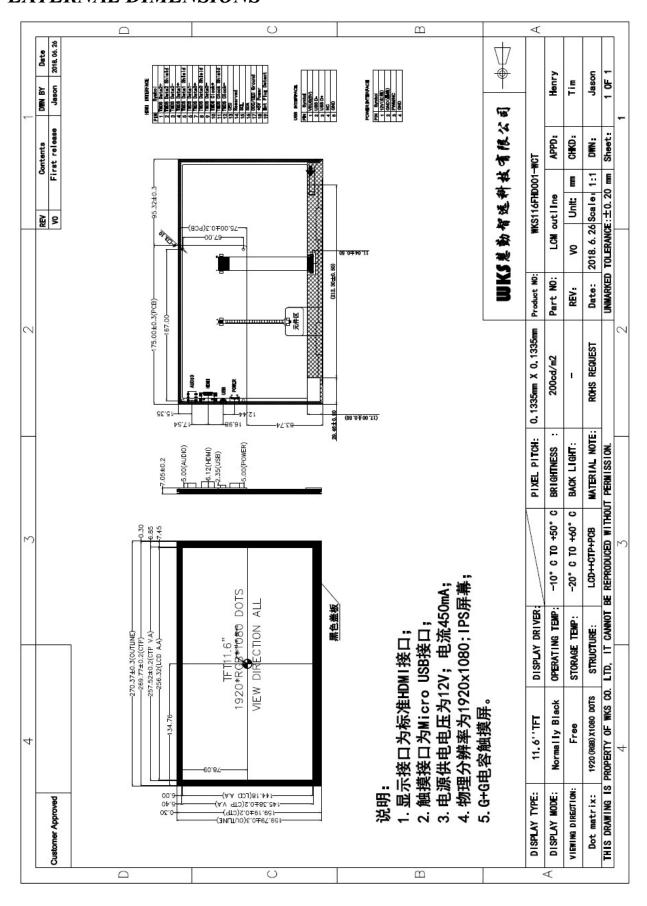


1. GENERAL INFORMATION

Item of general information		Contents	Unit
LCD Display Size (Diagonal)		inch	
Module Structure	LCD Displa	y + CTP Touch + PCB	-
LCD Display Type		TFT	-
LCD Display Mode	No	ormally Black	-
Recommended Viewing Direction		Free	-
Module size (W×H×T)	270.3	37×159.79×7.05	mm
Active area (W×H)	25	56.32×144.18	mm
Number of pixels (Resolution)	19	20RGB×1080	pixel
Pixel pitch (W×H)	0.	1335×0.1335	mm
Color Pixel Arrangement		RGB Stripe	-
M 1 1 L C T	LCD Display	HDMI interface	-
Module Interface Type	СТР	Micro USB interface	-
	Win7/Win8	/Win10(Plug and play)	-
System Support	Android/Linux (-	
Power Supply	Exter	-	
Color Numbers		-	
Backlight Type		White LED	-



2, EXTERNAL DIMENSIONS





3, ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Тор	-10	60	${\mathscr C}$
Storage temperature	Tst	-20	70	$^{\circ}\!C$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4, ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Power	9V	12V	16V	V
Supply current	Power	-	450	-	mA
DI DWM since al college	V _{PWM} H	1.85	-	3.3	V
BL PWM signal voltage	$V_{PWM}L$	0	-	0.7	V
Brightness Control Duty Ratio	Duty	1	-	100	%
Brightness Control pulse width	ТРWМ	5	-	-	us
Brightness Control frequency	fРWM	200	-	2000	Hz



Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1920 × 1080	pixel	-
Surface Hardness	6Н	-	-
Transparency	≥86%	-	-
Driver IC	-	-	-
Interface Type	Micro USB interface	-	-
Support Points	10(MAX)	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	5	V	-

6, ELECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0.0	-	25	-	ms	FIG 1.	4
Contrast l	Ratio	CR	$\theta=0$ $\varnothing=0$	-	700	-	-	FIG 2.	1
Luminance un	iformity	<i>SWHITE</i>	Ta=25°C	_	80	-	%	FIG 2.	3
Surface Lum	inance	Lv	10 20 0	-	200	-	cd/m2	FIG 2.	2
CIE (x, y)		White x	$\theta = 0$	0.283	0.313	0.343			
chromaticity	White	White y	Ø=0 Ta=25°C	0.299	0.329	0.359	1	FIG 2.	5
	Ø=90(1	2 o'clock)		80	89	-	deg		
. 8	(6 o'clock)	CD > 10	80	89	-	deg	FIG 3.		
	Ø=0(3	o'clock)	CR ≥ 10	80	89	-	deg	F1G 3.	6
	Ø=180 ₀	9 o'clock)		80	89	-	deg		
NTSC ratio		-	-	-	72	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (SWHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.



 $\delta \text{WHITE} = \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 4. The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For more information see FIG 1.

Note 5. CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

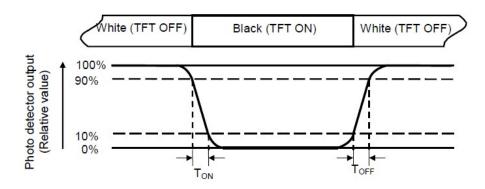




FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

CIE(x, y) chromaticity

A: H/6; B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

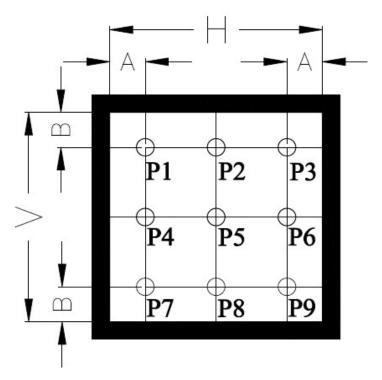
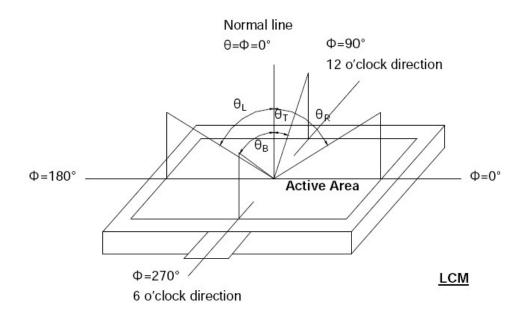


FIG.3. The definition of viewing angle





7. INTERFACE DESCRIPTION

A. HDMI Interface Description

NO.	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect

B. USB TOUCH Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power
2	D-	USB Data-
3	D+	USB Data+
4	NC	No connection
5	GND	Power Ground

C, External POWER Interface Description

NO.	Symbol	DESCRIPTION		
1	9 <i>V~16V</i>	External power input(9V~16V)		
2	GND	Power Ground		
3	PWM/NC	Brightness Control input signal(200~2000Hz)		
4	GND	Power Ground		

8, LCD TIMING

Down out on	Come bol		Unit		
Parameter	Symbol	Min.	Тур.	Max.	Unu
DCLK frequency@ Frame rate=60Hz	DCLK	-	138.5	-	MHz
Horizontal display area	thd		1920		DCLK
1 Horizontal Line	th	-	2080	-	DCLK
HSYNC pulse width	thpw	-	-	-	DCLK
HSYNC Back Porch(Blanking)	thb	-	-	-	DCLK
HSYNC Front Porch	thfp	-	-	-	DCLK
Vertical display area	tvd		1080		Н
VSYNC period time	tv	-	1111	-	Н
VSYNC pulse width	tvpw	-	-	-	Н
VSYNC Back Porch(Blanking)	tvb	-	-	-	Н
VSYNC Front Porch	tvfp	-	-	-	Н

9, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	70°C/120 hours
2	Low Temperature Storage	-20°C/120 hours
3	High Temperature Operating	60°C/120 hours
4	Low Temperature Operating	-10°C/120 hours
5	Temperature Cycle Storage	-10°C(30min.)~25(5min.)~60°C(30min.)×10cycles

A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- Missing segments;
- ➤ Glass crack;
- Current is twice higher than initial value.

B. Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



10 SINSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

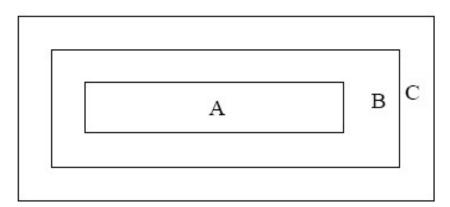
Minor defect: AQL 1.5

10.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of $20\sim40W$ light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature $20\sim25$ ° Cand normal humidity 60 $\pm15\%$ RH)

10.3 Definition of Inspection Item.

A, Definition of inspection zone in LCD.





Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone $A + Zone B = minimum \ Viewing \ area)$

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig. 1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B. Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the					
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under					
	black pattern.					
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying					
	under pure red, green, blue picture, or pure whiter picture.					

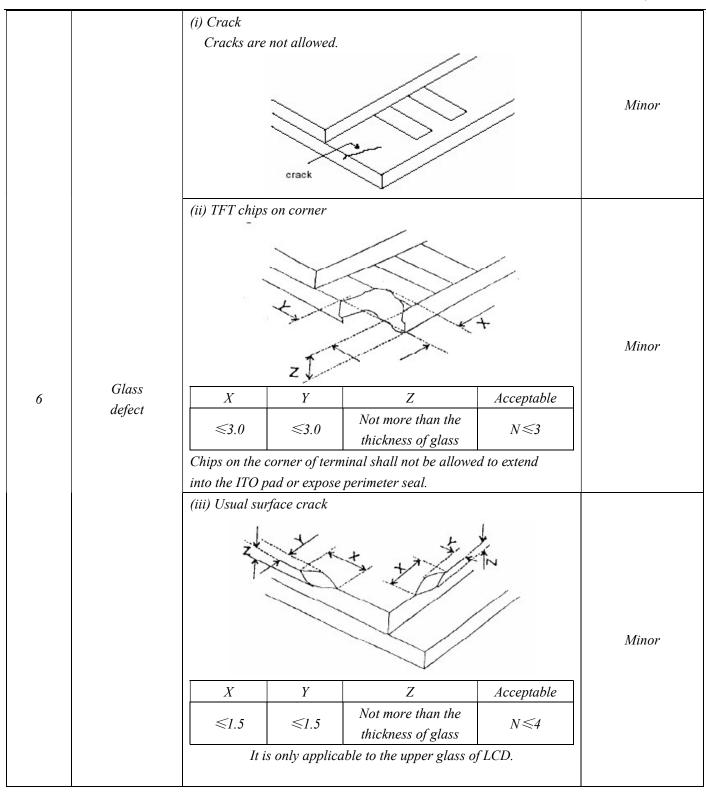
10.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
I	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

10.5. Minor Defect

Item No.	Items to be	Inspection standard						Classification of defects		
			Zone		Acceptable Qty A+B					
					4.3" ~ 7"	7~10.1	>10.1"	С		
		Bri	ght pixel do	t	1	2	3			
	Bright dot	Dark pixel dot			4	4	4	A_0		
1	/dark dot	2brigh	t dots adje	icent	0	0	0	Acceptable	Minor	
	defect	2dark	dots adja	cent	0	0	0	ntab.		
		Total b	oright and dots	dark	5	6	7	le		
		Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.								
		Zone Acceptable Qty								
				A+B					11	
		Size(mm) $\Phi \leqslant 0.2$ $0.2 < \Phi \leqslant 0.5$ $\Phi > 0.5$		4.3"~7" 7		7~10.1"	~10.1" >10.1"			
	Dot defect			Accepto	Acceptable Acceptable Accept		Acceptable	Acceptable	Minor	
2				4		5	6			
2				0		0	0	ıble	Winor	
		Note: 1. Minimu 2. The qua					nore than 5 mi	m;		
3	Linear defect		Zone		£	Acceptable	Qty			
		Size (mm)		A+B						
		Linear Length	Width	4.3"~	-7"	7~10.1"	>10.1"	С	Minor	
		Ignore	<i>W</i> ≤0.05	Accepto	able A	cceptable	Acceptable	Ac	Millor	
			L ≤5.0	0.05 < W≤0.1	4		5	6	Acceptable	
		L>5.0	W>0.1	0		0	0	le		

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4	Polarizer defect	(i) Shifting dimension (ii) Incompallowed. 5.4.2 Dirt Dirt which 5.4.3 Pold Size(mm, \$\Phi\$: 0.2 < \Phi\$: 5.4.4 Pold (i) If the part or in the (ii) If the part of the part	on polarizer h can be warizer Denti Zone \$\int 0.2\$ \$\int 0.5\$ \$\int 0.5\$ \$\int 0.5\$ \$\int 0.5\$ \$\int 0.6\$ \$\int 0.7\$ \$	on should not ering of the vi zer tiped easily should the should be should b	ewing area di ould be accept Acceptable A+B 7~10.1" Acceptable 5 0 be seen after udge by the l be seen only	table. Qty >10.1" Acceptable 6 0 cover asseminear defect or in non-operate following:	C Acceptable bling of 5.3.	Minor
5	MURA White/Black dot (MURA)	V	Using 3% ND filter, it's NG if it can be seen in R,G,B picture. $Visible \ under: \ ND3\%; \ D \leq 0.15mm, \ Acceptable; \\ 0.15mm < D \leq 0.5mm, \ N \leq 4; \ D > 0.5mm, \ Not \ allowable.$					Minor



10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects		
1	Difference in Spec.	Not allowable	Major		
2	Pattern peeling	No substrate pattern peeling and floating	Major		
		No soldering missing	Major		
3	Soldering defects	No soldering bridge	Major		
		No cold soldering	Minor		
4	Resist flaw on PCB	Visible copper foil (Φ 0.5 mm or more) on substrate pattern is not allowed	Minor		
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major		
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor		
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor		
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ0.2mm)	Minor		
9	Stain	No stain to spoil cosmetic badly	Minor		
10	Plate discoloring	No plate fading, rusting and discoloring	Minor		
	I. I and moute	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor		
	1. Lead parts	b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor		
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor		
11	3. Chips	Minor			
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor		
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor		
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.			