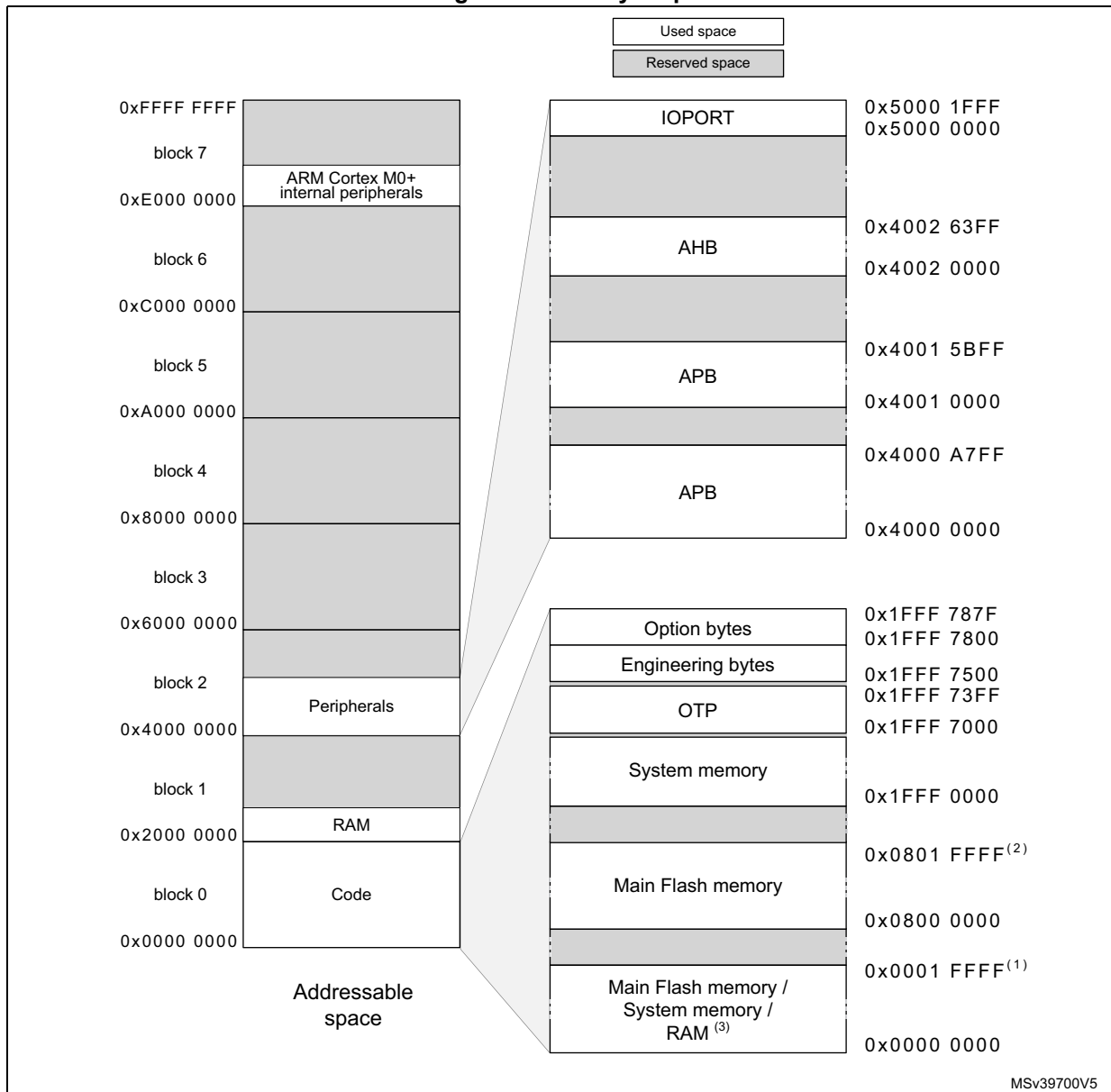


2.2.2 Memory map and register boundary addresses

Figure 2. Memory map



1. Applies to STM32G071xx and STM32G081xx. For STM32G031xx and STM32G041xx, the value is 0x0000 FFFF.
2. Applies to STM32G071xx and STM32G081xx. For STM32G031xx and STM32G041xx, the value is 0x0800 FFFF.
3. Depends on boot configuration

All the memory map areas that are not allocated to on-chip memories and peripherals are considered “Reserved”. For the detailed mapping of available memory and register areas, refer to the two following tables.