



GND

+1,2V FPGA  $V_{core}$

+1,75V ADC

+2,4V FPGA  $V_{IO}$

+3,3V RAM

+5,8V TTL

+5,8V

Gemessen  
gegen GND

FPGA Altera Cyclon II  $V_{CORE} = 1,15V < 1,20V > 1,25V$   $V_{IO} = 1,2V$  2,4V 3,2V  
 Ram 3,15V < 3,3V > 3,6V  
 ADC Max 1121  $A_{VCC} = 1,7V < 1,8V > 1,9V$