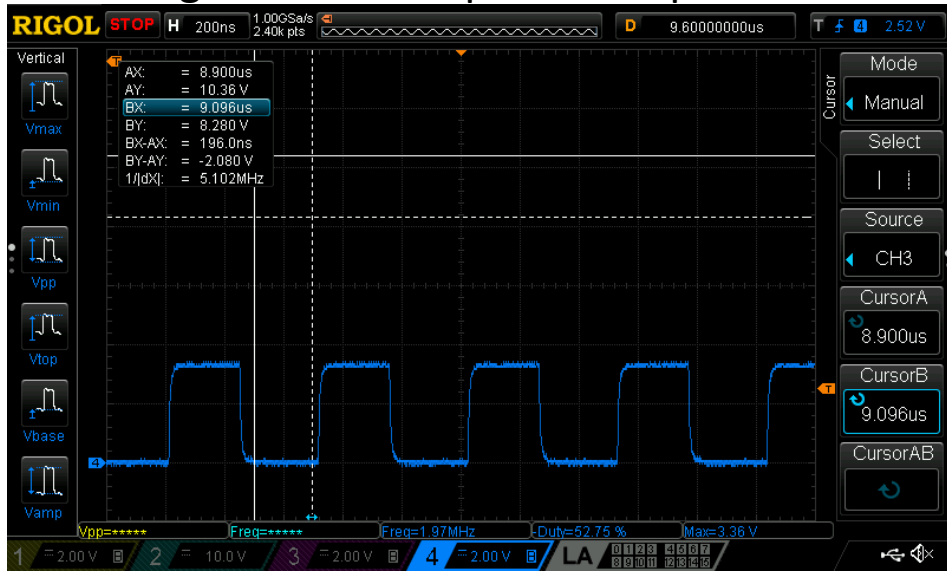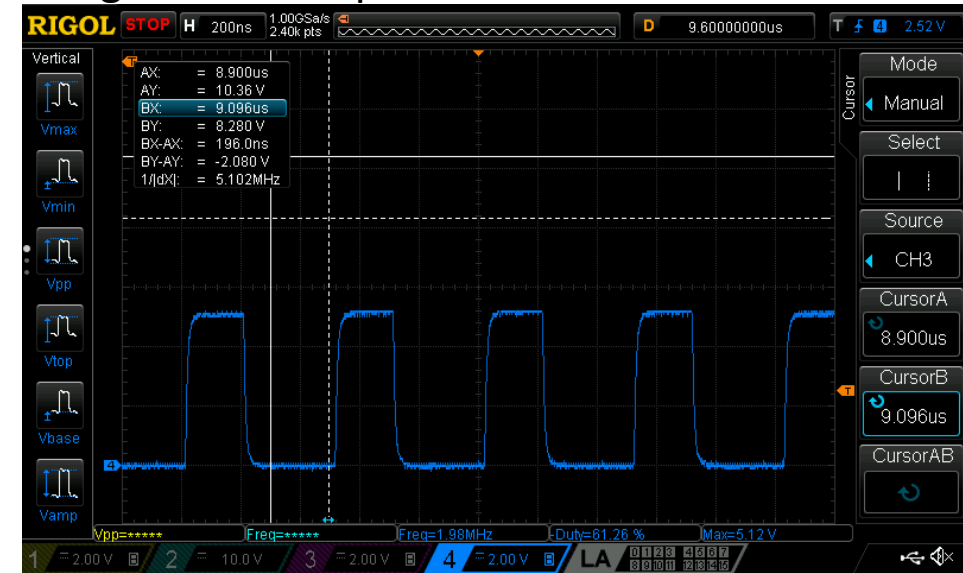# Clock Signal by 4 cascaded SPI-foils @2MHz

Signal at the output of the µCs
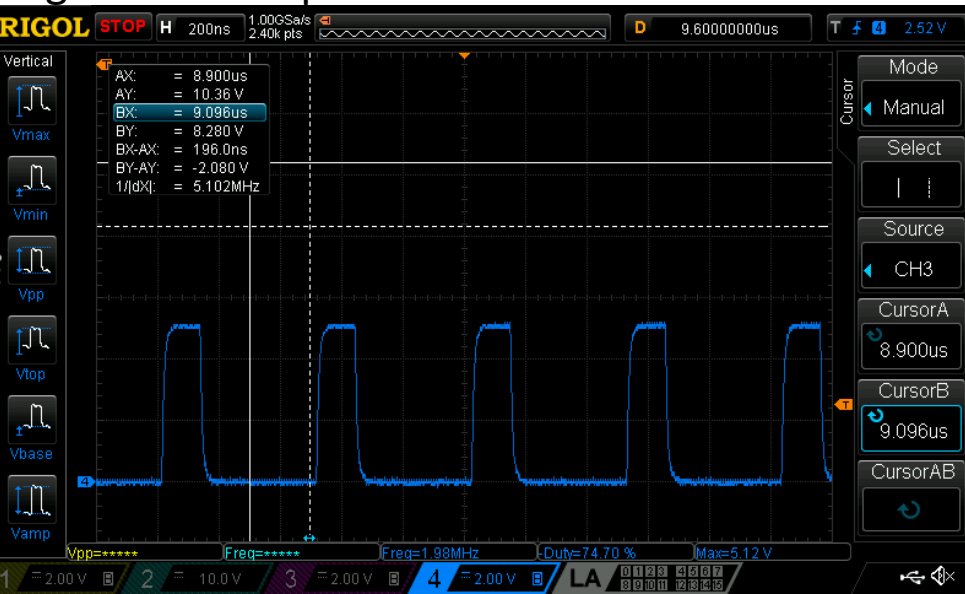
Signal at the input of second foil

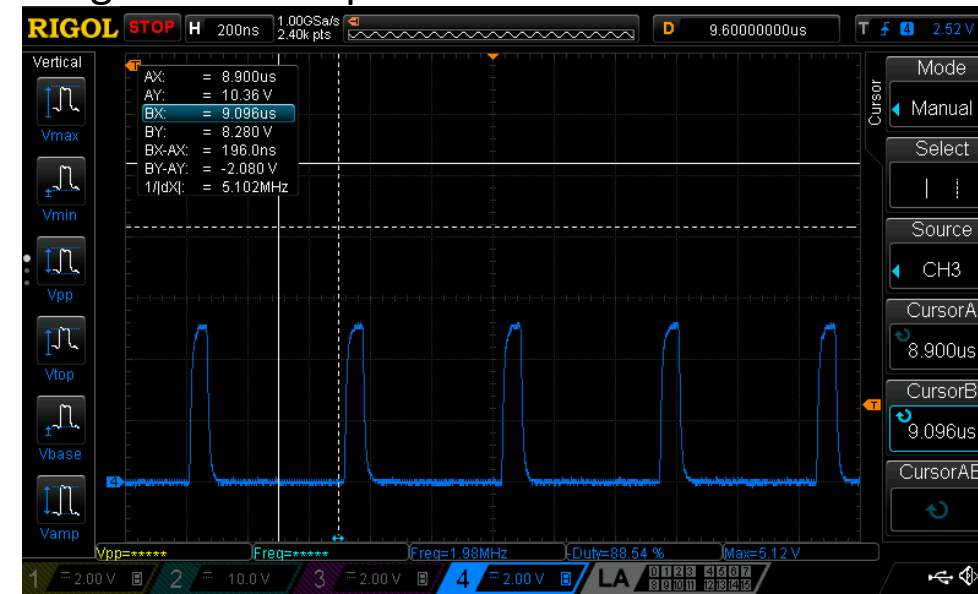

P.S. the fourth is working
Only at ¾ of the foil

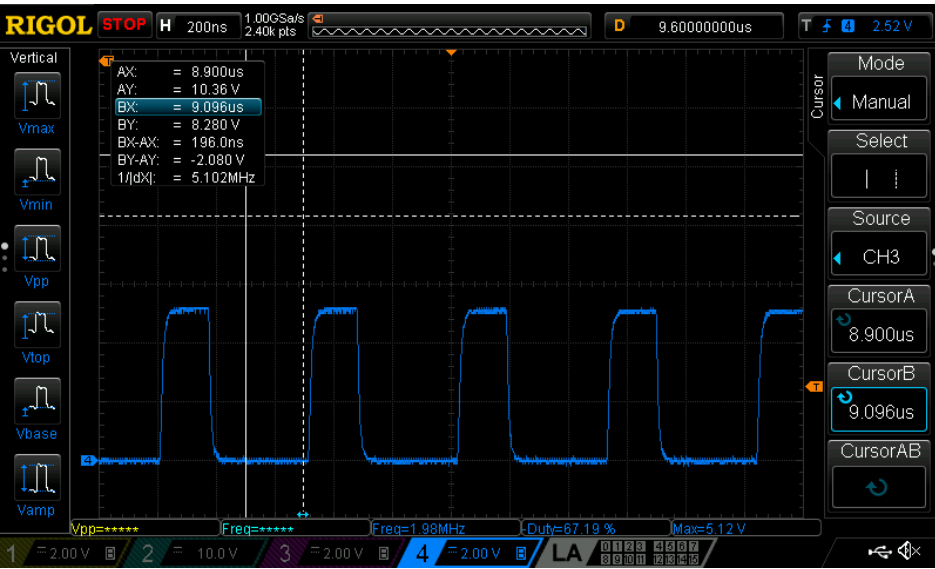## Signal at the input of third foil

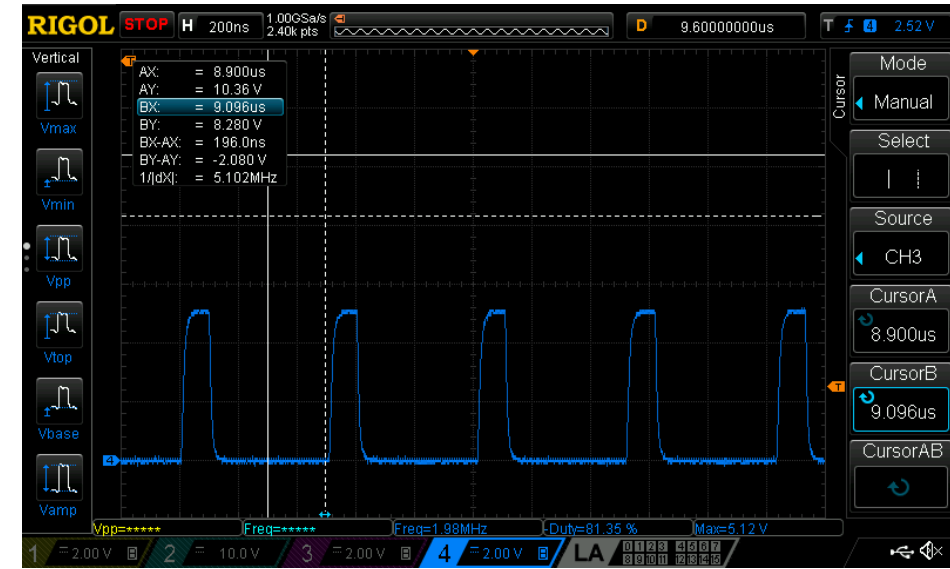## Signal at the input of 4 foils

Clock Signal by 4 cascaded SPI-foils @2MHz
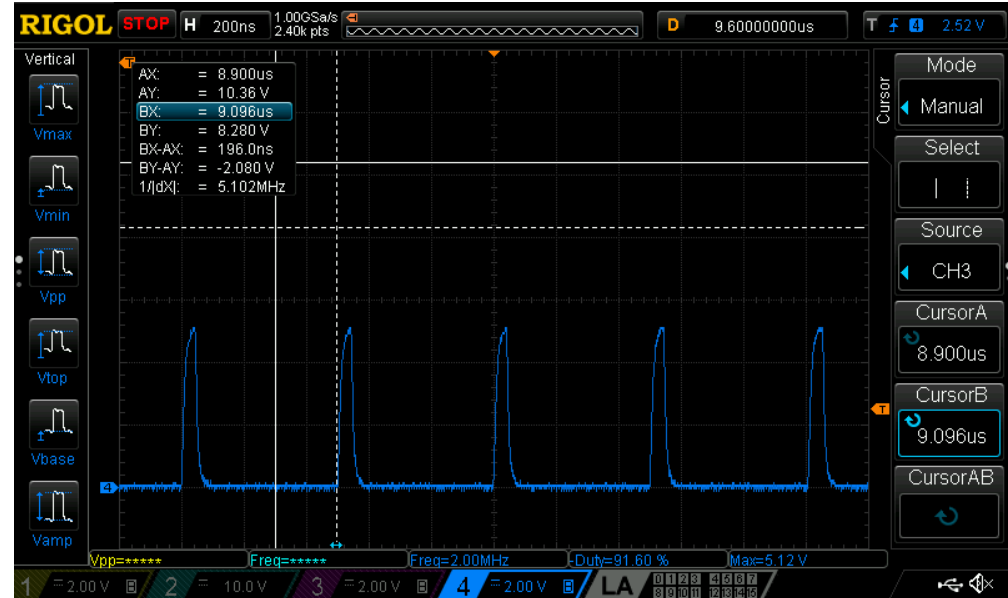
Signal in the middler of the second foil
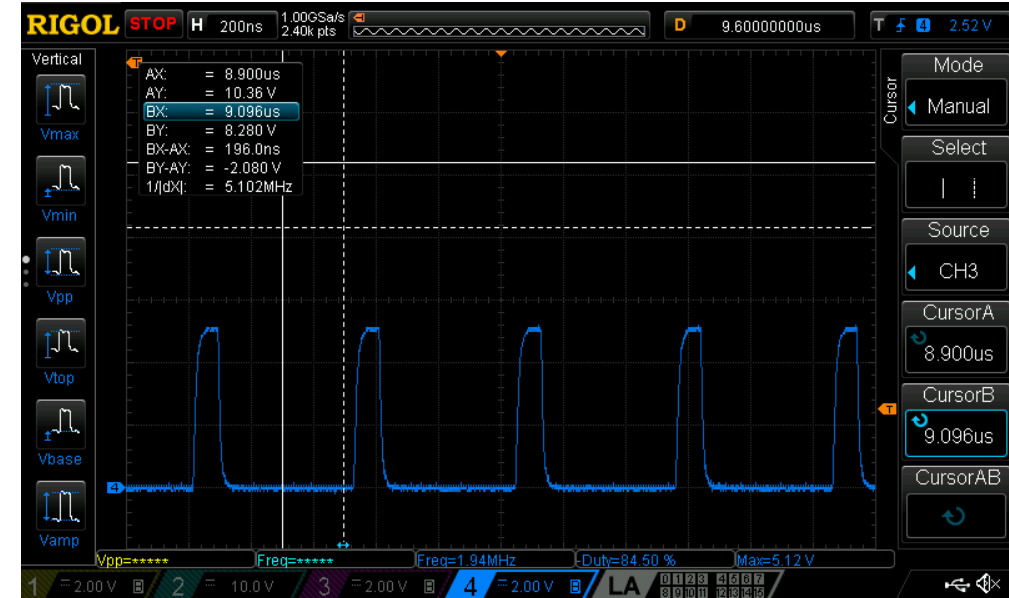
Signal in the middle of third foil

P.S. the duty cycle between Third Foils middle and the ¾ of the Third foil is 5%

⇒ High delay between LED Causing signal distortion As the number of LED increase
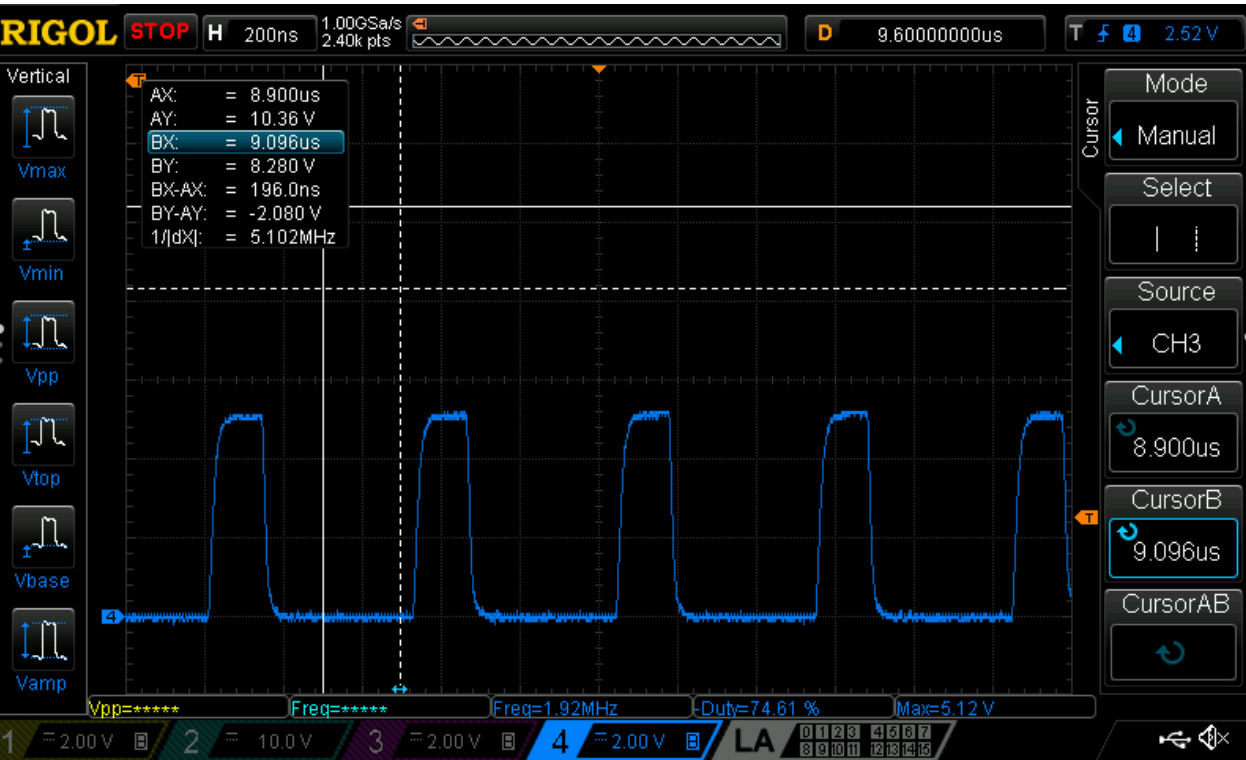
Signal in the middler of the fourth foil
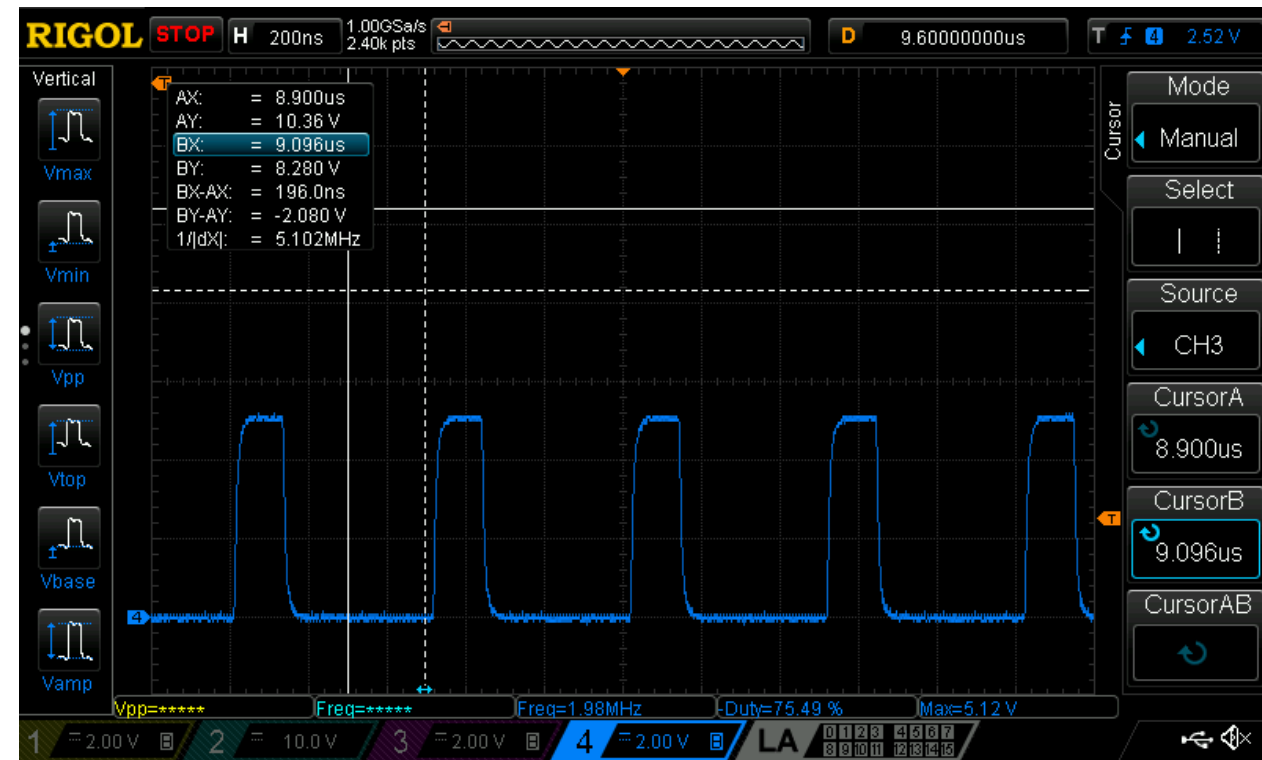
Signal at about ¾ of the third foils

Clock Signal by 4 cascaded SPI-foils @2MHz
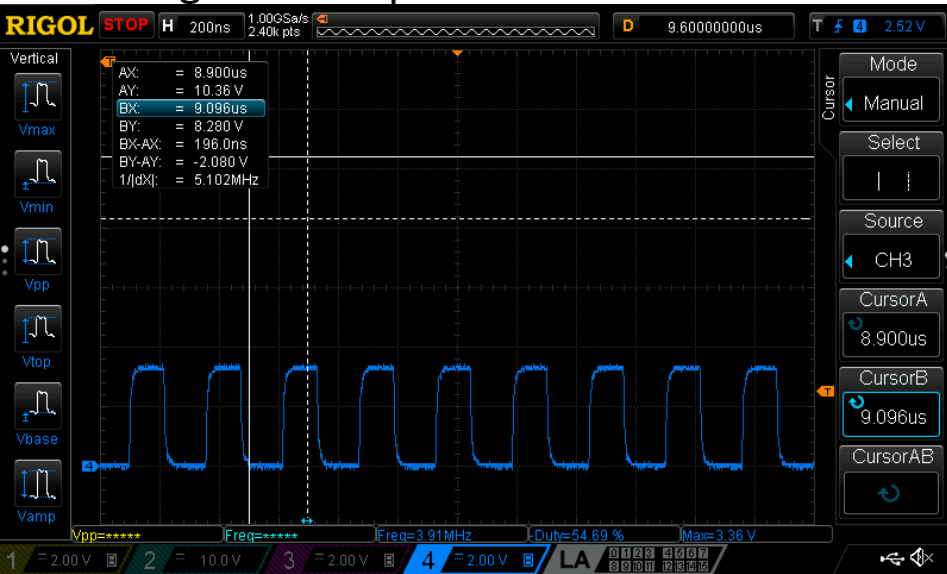
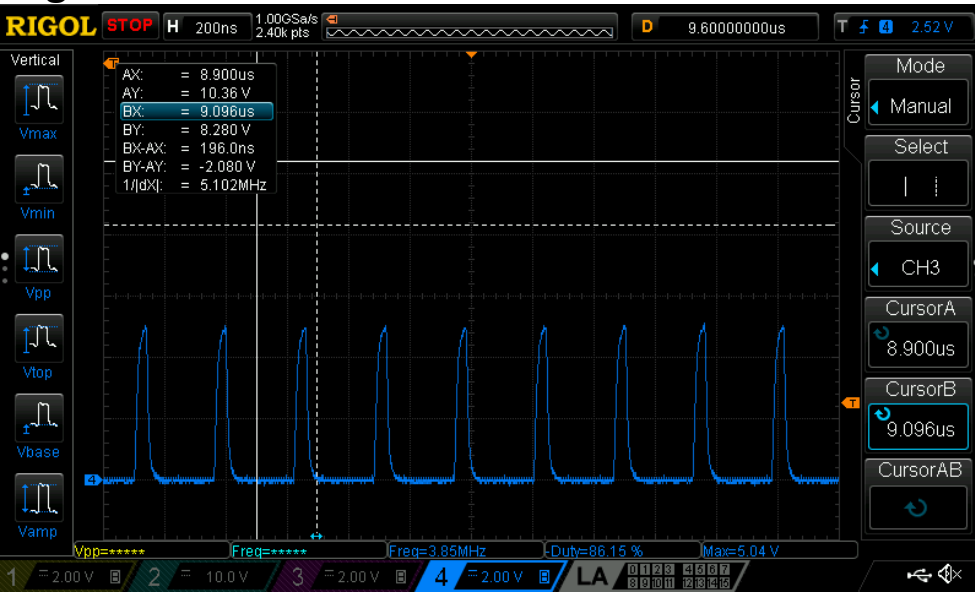Signal at output of second foil

Signal signal at input of third foil

=> No considerable delay between foils
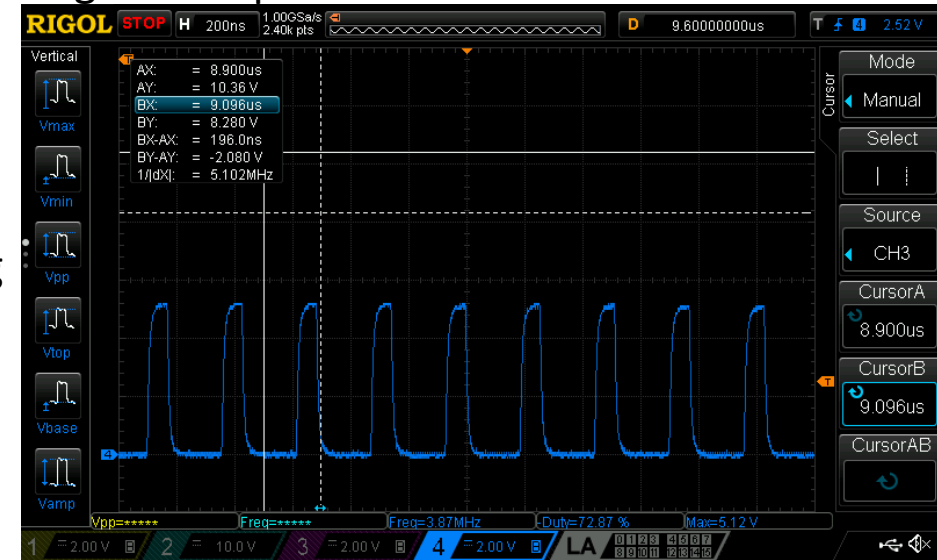
Clock Signal by 4 cascaded SPI-foils @4MHz

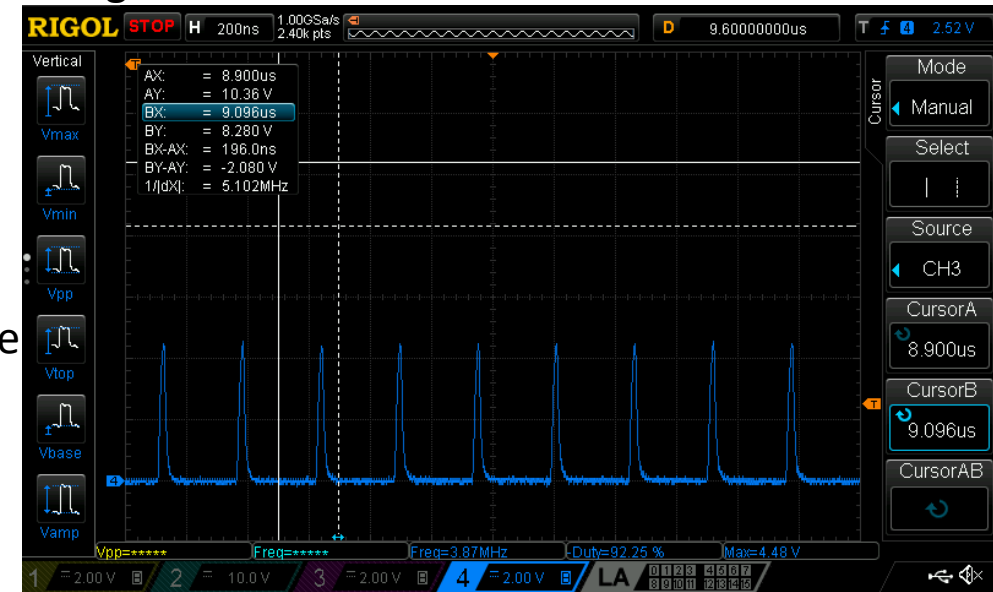## Signal at output of the microcontroller

Signal at input of the second foil

P.S. The second foil is working
At ¾ .

Signal distortion as the
Data rate increase.
⇒ The duty cycle keep on
Reducing.

Signal in the middler of the second foil

Signal at ¾ of the second foil

They are no further circuit
Between two LEDs.

The voltage quality is stable
And no high ripple