



PIC 15

### XI JP811 Terminal Function

Terminal Pin No.	NAME	INPUT & OUTPUT	Function Description
1	SP+	Output	SPK Output
2	MIC+	Input	MIC Input
3	BATT	Input	Input Electronic Source of Keypad backlight
4	5V	Input	LCD Drive IC & Input Electronic Source of LCD Backlight
5	GND		
6	GND		
7	GND		
8	LCD-CS	Output	IC始能端控制
9	(BATT-TEST) / LCD-RS	Output	Electronic Source Voltage Examination/ Read-Write End Control of LCD Drive IC
10	LAMP1	Output	Electronic Source Control of LCD Backlight
11	GND		
12	K1	Input	Keypad Matrix Input
13	K2	Input	Keypad Matrix Input
14	K3	Input	Keypad Matrix Input

15	K4	Input	Keypad Matrix Input
16	D1	Output	Keypad Matrix Output
17	D2	Output	Keypad Matrix Output
18	D3	Output	Keypad Matrix Output
19	D4	Output	Keypad Matrix Output
20	GND		

## XII Terminal Function of Microprocessor (IC811)

Pin NO.	Tube Feet Name	Function Description
1	PLL-LE/ROM-WP	Phase-locked loop Data End/ Stored IC Write Protection Board
2	LAMP1	LCD Backlight Control
3	5SC-UHF	Control by (UHF) VCO Electrical Source
4	K1	Row Line 1 on K1 Keypad
5	K2	Row Line 2 on K2 Keypad
6	K3	Row Line 3 on K3 Keypad
7	K4	Row Line 4 on K4 Keypad
8	D1/PLL-CLK/ROM-CLK/MONI/LCD-D0	Line 1 on Keypad/Phase-locked Loop Clock/ Squelch Key/LCD Drive Data
9	D2/PLL-DATA/PF1/LCD-D1	Row Line 2 on Keypad/Phase-locked Loop Data/ Sidekey1/LCD Drive Data
10	D3/Viose-CLK/ (A/B) /LCD-D2	Row Line 3 on keypad/ Voice Clock/(A/B) Button/ LCD Drive Data
11	D4/PF2/LCD-D3	Row Line 4 on keypad/Topkey PF2/ LCD Drive Data
12	CN2	CN2 Channel Decoding Bit
13	CN1	CN1 Channel Decoding Bit
14	(N/W) /ROM-Data	Wide/Narrow Bandwidth Control/Packet Data
15	AVDD	
16	RXO	Microprocessor Audio Amplifier Output
17	RXI	Microprocessor Audio Amplifier Input
18	MICI	Microprocessor Audio Amplifier Output
19	MICO	Microprocessor Audio Amplifier Input
20	TONE/DTMF/Beep	Audio Signal Output/Double Audio Signal Output/Prompt Output
21	PLLC	
22	AVSS	
23	CTCSS/DCS/TUNE	CTCSS/DCS/Receiving Auto Track Gain Output
24	APC	Auto Power Control Voltage Output
25	AURX	Input End of Microprocessor Audio Channel
26	Batt-TEST/LCD-RS	Battery Voltage Sampling Input/LCD Drive IC Control End
27	VOX	MIC Sampling Data Input
28	ANSQL	Squelch Voltage Sample Input End
29	XIN	
30	XOUT	
31	VSS	
32	RESET	