

# Specification for Approval

**PRODUCT NAME:** RGC24128064WW003  
**PRODUCT NO.:** 9OL9919706000

<b>CUSTOMER</b>
<b>APPROVED BY</b>
<b>DATE:</b>

<b>RITDISPLAY CORP. APPROVED</b>

## REVISION RECORD

REV.	REVISION DESCRIPTION	REV. DATE	REMARK
X01	INITIAL RELEASE	2012. 11. 01	
A01	<ul style="list-style-type: none"><li>■ Transfer from X version</li><li>■ Add the information of module weight</li><li>■ Add tape</li><li>■ Add the packing specification</li></ul>	2013. 01. 31	Page 5, 15 & 16

## CONTENTS

ITEM	PAGE
<b><u>1. SCOPE</u></b>	4
<b><u>2. WARRANTY</u></b>	4
<b><u>3. FEATURES</u></b>	4
<b><u>4. MECHANICAL DATA</u></b>	5
<b><u>5. MAXIMUM RATINGS</u></b>	6
<b><u>6. ELECTRICAL CHARACTERISTICS</u></b>	7
6.1 D.C ELECTRICAL CHARACTERISTICS	
6.2 ELECTRO-OPTICAL CHARACTERISTICS	
<b><u>7. INTERFACE</u></b>	9
7.1 FUNCTION BLOCK DIAGRAM	
7.2 PANEL LAYOUT DIAGRAM	
7.3 PIN ASSIGNMENTS	
7.4 GRAPHIC DISPLAY DATA RAM ADDRESS MAP	
7.5 INTERFACE TIMING CHART	
<b><u>8. APPLICATION CIRCUIT</u></b>	13
8.1 APPLICATION CIRCUIT	
8.2 COMMAND TABLE	
<b><u>9. RELIABILITY TEST CONDITIONS</u></b>	14
<b><u>10. EXTERNAL DIMENSION</u></b>	15
<b><u>11. PACKING SPECIFICATION</u></b>	16
<b><u>12. APPENDIXES</u></b>	17

## **1. SCOPE**

The purpose of this specification is to define the general provisions and quality requirements that apply to the supply of display cells manufactured by RiTdisplay. This document, together with the Module Assembly Drawing, is the highest-level specification for this product. It describes the product, identifies supporting documents and contains specifications, which are either not addressed, or are exceptions to the supporting documents.

## **2. WARRANTY**

RiTdisplay warrants that the products delivered pursuant to this specification (or order) will conform to the agreed specifications for twelve (12) months from the shipping date ("Warranty Period"). RiTdisplay is obligated to repair or replace the products which are found to be defective or inconsistent with the specifications during the Warranty Period without charge, on condition that the products are stored or used as the conditions specified in the specifications. Nevertheless, RiTdisplay is not obligated to repair or replace the products without charge if the defects or inconsistency are caused by the force majeure or the reckless behaviors of the customer.

After the Warranty Period, all repairs or replacements of the products are subject to charge.

## **3. FEATURES**

- Small molecular organic light emitting diode.
- Color : White
- Panel matrix : 128\*64
- Driver IC : SSD1303
- Excellent Quick response time : 10 $\mu$ s
- Extremely thin thickness for best mechanism design : 2.01 mm
- High contrast : 2000:1
- Wide viewing angle : 160°
- 8-bit 6800-series Parallel Interface, 8-bit 8080-series Parallel Interface, Serial Parallel Interface, I<sup>2</sup>C Interface.
- Strong environmental resistance
- Wide range of operating temperature : -40 to 70 °C
- Anti-glare polarizer.

#### 4. MECHANICAL DATA

NO	ITEM	SPECIFICATION	UNIT
1	Dot Matrix	128 (W) x 64 (H)	dot
2	Dot Size	0.4 (W) x 0.4 (H)	mm <sup>2</sup>
3	Dot Pitch	0.43 (W) x 0.43 (H)	mm <sup>2</sup>
4	Aperture Rate	86	%
5	Active Area	55.01 (W) x 27.49 (H)	mm <sup>2</sup>
6	Panel Size	60.5 (W) x 37 (H)	mm <sup>2</sup>
7*	Panel Thickness	1.82 ± 0.1	mm
8	Module Size	60.5 (W) x 92 (H) x 2.01 (D)	mm <sup>3</sup>
9	Diagonal A/A size	2.4	inch
10	Module Weight	9.6 ± 10%	gram

\* Panel thickness includes substrate glass, cover glass and UV glue thickness.

## 5. MAXIMUM RATINGS

ITEM	MIN	MAX	UNIT	Condition	Remark
Supply Voltage ( $V_{DD}$ )	-0.3	+3.5	V	Ta = 25 °C	IC maximum rating
Supply Voltage ( $V_{CC}$ )	8	16	V	Ta = 25 °C	IC maximum rating
Operating Temp.	-40	70	°C		
Storage Temp	-40	85	°C		
Humidity		85	%		
Life Time	18,000	-	Hrs	60 cd/m <sup>2</sup> , 50% checkerboard	Note (1)
Life Time	21,000	-	Hrs	50 cd/m <sup>2</sup> , 50% checkerboard	Note (2)
Life Time	26,000	-	Hrs	40 cd/m <sup>2</sup> , 50% checkerboard	Note (3)

Note:

(A) Under VCC = 12V, Ta = 25 °C, 50% RH.

(B) Life time is defined the amount of time when the luminance has decayed to less than 50% of the initial measured luminance.

(1) Setting of 60 cd/m<sup>2</sup> :

- Contrast setting : 0x98
- Frame rate : 105Hz
- Duty setting : 1/64

(2) Setting of 50 cd/m<sup>2</sup> :

- Contrast setting : 0x75
- Frame rate : 105Hz
- Duty setting : 1/64

(3) Setting of 40 cd/m<sup>2</sup> :

- Contrast setting : 0x50
- Frame rate : 105Hz
- Duty setting : 1/64

## 6. ELECTRICAL CHARACTERISTICS

### 6.1 D.C ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETERS	TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{CC}$	Analog power supply (for OLED panel)	$T_a = -20\text{ }^{\circ}\text{C}$ to $+70\text{ }^{\circ}\text{C}$	11.5	12	12.5	V
$V_{DD}$	Digital power supply	$T_a = -20\text{ }^{\circ}\text{C}$ to $+70\text{ }^{\circ}\text{C}$	2.4	-	3.5	V
$I_{DD}$	Operating current for $V_{DD}$ $V_{DD} = 2.7\text{V}$ , $V_{CC} = 12\text{V}$ , $I_{REF} = 10\mu\text{A}$ No loading, All Display ON	Contrast=FF	-	190	300	$\mu\text{A}$
$I_{CC}$	Operating current for $V_{CC}$ $V_{DD} = 2.7\text{V}$ , $V_{CC} = 12\text{V}$ , $I_{REF} = 10\mu\text{A}$ All Display ON	Contrast=FF	-	550	1000	$\mu\text{A}$
$V_{IH}$	High logic input level		$0.8^* V_{DD}$	-	$V_{DD}$	V
$V_{IL}$	Low logic input level		0	-	$0.2^* V_{DD}$	V
$V_{OH}$	High logic output level		$0.9^* V_{DD}$	-	$V_{DD}$	V
$V_{OL}$	Low logic output level		0	-	$0.1^* V_{DD}$	V
$I_{SEG}$	Segment on output current $V_{DD} = 2.7\text{V}$ , $V_{CC} = 12\text{V}$ , $I_{REF} = 10\mu\text{A}$ , Display on, Segment pin under test is connected with a 20K resistive load to $V_{SS}$	Contrast=FF	285	320	355	$\mu\text{A}$
		Contrast=AF	-	220	-	$\mu\text{A}$
		Contrast=5F	-	120	-	$\mu\text{A}$
		Contrast=0F	-	20	-	$\mu\text{A}$

## 6.2 ELECTRO-OPTICAL CHARACTERISTICS

### PANEL ELECTRICAL SPECIFICATIONS

PARAMETER	MIN	TYP.	MAX	UNITS	COMMENTS
Normal mode current		24	26	mA	All pixels on (1)
Standby mode current		3	4	mA	Standby mode 10% pixels on (2)
Normal mode power consumption		288	312	mW	All pixels on (1)
Standby mode power consumption		36	48	mW	Standby mode 10% pixels on (2)
Normal mode Luminance	40	50		cd/m <sup>2</sup>	Display Average
Standby mode Luminance		20		cd/m <sup>2</sup>	
CIE <sub>x</sub> (White)	0.24	0.28	0.32		x, y (CIE 1931)
CIE <sub>y</sub> (White)	0.28	0.32	0.36		
Dark Room Contrast	2000:1				
Viewing Angle	160			degree	
Response Time		10		μs	

(1) Normal mode condition :

- Driving Voltage : 12V
- Contrast setting : 0x75
- Frame rate : 105Hz
- Duty setting : 1/64

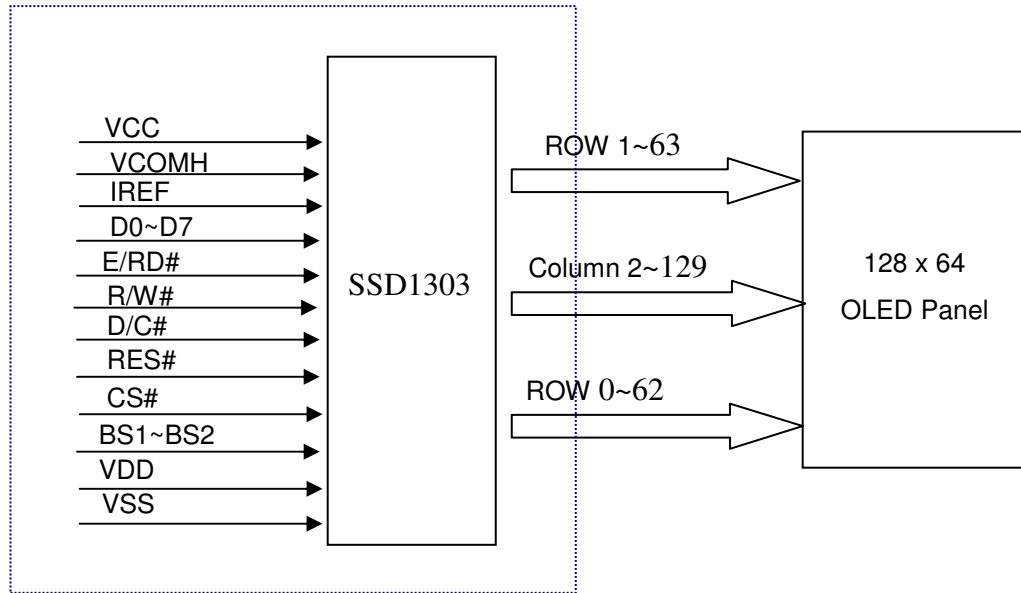
(2) Standby mode condition :

- Driving Voltage : 12V
- Contrast setting : 0x05
- Frame rate : 105Hz
- Duty setting : 1/64

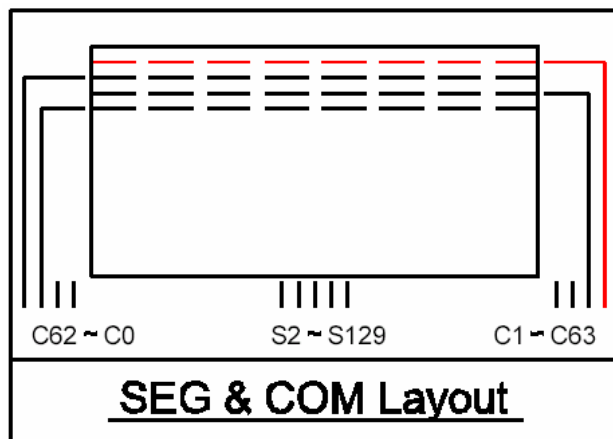


## 7. INTERFACE

### 7.1 FUNCTION BLOCK DIAGRAM



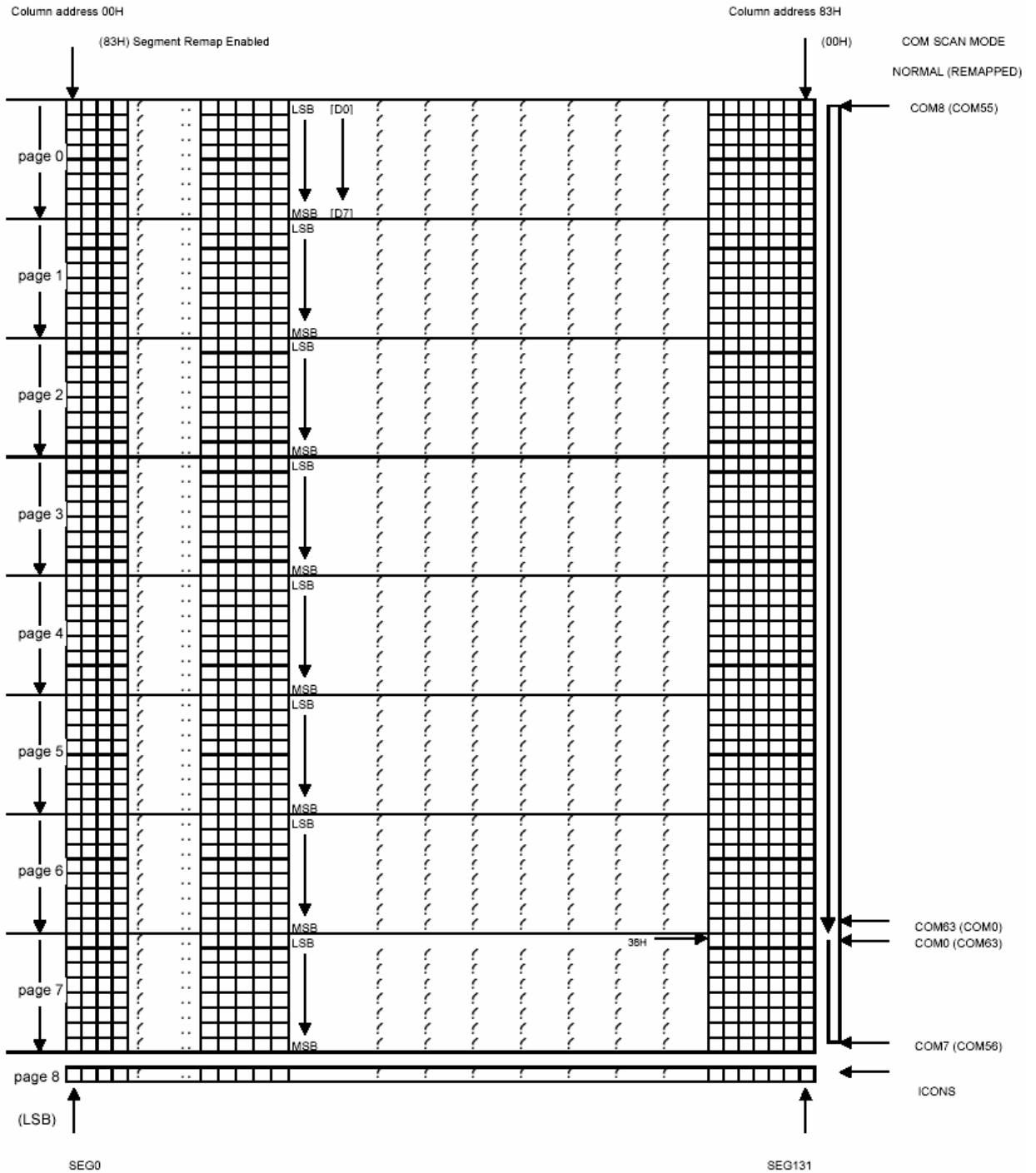
### 7.2 PANEL LAYOUT DIAGRAM



### 7.3 PIN ASSIGNMENTS

Pin No.	Pin Name	Description
1	VCC	Power supply for panel driving voltage.
2	VCOMH	The COM voltage reference pin, This pin should be connected to ground through a capacitor.
3	IREF	The current reference input pin. This pin should be connected to ground through a resistor.
4	D7	These pins are bi-directional data bus connecting to the MCU data bus.
5	D6	
6	D5	When serial interface mode is selected, D0 will be the serial clock input: SCL; D1 will be the serial data input: SDA and D2 should be kept NC.
7	D4	
8	D3	
9	D2	
10	D1	When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDA in application and D0 is the serial clock input, SCL
11	D0	
12	E/RD#	Data read operation is initiated when it's pull low.
13	R/W#	Data write operation is initiated when it's pull low.
14	D/C#	Data/Command control pin. Pull high for write/read display data. Pull low for write command or read status.
15	RES#	Reset signal input.
16	CS#	Chip select input.
17	BS2	Interface select pin.
18	BS1	Interface select pin.
19	VDD	Power supply pin for core logic operation.
20	NC	No connection.
21	VSS	This is ground pin.
22	VSS	This is ground pin.

### 7.4 GRAPHIC DISPLAY DATA RAM ADDRESS MAP

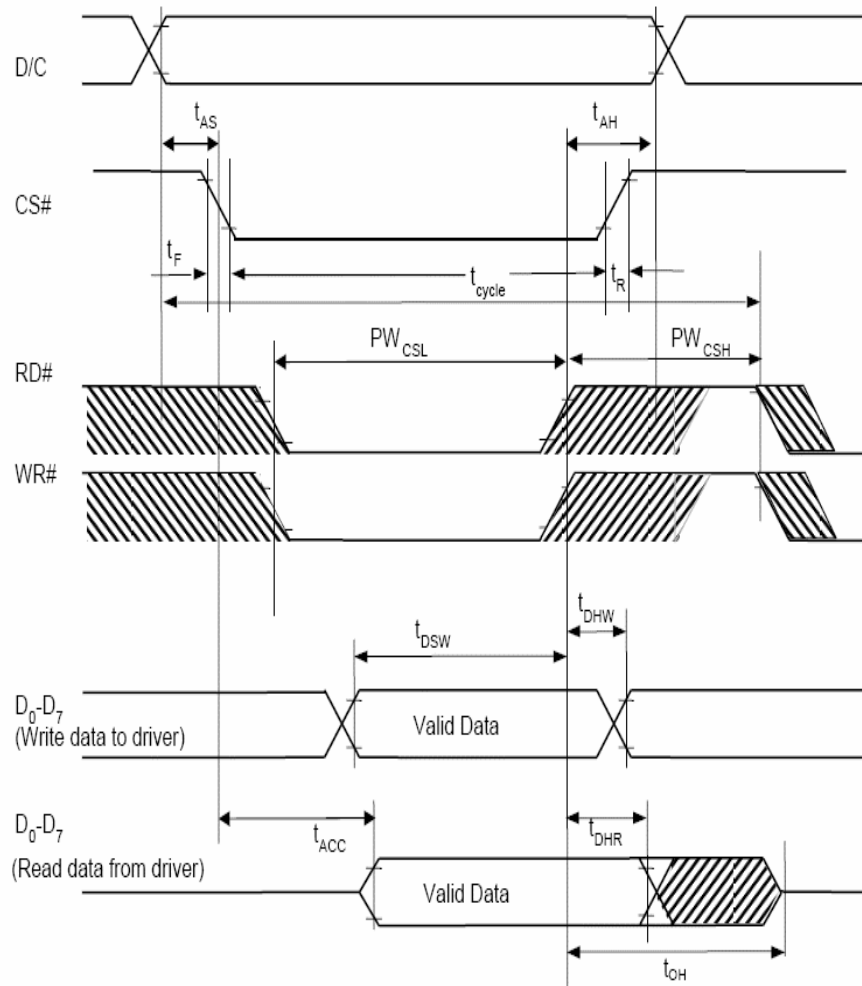


## 7.5 INTERFACE TIMING CHART

### 8080-Series MCU Parallel Interface Timing Characteristics

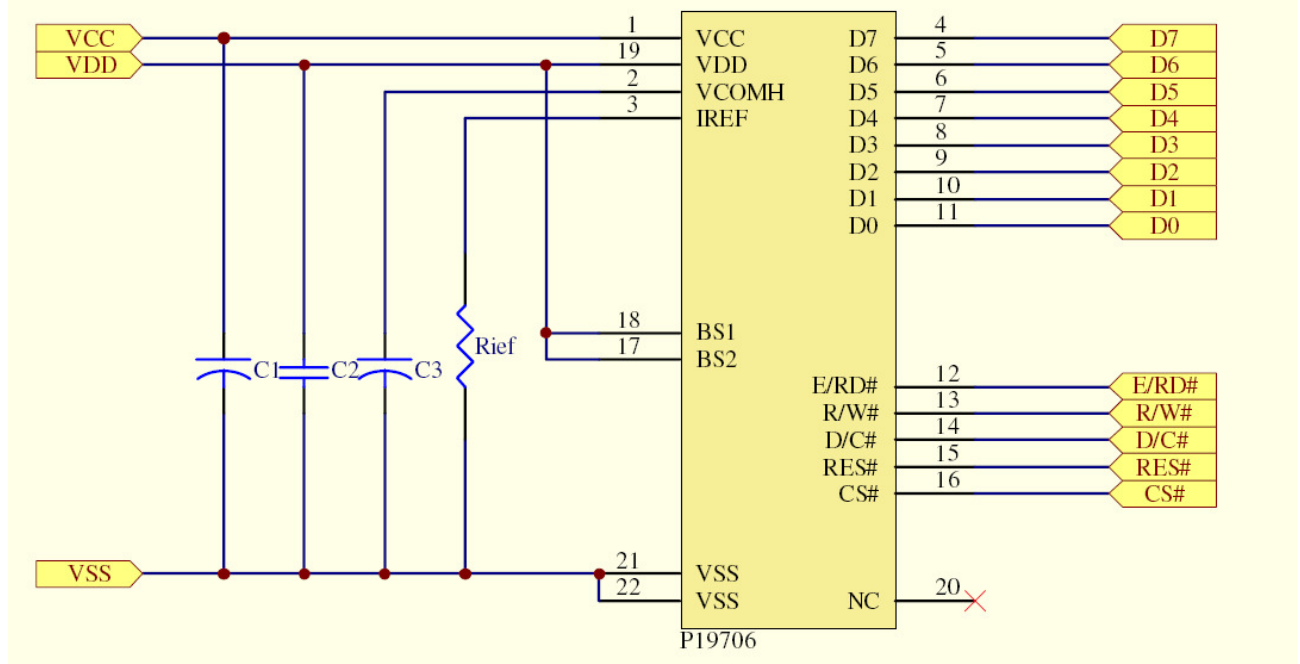
( $V_{DD} - V_{SS} = 2.4$  to  $3.5V$ ,  $T_A = 25^\circ C$ )

Symbol	Parameter	Min	Typ	Max	Unit
$t_{cycle}$	Clock Cycle Time	300	-	-	ns
$t_{AS}$	Address Setup Time	0	-	-	ns
$t_{AH}$	Address Hold Time	0	-	-	ns
$t_{DSW}$	Write Data Setup Time	40	-	-	ns
$t_{DHW}$	Write Data Hold Time	7	-	-	ns
$t_{DHR}$	Read Data Hold Time	20	-	-	ns
$t_{OH}$	Output Disable Time	-	-	70	ns
$t_{ACC}$	Access Time	-	-	140	ns
$PW_{CSL}$	Chip Select Low Pulse Width (read)	150	-	-	ns
	Chip Select Low Pulse Width (write)	60	-	-	ns
$PW_{CSH}$	Chip Select High Pulse Width (read)	60	-	-	ns
	Chip Select High Pulse Width (write)	60	-	-	ns
$t_R$	Rise Time	-	-	40	ns
$t_F$	Fall Time	-	-	40	ns



## 8. APPLICATION CIRCUIT

### 8.1 APPLICATION CIRCUIT



### Recommended components

Rief : 1M ohm /1%.(0603)

C1,C3 : 4.7uF/35V (Tantalum type) or VISHAY (572D475X0025A2T)

C2 : 4.7uF/16V(0603)

This circuit is designed for 8080 8-bits interface.

### 8.2 COMMAND TABLE

Refer to IC Spec.: SSD1303

## 9. RELIABILITY TEST CONDITIONS

No.	Items	Specification	Quantity
1	High temp. (Non-operation)	85 °C, 240hrs	5
2	High temp. (Operation)	70 °C, 120hrs	5
3	Low temp. (Operation)	-40 °C, 120hrs	5
4	High temp. / High humidity (Operation)	65 °C, 90%RH, 120hrs	5
5	Thermal shock (Non-operation)	-40 °C ~85 °C (-40 °C /30min; transit /3min; 85 °C /30min; transit /3min) 1cycle: 66min, 100 cycles	5
6	Vibration	Frequency : 5~50HZ, 0.5G Scan rate : 1 oct/min Time : 2 hrs/axis Test axis : X, Y, Z	1 Carton
7	Drop	Height: 120cm Sequence : 1 angle 、3 edges and 6 faces Cycles: 1	1 Carton
8	ESD (Non-operation)	Air discharge model, ±8kV, 10 times	5

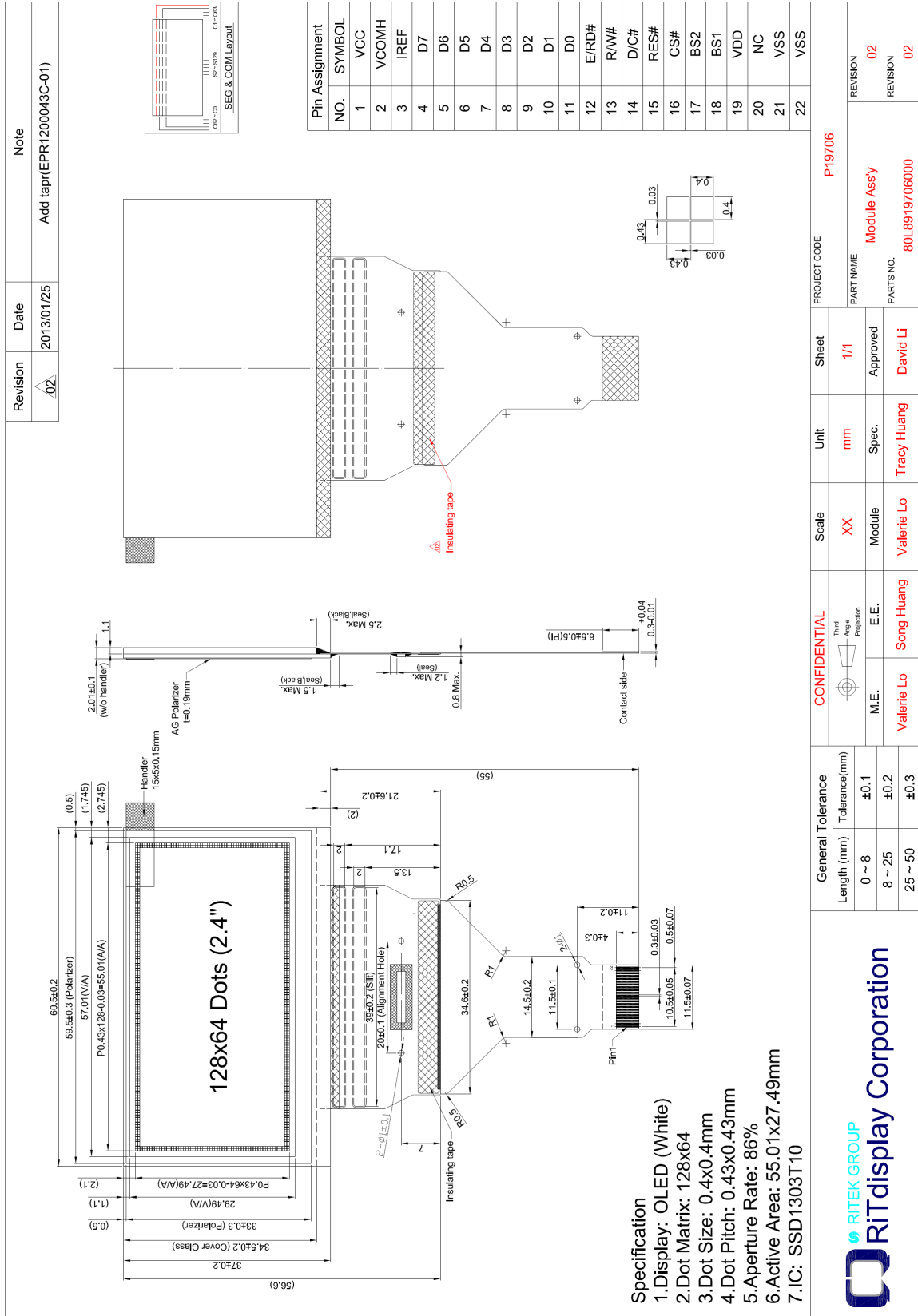
### Test and measurement conditions

1. All measurements shall not be started until the specimens attain to temperature stability.
2. All-pixels-on is used as operation test pattern.
3. The degradation of Polarizer are ignored for item 1, 4 & 5.

### Evaluation criteria

1. The function test is OK.
2. No observable defects.
3. Luminance: > 50% of initial value.
4. Current consumption: within  $\pm$  50% of initial value.

## 10. EXTERNAL DIMENSION



**11. PACKING SPECIFICATION**

Revision △01	Date 2013/01/25	Note Packing Tray Instruction
-----------------	--------------------	----------------------------------

80L9819706000  
Module Assy For P19706 x12 pcs

700P197061001  
Tray 330x270x8.5mm T=0.5mm, PET, P19706

3010000003  
5G Silica Gel Desiccants  
5G 乾燥劑

3003000012  
Vacuum Bag ONY/LDPE  
真空包裝袋  
480x285x90

3003000016  
Antistatic Bubble bag 440x(350+450)mm  
抗靜電氣泡袋

3001000005  
Pizza Box 345x285x88, B corrugated

3006000000  
Label

3208000125  
Tape

3006000000  
Label x1 pcs

3006000000  
Label x2 pcs

Vacuum packing : 4 sec  
抽真空 : 4 秒

Tray =21 pcs  
Tape  
3208000125

Rotate stack  
旋轉堆疊

Pizza Box 345x285x88, B corrugated

Label x2 pcs

ITEM	PART No.	DESC	QTY
	90L9919706000		1
1	80L9819706000	Module Assy For P19706	480
2	700P197061001	Tray 330x270x8.5 T:0.5mm PET P19706	42
3	3010000003	5G Silica Gel Desiccants	8
4	3003000012	Vacuum Bag ONY/LDPE 480x285x90	2
5	3003000016	Antistatic Bubble bag 440x(350+450)mm	2
6	3001000005	Pizza Box 345x285x88, B corrugated	2
7	3000000009	Carton 385x305x203mm	1
8	3006000000	Label	3
9	3208000125	Tape, W=48mm, L=910cm	

General Tolerance		Scale	Unit	Sheet	PROJECT CODE
Length (mm)	Tolerance(mm)	1:15	mm	1/1	P19706
0 ~ 8	±0.1	Module	Spec.	Approved	PART NAME
8 ~ 25	±0.2	Kelly Hsu	Irene Fan	Strong Tsai	Packing Tray Instruction
25 ~ 50	±0.3	Iven Lee	X		PARTS NO.
					90L9919706000
					VERSION
					01
					VERSION
					01

**RITEK GROUP**  
**RiTdisplay Corporation**



## **12. APPENDIXES**

### **APPENDIX 1: DEFINITIONS**

#### **A. DEFINITION OF CHROMATICITY COORDINATE**

The chromaticity coordinate is defined as the coordinate value on the CIE 1931 color chart for R, G, B, W.

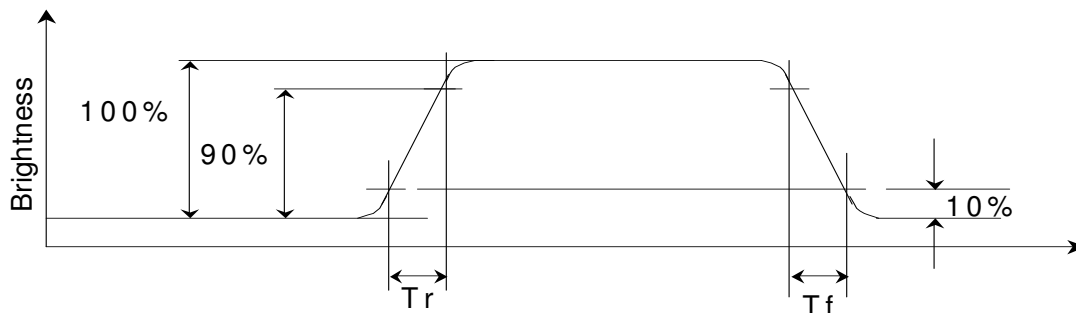
#### **B. DEFINITION OF CONTRAST RATIO**

The contrast ratio is defined as the following formula:

$$\text{Contrast Ratio} = \frac{\text{Luminance of all pixels on measurement}}{\text{Luminance of all pixels off measurement}}$$

#### **C. DEFINITION OF RESPONSE TIME**

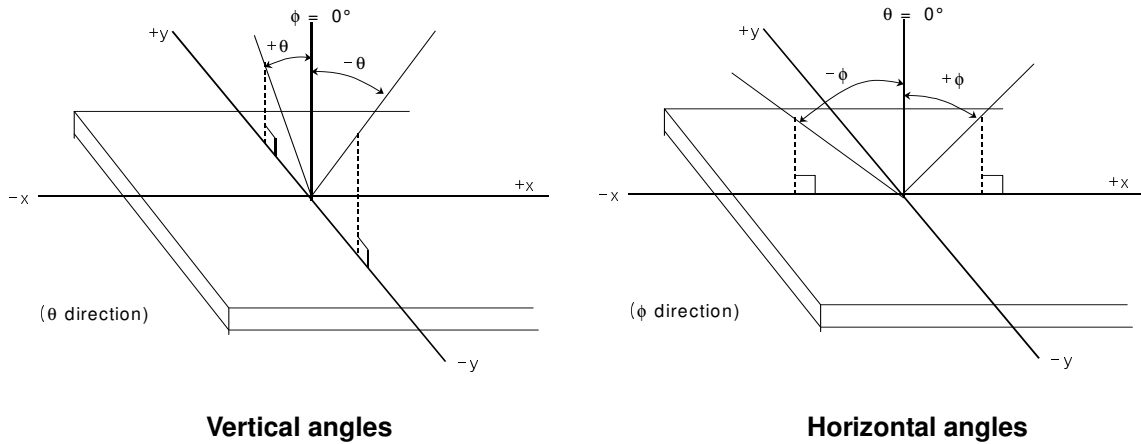
The definition of turn-on response time  $T_r$  is the time interval between a pixel reaching 10% of steady state luminance and 90% of steady state luminance. The definition of turn-off response time  $T_f$  is the time interval between a pixel reaching 90% of steady state luminance and 10% of steady state luminance. It is shown in Figure 2.



**Figure 2 Response time**

### D. DEFINITION OF VIEWING ANGLE

The viewing angle is defined as Figure 3. Horizontal and vertical (H & V) angles are determined for viewing directions where luminance varies by 50% of the perpendicular value.

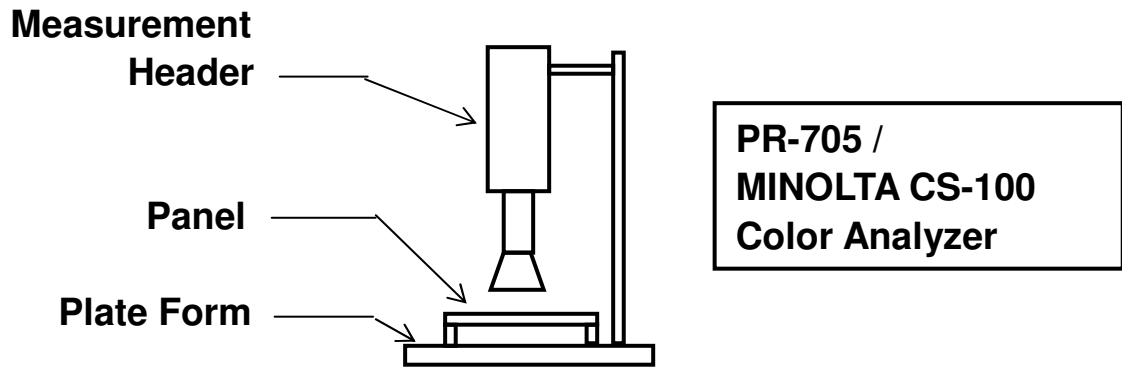


**Figure 3 Viewing Angle**

**APPENDIX 2: MEASUREMENT APPARATUS**

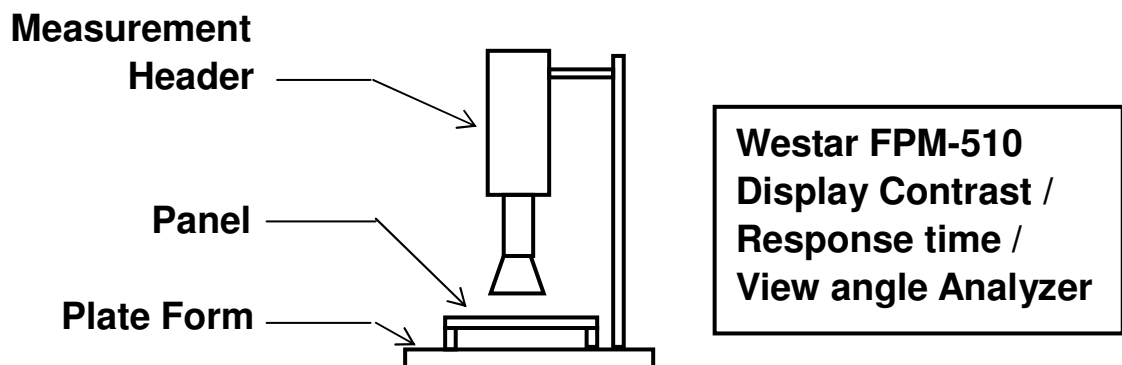
**A. LUMINANCE/COLOR COORDINATE**

PHOTO RESEARCH PR-705, MINOLTA CS-100

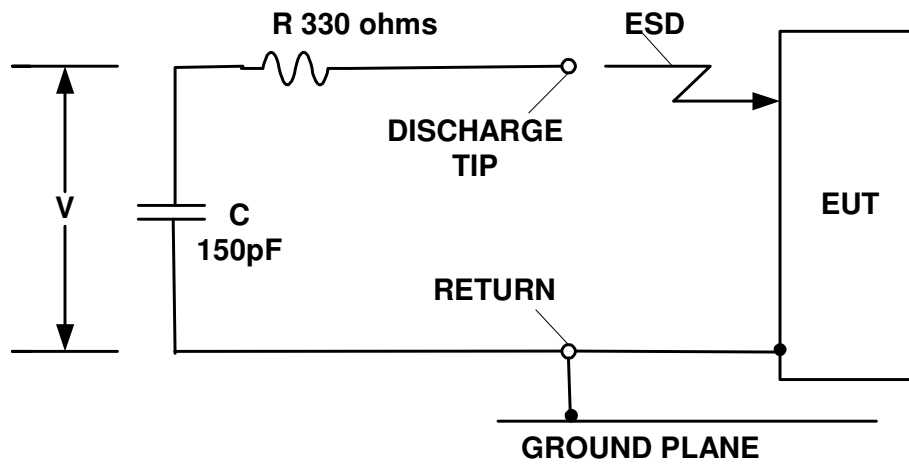


**B. CONTRAST / RESPONSE TIME / VIEWING ANGLE**

WESTAR CORPORATION FPM-510



**C. ESD ON AIR DISCHARGE MODE**



## APPENDIX 3: PRECAUTIONS

### A. RESIDUE IMAGE

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.