

# **I2C Bus Pullup Resistor Calculation**

Rajan Arora

# ABSTRACT

Pullup resistor calculation for I2C interface is a commonly asked question. In this application note we show how to use simple equations for this calculation.

#### Contents

Introduction	1
Pullup Resistor Calculation	2
Speed Versus Power Trade-off	4
Example	4
	Pullup Resistor Calculation Speed Versus Power Trade-off

### List of Figures

1	Application Example Showing I2C Communication Between the Different IC's on a System and With Pullup Resistors on I2C Bus	2
2	Minimum Pullup Resistance [ $R_P$ (min)] vs Pullup Reference Voltage ( $V_{CC}$ )	
3	Maximum Pullup Resistance [ $R_P$ (max)] vs Bus Capacitance ( $C_b$ )	3

## List of Tables

1 Parametrics from I2	C specifications	3
-----------------------	------------------	---

## 1 Introduction

I2C communication standard is the mostly widely used inter-chip communication standard in today's electronic systems. It is an open-drain/open-collector communication standard which implies integrated circuits (IC's) with different voltage supply rails can be connected for communication. Pullup resistors need to be connected from the I2C lines to the supply to enable communication as shown in Figure 1. The pullup resistors pull the line high when it is not driven low by the open-drain interface. The value of the pullup resistor is an important design consideration for I2C systems as an incorrect value can lead to signal loss. In this article we show the simple equations for the pullup resistor calculation which the system designer can use to do quick calculations for their design.

1



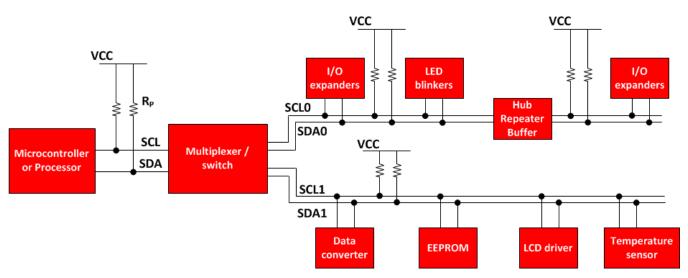


Figure 1. Application Example Showing I2C Communication Between the Different IC's on a System and With Pullup Resistors on I2C Bus

#### 2 **Pullup Resistor Calculation**

A strong pullup (small resistor) prevents the I2C pin on an IC from being able to drive low. The V<sub>OL</sub> level that can be read as a valid logical low by the input buffers of an IC determines the minimum pullup resistance [ $R_P(min)$ ].  $R_P(min)$  is a function of  $V_{CC}$ ,  $V_{OL}$  (max), and  $I_{OL}$ :

$$R_{P}(\min) = \frac{\left(V_{CC} - V_{OL}(\max)\right)}{I_{OL}}$$
(1)

The maximum pullup resistance is limited by the bus capacitance ( $C_b$ ) due to I2C standard rise time specifications. If the pullup resistor value is too high, the I2C line may not rise to a logical high before it is pulled low. The response of an RC circuit to a voltage step of amplitude V<sub>cc</sub>, starting at time t = 0 is characterized by time constant RC. The voltage waveform can be written as:

$$V(t) = V_{CC} \times \left(1 - e^{\frac{-t}{RC}}\right)$$
(2)

For  $V_{IH} = 0.7 \times V_{CC}$ :

$$V_{IH} = 0.7 \times V_{CC} = V_{CC} \times \left(1 - e^{\frac{-t1}{R_P \times C_b}}\right)$$
(3)

For  $V_{\parallel} = 0.3 \times V_{CC}$ :

$$V_{IL} = 0.3 \times V_{CC} = V_{CC} \times \left(1 - e^{\frac{-t2}{R_{P} \times C_{b}}}\right)$$
(4)

The rise time for the I2C bus can be written as:

$$t_{r} = t_{2} - t_{1} = 0.8473 \times R_{p} \times C_{b}$$
(5)

The maximum pullup resistance is a function of the maximum rise time (t<sub>r</sub>):

$$\mathsf{R}_{\mathsf{p}}(\mathsf{max}) = \frac{\mathsf{r}_{\mathsf{r}}}{\left(0.8473 \times \mathsf{C}_{\mathsf{b}}\right)}$$

Copyright © 2015, Texas Instruments Incorporated

where parametrics from I2C specifications are listed in Table 1.

(6)

www.ti.com

	Parameter	Standard Mode (Max)	Fast Mode (Max)	Fast Mode Plus (Max)	Unit
t,	Rise time of both SDA and SCL signals	1000	300	120	ns
C <sub>b</sub>	Capacitive load for each bus line	400	400	550	pF
V <sub>OL</sub>	Low-level output voltage (at 3 mA current sink, $V_{CC} > 2 V$ )	0.4	0.4	0.4	V
	Low-level output voltage (at 2 mA current sink, $V_{CC} \le 2 V$ )	-	$0.2 \times V_{CC}$	$0.2 \times V_{CC}$	V

 Table 1. Parametrics from I2C specifications

The R<sub>P</sub> (min) is plotted as a function of V<sub>CC</sub> in Figure 2. The R<sub>P</sub> (max) is plotted as a function of C<sub>b</sub> in Figure 3 for standard-mode and fast-mode I2C.

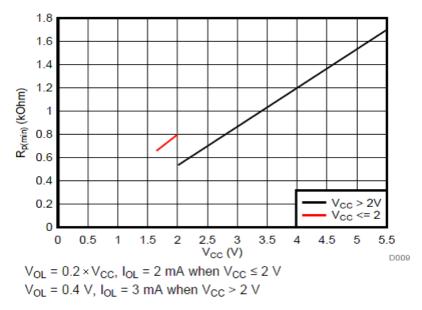


Figure 2. Minimum Pullup Resistance [R<sub>P</sub> (min)] vs Pullup Reference Voltage (V<sub>cc</sub>)

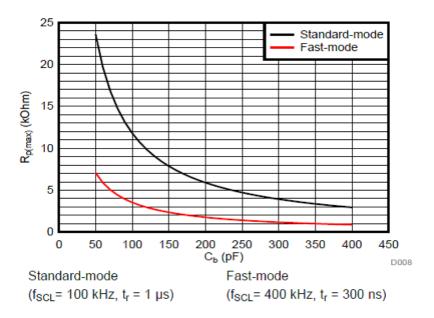


Figure 3. Maximum Pullup Resistance  $[R_P (max)]$  vs Bus Capacitance  $(C_b)$ 

3



www.ti.com

(8)

Speed Versus Power Trade-off

#### 3 Speed Versus Power Trade-off

Once the minimum and maximum value of the pullup resistor has been selected, the decision for the value of resistor can be made based on trade-off between the speed and power budget. A smaller resistor will give a higher speed because of smaller RC delay, and a larger resistor will give lower power consumption.

#### 4 Example

For Fast-mode I2C communication with the following parameters, calculate the pullup resistor value.

 $C_{b} = 200 \text{ pF}, V_{CC} = 3.3 \text{ V}$ 

Solution:

Taking the values from Table 1:

$$R_{P}(max) = \frac{t_{r}}{(0.8473 \times C_{b})} = \frac{(300 \times 10^{-9})}{(0.8473 \times 200 \times 10^{-12})} = 1.77 \text{ k}\Omega$$

$$R_{P}(min) = \frac{V_{CC} - V_{OL}(max)}{l_{OL}} = \frac{(3.3 - 0.4)}{(3 \times 10^{-3})} = 966.667 \Omega$$
(8)

Therefore, we can select any available resistor value between 966.667  $\Omega$  and 1.77 k $\Omega$ . The value of the pullup resistor can be selected based on the trade-off for the power consumption and speed.

4

### **IMPORTANT NOTICE**

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products		Applications	
Audio	www.ti.com/audio	Automotive and Transportation	www.ti.com/automotive
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial
Interface	interface.ti.com	Medical	www.ti.com/medical
Logic	logic.ti.com	Security	www.ti.com/security
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video
RFID	www.ti-rfid.com		
OMAP Applications Processors	www.ti.com/omap	TI E2E Community	e2e.ti.com
Wireless Connectivity	www.ti.com/wirelessconne	ctivity	

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2015, Texas Instruments Incorporated