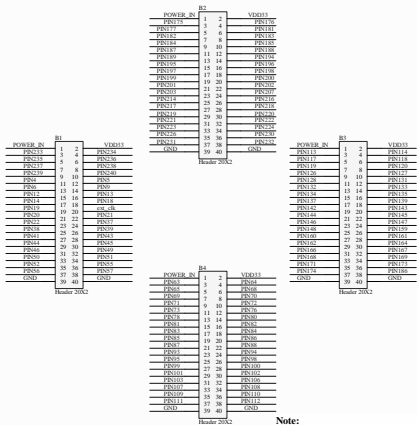
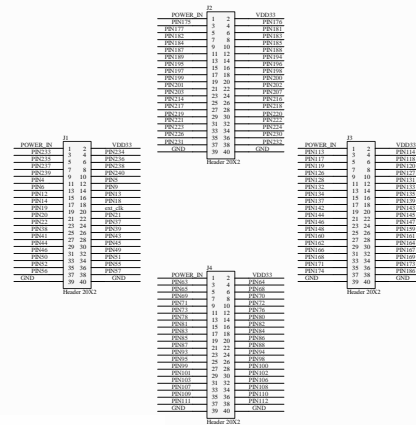


IO PIN HEADER



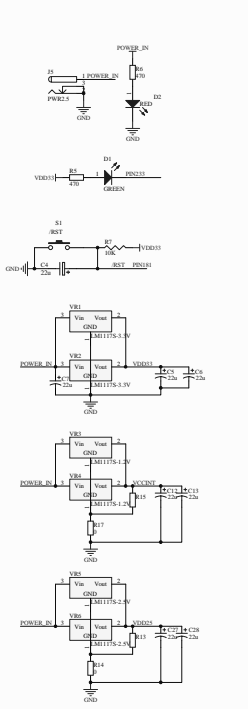
IO PIN HEADER



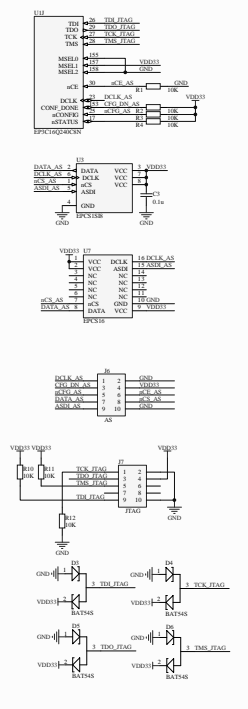
Note: CycloneIII serial FPGA EP3C16Q240 is pin to pin compatible with EP3C25Q240. The difference between EP3C16Q240 and EP3C25Q240 is listed in the following pins:

Pins	EP3C16Q240	EP3C25Q240
19	IO	Power Pin
20	IO	Power Pin
85	IO	Power Pin
86	IO	Power Pin
101	IO	Power Pin
102	IO	Power Pin
174	IO	Power Pin
175	IO	Power Pin
198	IO	Power Pin
199	IO	Power Pin
220	IO	Power Pin
222	IO	Power Pin

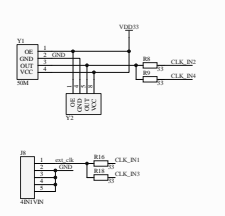
POWER



FPGA CONFIGURATION



FPGA CLOCK SOURCE



SDRAM AND SRAM

