

SKT 1200/18 E



Capsule Thyristor

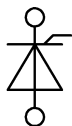
Line Thyristor SKT 1200/18 E

Features

- Hermetic metal case with ceramic insulator
- Capsule package for double sided cooling
- Shallow design with single sided cooling
- International standard case
- Off-state and reverse voltages up to 1800 V
- Amplifying gate

Typical Applications*

- DC motor control (e. g. for machine tools)
- Controlled rectifiers (e. g. for battery charging)
- AC controllers (e. g. for temperature control)
- Recommended snubber network e. g. for $V_{VRMS} \leq 400$ V: $R = 33 \Omega / 32$ W, $C = 1 \mu F$



SKT

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
Chip				
I _{T(AV)}	sinus 180°	T _c = 85 °C	1200	A
		T _c = 100 °C	840	A
I _{TSM}	10 ms	T _j = 25 °C	30000	A
		T _j = 125 °C	25500	A
i ² t	10 ms	T _j = 25 °C	4500000	A ² s
		T _j = 125 °C	3251250	A ² s
V _{RSM}			1800	V
V _{RRM}			1800	V
V _{DRM}			1800	V
(di/dt) _{cr}	T _j = 125 °C		125	A/μs
(dv/dt) _{cr}	T _j = 125 °C		1000	V/μs
T _j			-40 ... +125	°C
Module				
T _{stg}			-40 ... +130	°C

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Chip						
V _T	T _j = 25 °C, I _T = 3600 A				1.65	V
V _{T(TO)}	T _j = 125 °C				0.95	V
r _T	T _j = 125 °C				0.18	mΩ
I _{DD} ;I _{RD}	T _j = 125 °C, V _{DD} = V _{DRM} ; V _{RD} = V _{RRM}				160	mA
t _{gd}	T _j = 25 °C, I _G = 1 A, di _G /dt = 1 A/μs			1		μs
t _{gr}	V _D = 0.67 * V _{DRM}			2		μs
t _q	T _j = 125 °C		100		250	μs
I _H	T _j = 25 °C			250	500	mA
I _L	T _j = 25 °C, R _G = 33 Ω			500	2000	mA
V _{GT}	T _j = 25 °C, d.c.		3			V
I _{GT}	T _j = 25 °C, d.c.		250			mA
V _{GD}	T _j = 125 °C, d.c.				0.25	V
I _{GD}	T _j = 125 °C, d.c.				10	mA
R _{th(j-c)}	continuous DC	SSC				K/W
		DSC			0.021	K/W
R _{th(j-c)}	sin. 180°	SSC			0.054	K/W
		DSC			0.0225	K/W
R _{th(j-c)}	rec. 120°	SSC			0.06	K/W
		DSC			0.027	K/W
Module						
R _{th(c-s)}	SSC			0.01		K/W
	DSC			0.005		K/W
F			22		25	KN
a						m/s ²
w				480		g

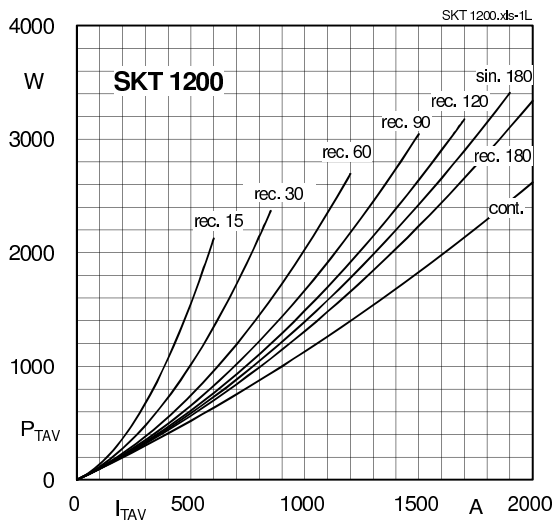


Fig. 1L: Power dissipation vs. on-state current

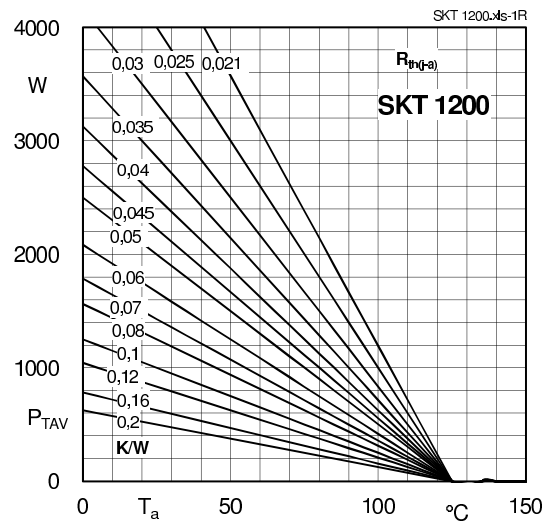


Fig. 1R: Power dissipation vs. ambient temperature

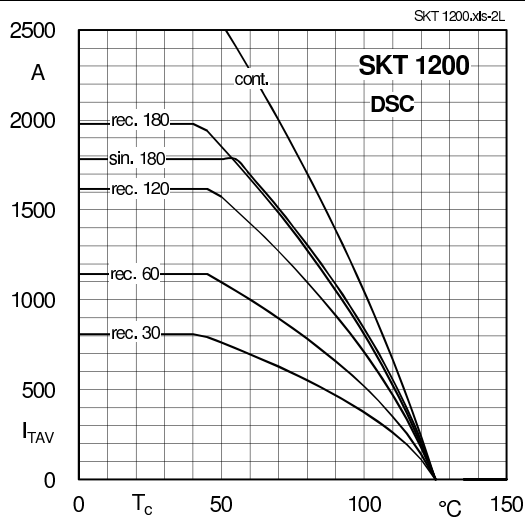


Fig. 2L: Rated on-state current vs. case temperature

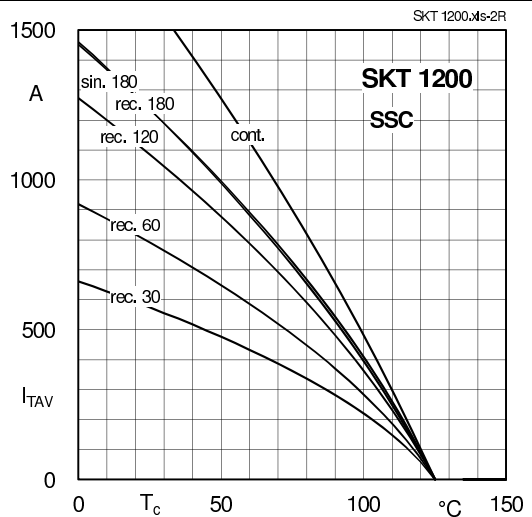


Fig. 2R: Rated on-state current vs. case temperature

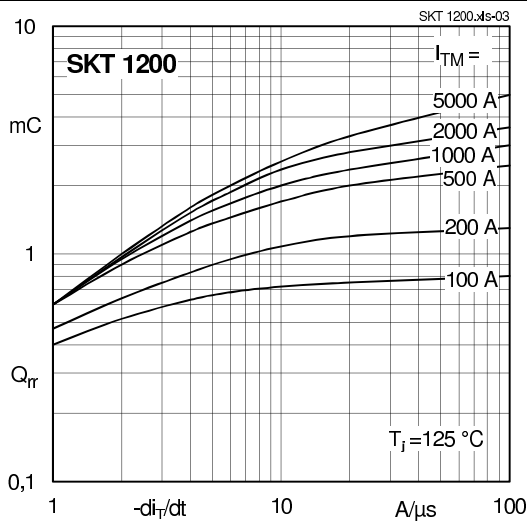


Fig. 3: Recovered charge vs. current decrease

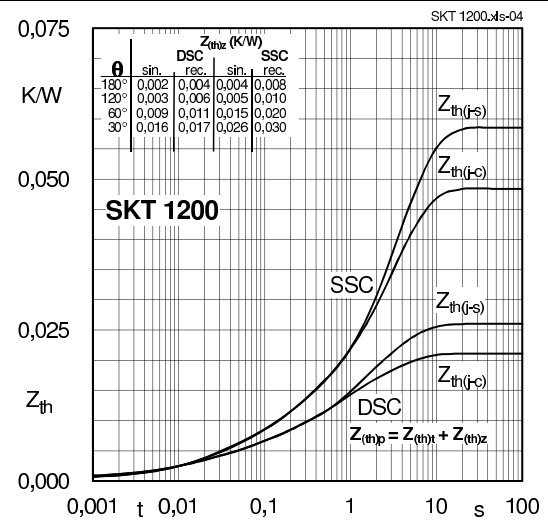


Fig. 4: Transient thermal impedance vs. time

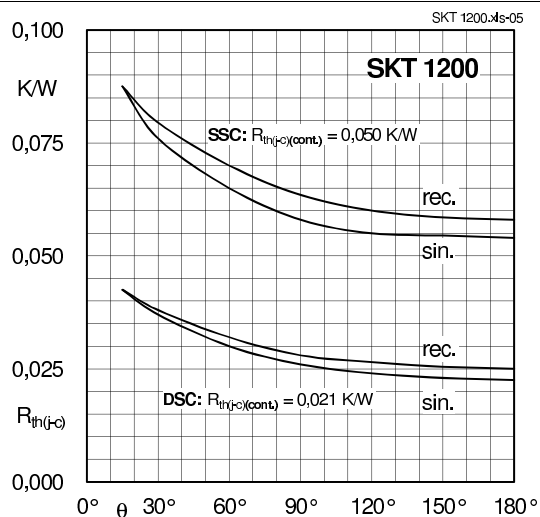


Fig. 5: Thermal resistance vs. conduction angle

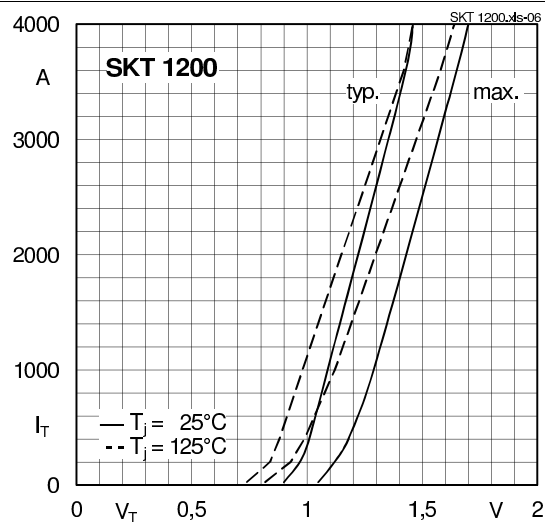


Fig. 6: On-state characteristics

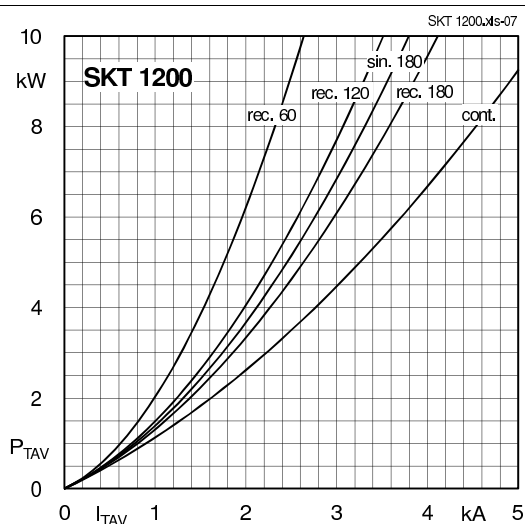


Fig. 7: Power dissipation vs. on-state current

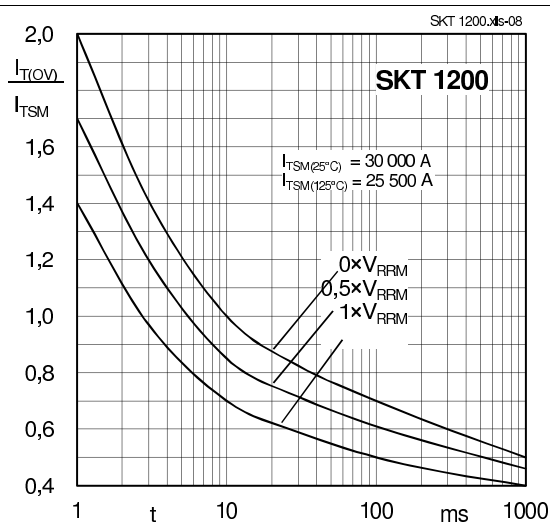


Fig. 8: Surge overload current vs. time

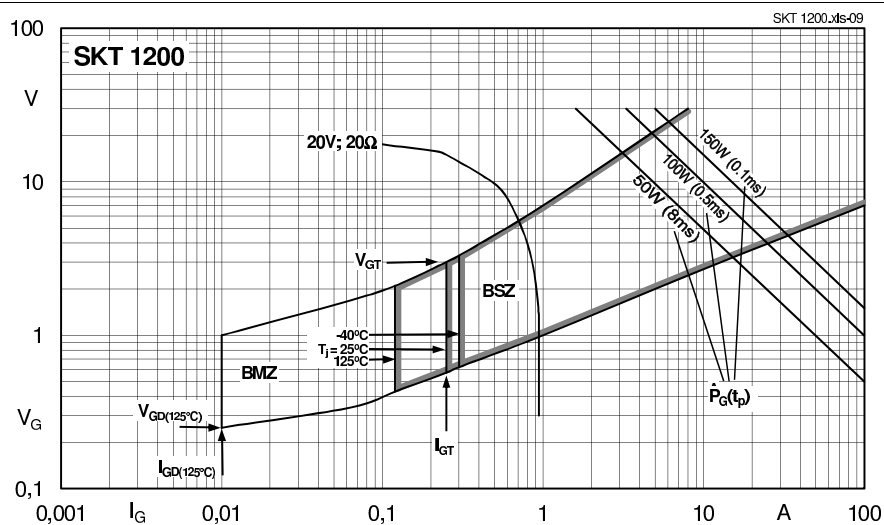
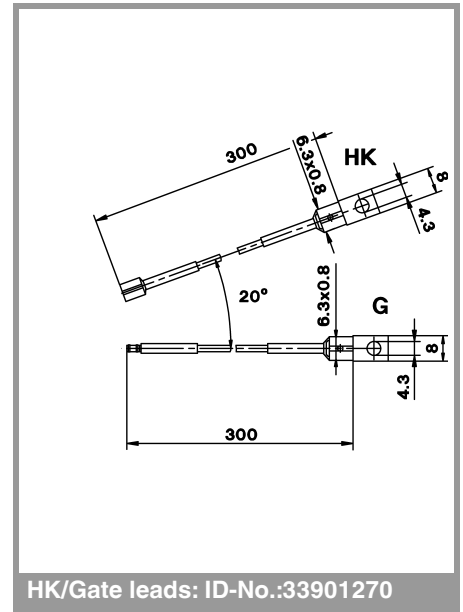
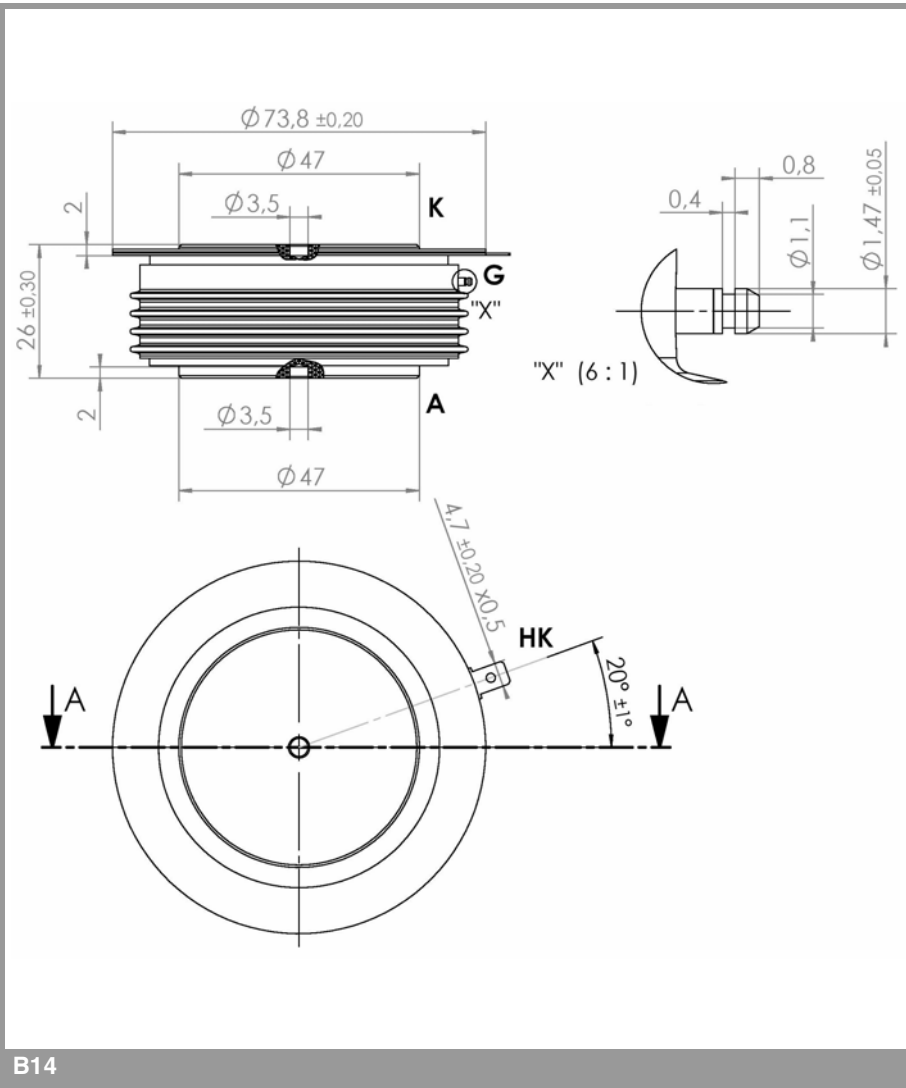


Fig. 9: Gate trigger characteristics



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX

* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our staff.