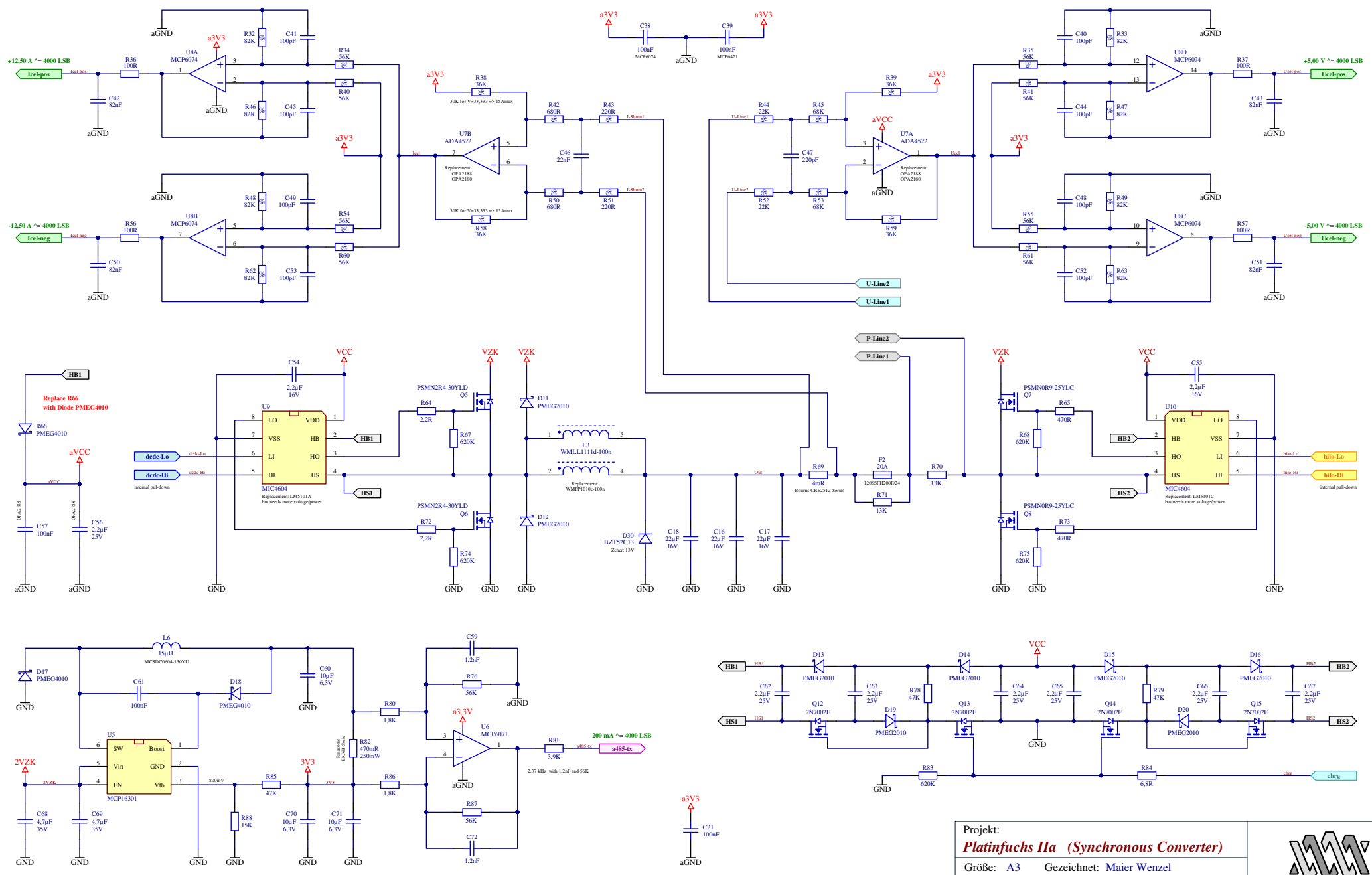

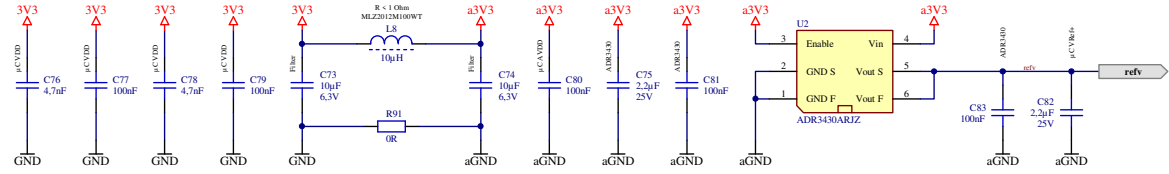
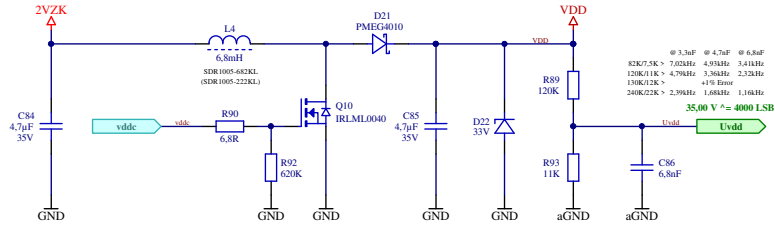


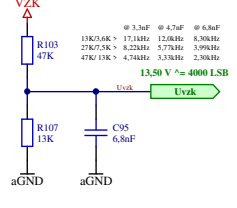
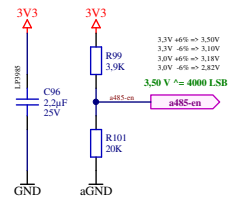
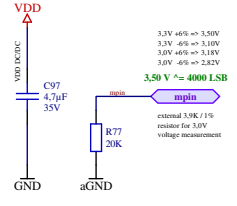
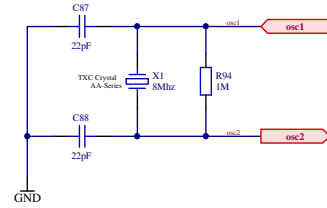
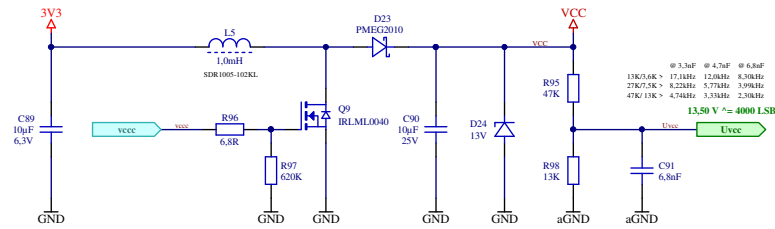
Projekt: Platinfuchs IIa (Push-Pull Converter)	
Größe: A3 Gezeichnet: Maier Wenzel	
Blatt: 1 of 4 Datum: 31.03.2022 Revision: 1	
File: E:\Silberfuchs\Platinfuchs_IIa_20220217-UFSCAR\Platinfuchs_IIa.SchDoc	



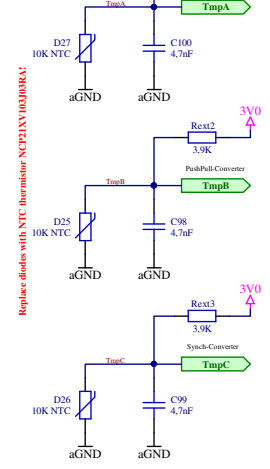
Projekt:		
Platinfuchs Iia (Synchronous Converter)		
Größe: A3	Gezeichnet: Maier Wenzel	
Blatt: 2 of 4	Datum: 31.03.2022 Revision: 1	
File: E:\Silberfuchs\Platinfuchs_Iia_20220217-UFSCAR\Platinfuchs_Iib.SchDoc		



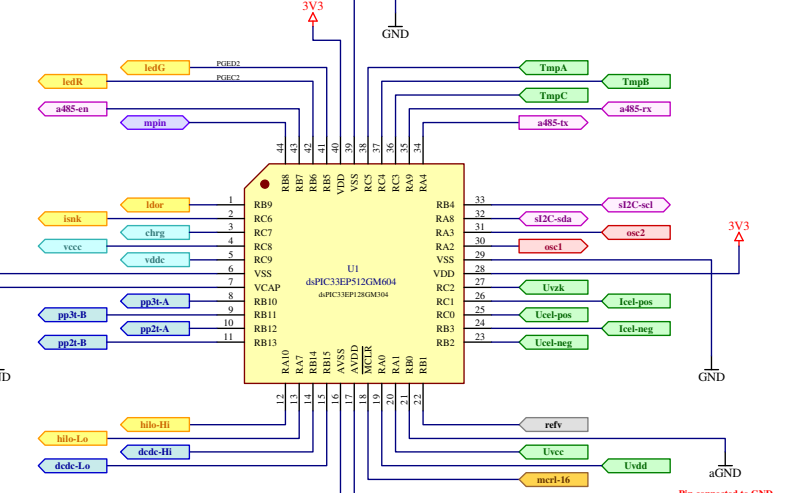
Lay an extra aGND line from the reference output to the output filter of current and voltage.



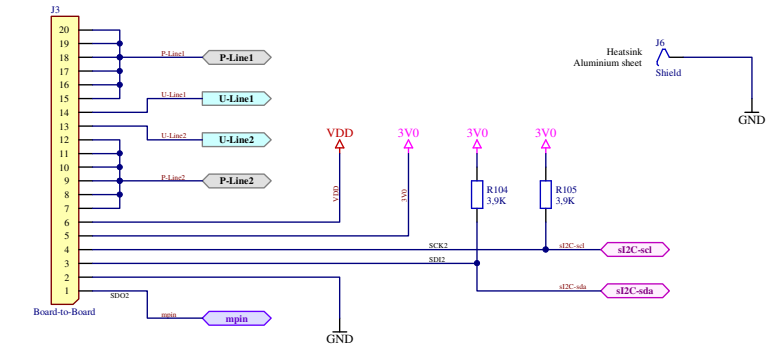
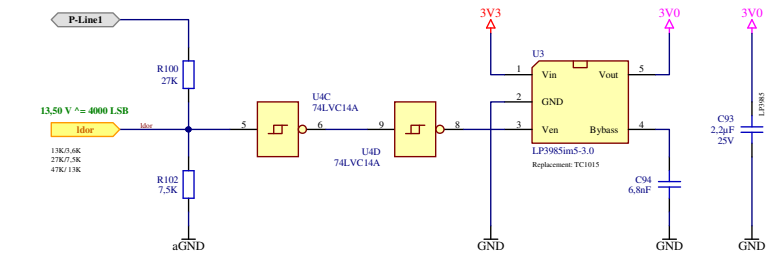
Replace diodes with NTC thermistor NCP21XX103J03RA1



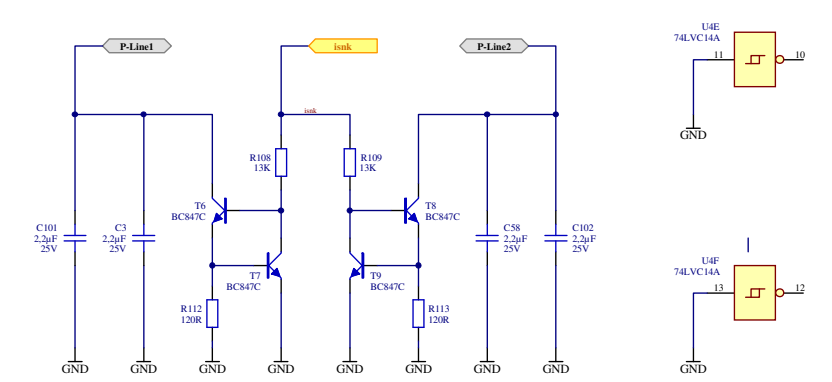
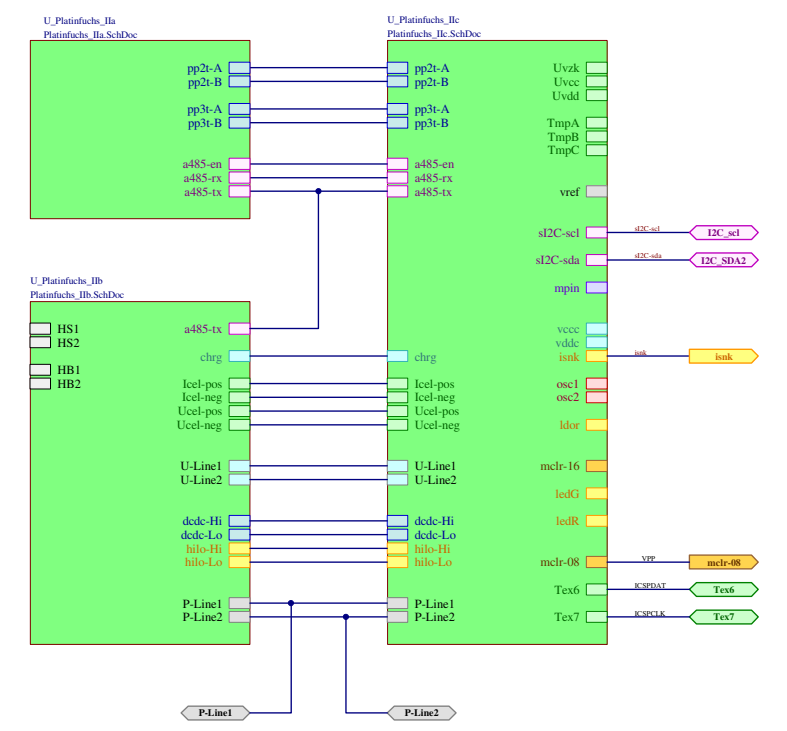
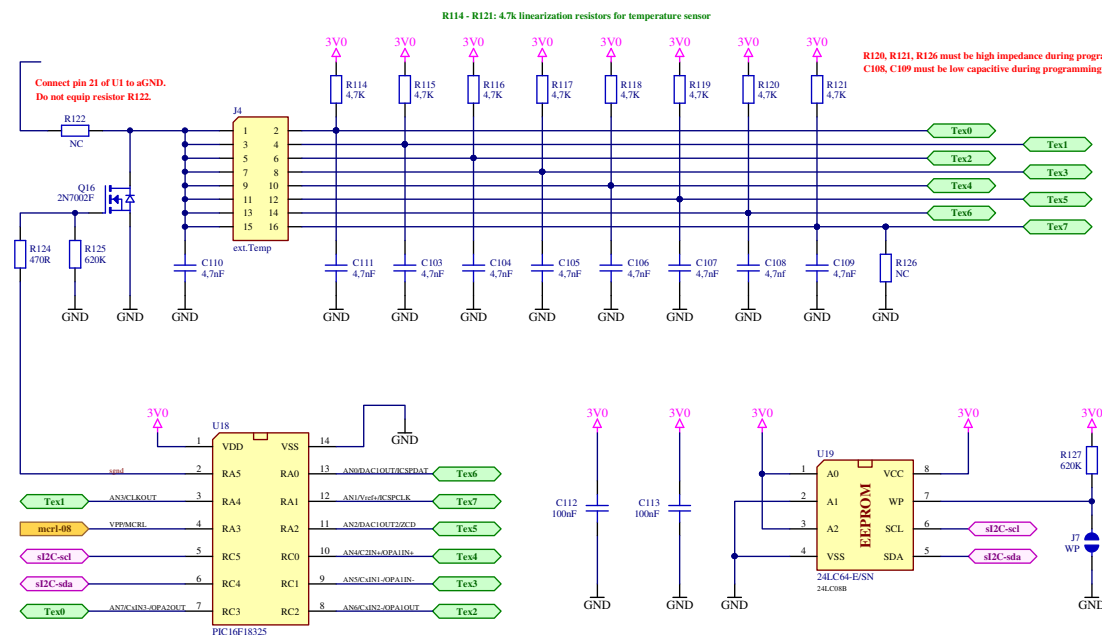
Supply via pull-up resistor (CTM) until from aG does not work well!
Wire Rext1 and Rext2 as a floating 300 resistor on the printed circuit board.



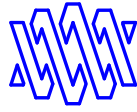
Pin connected to GND with a cable.



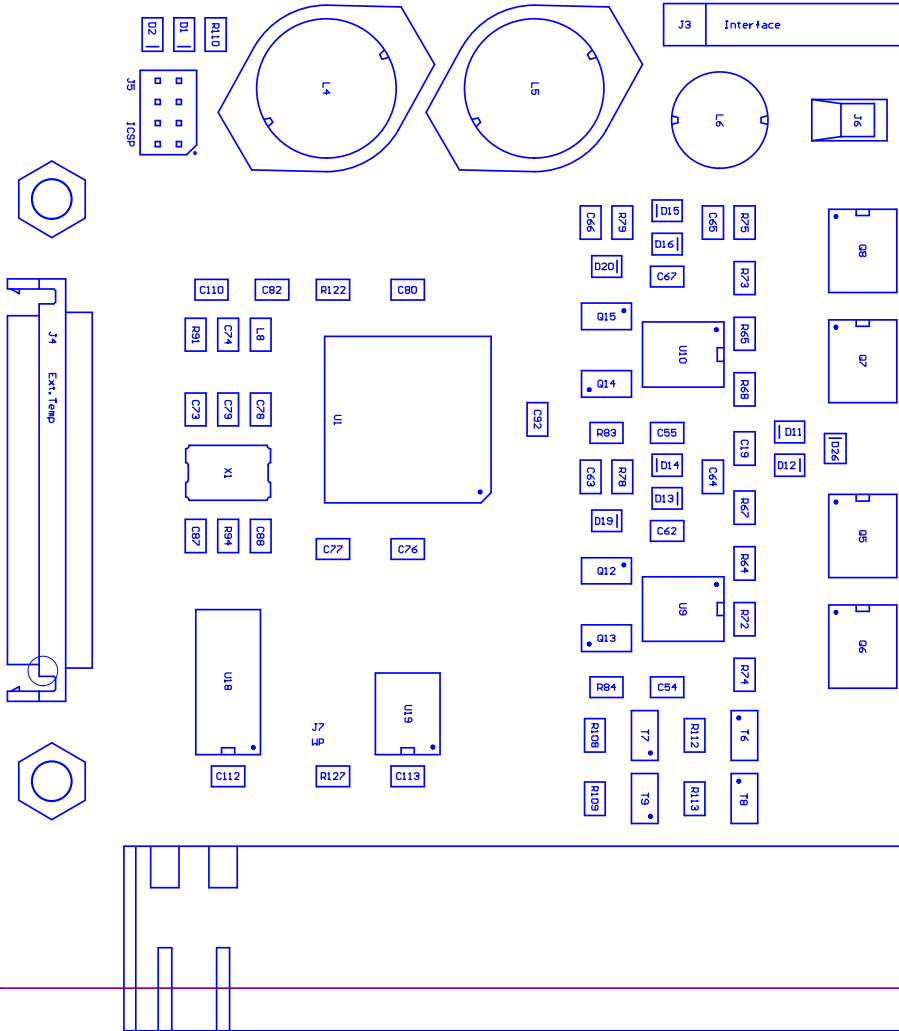
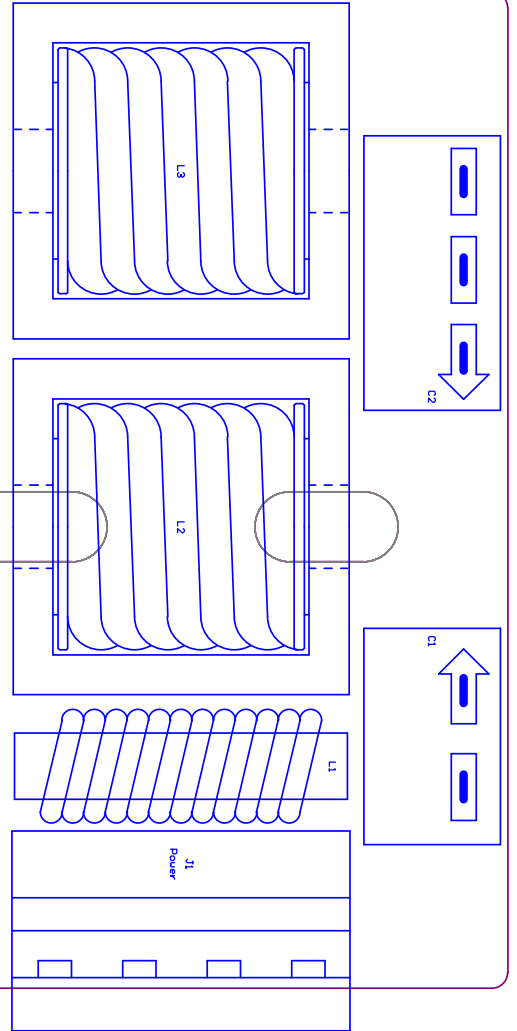
Projekt:			
Platinfuchs IIA (Microcontroller)			
Größe: A3	Gezeichnet: Maier Wenzel		
Blatt: 3 of 4	Datum: 31.03.2022	Revision: 1	
File: E:\Silberfuchs\Platinfuchs_IIA_20220217-UFSCAR\Platinfuchs_IIC.SchDoc			

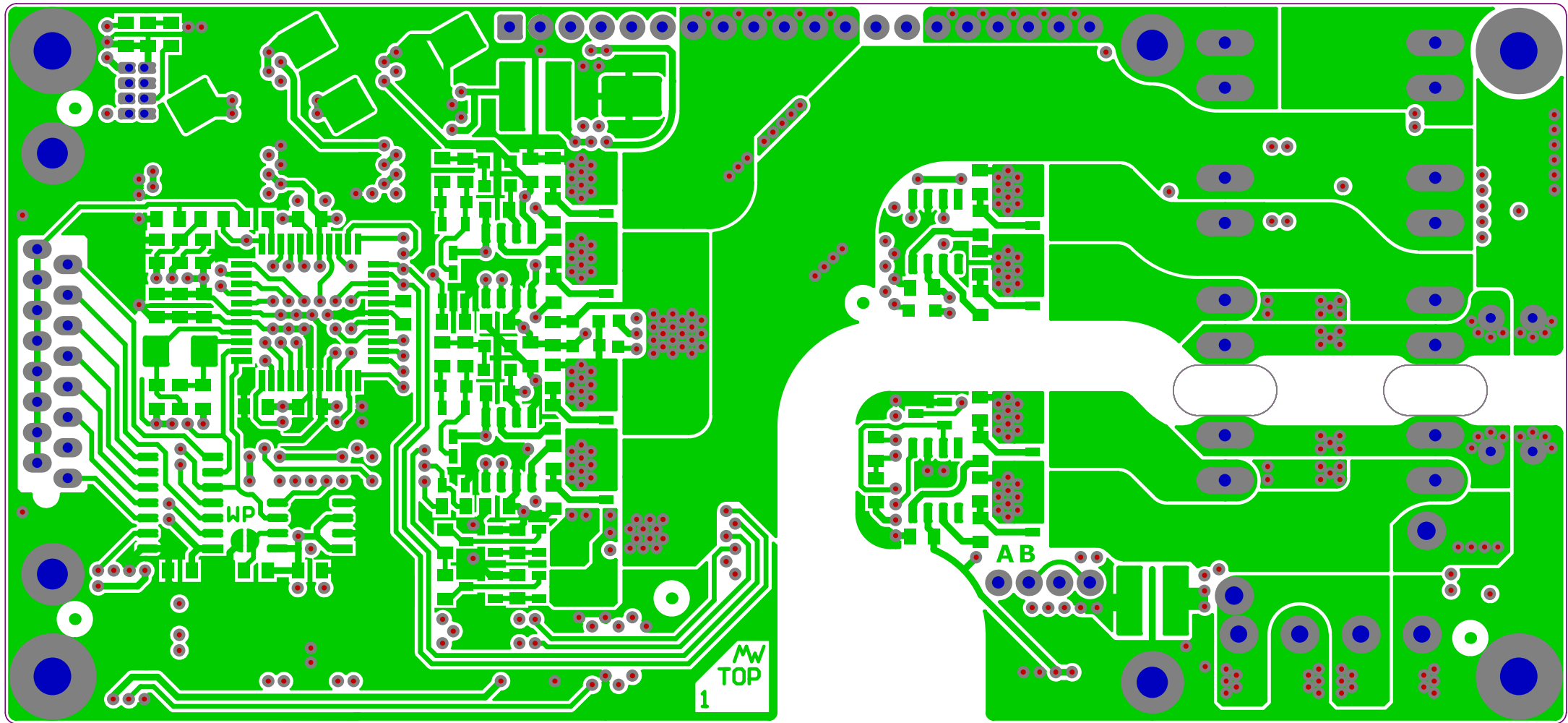


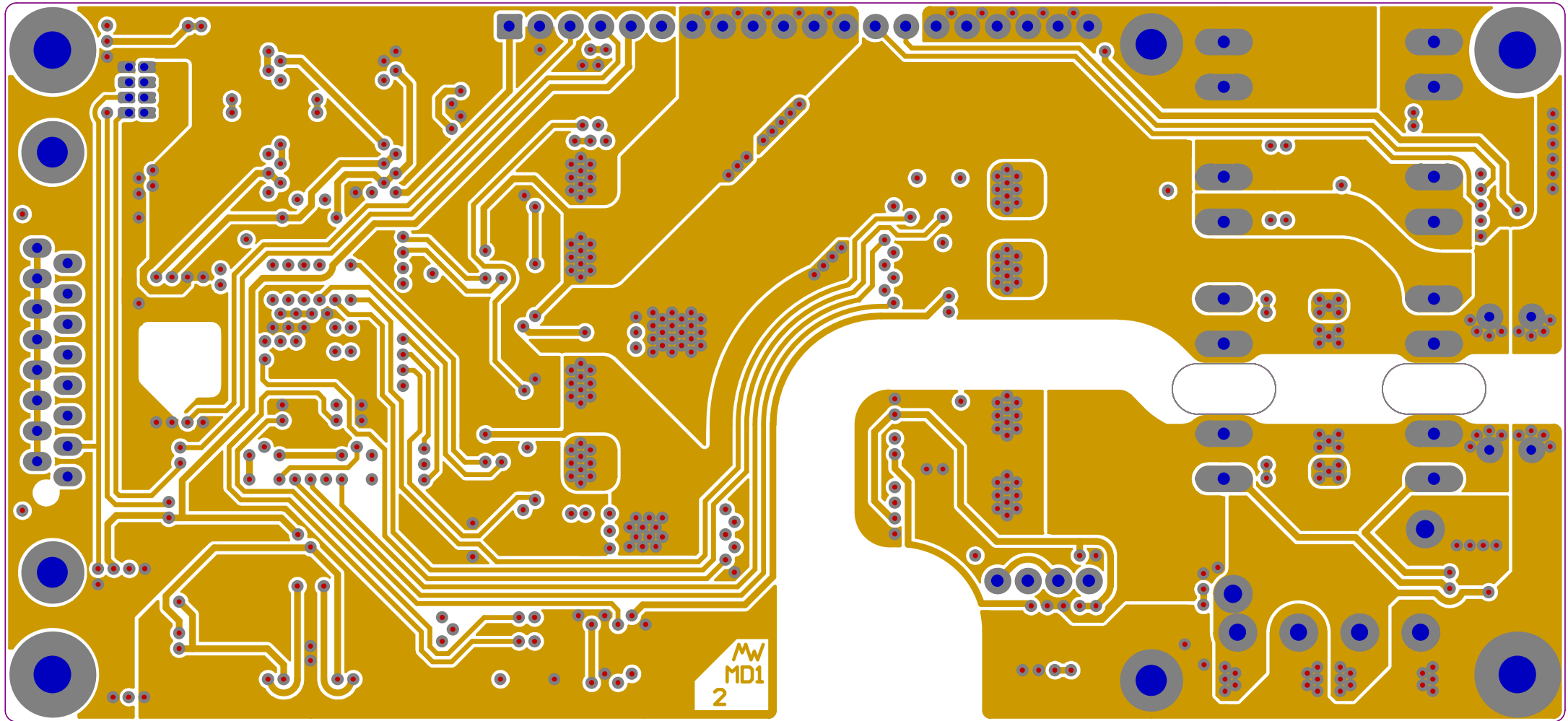
Projekt: Platinfuchs IIa (external Temperature)		
Größe: A3	Gezeichnet: Maier Wenzel	
Blatt: 4 of 4	Datum: 31.03.2022 Revision: 1	
File: E:\Silberfuchs\Platinfuchs_IIa_20220217-UFSCAR\Platinfuchs_IIa.SchDoc		

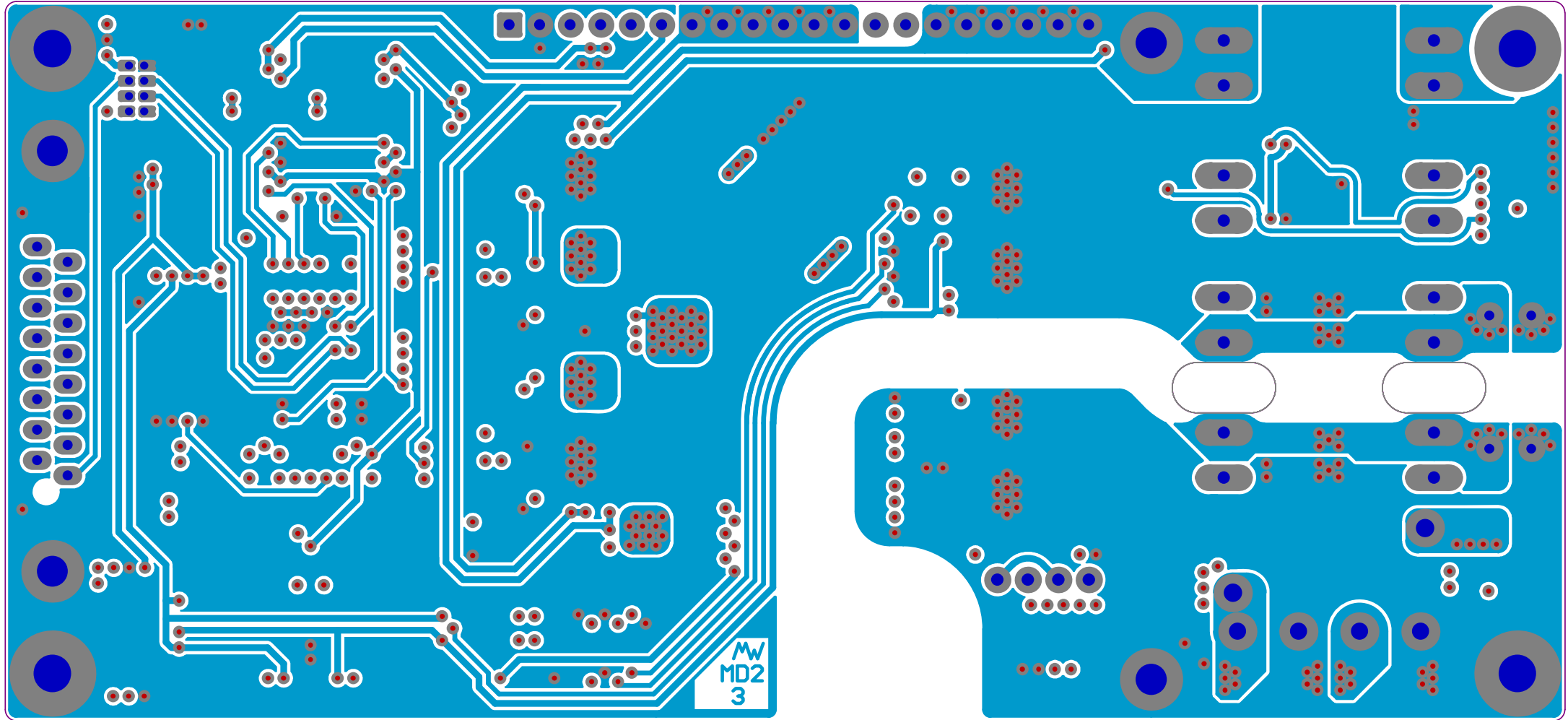


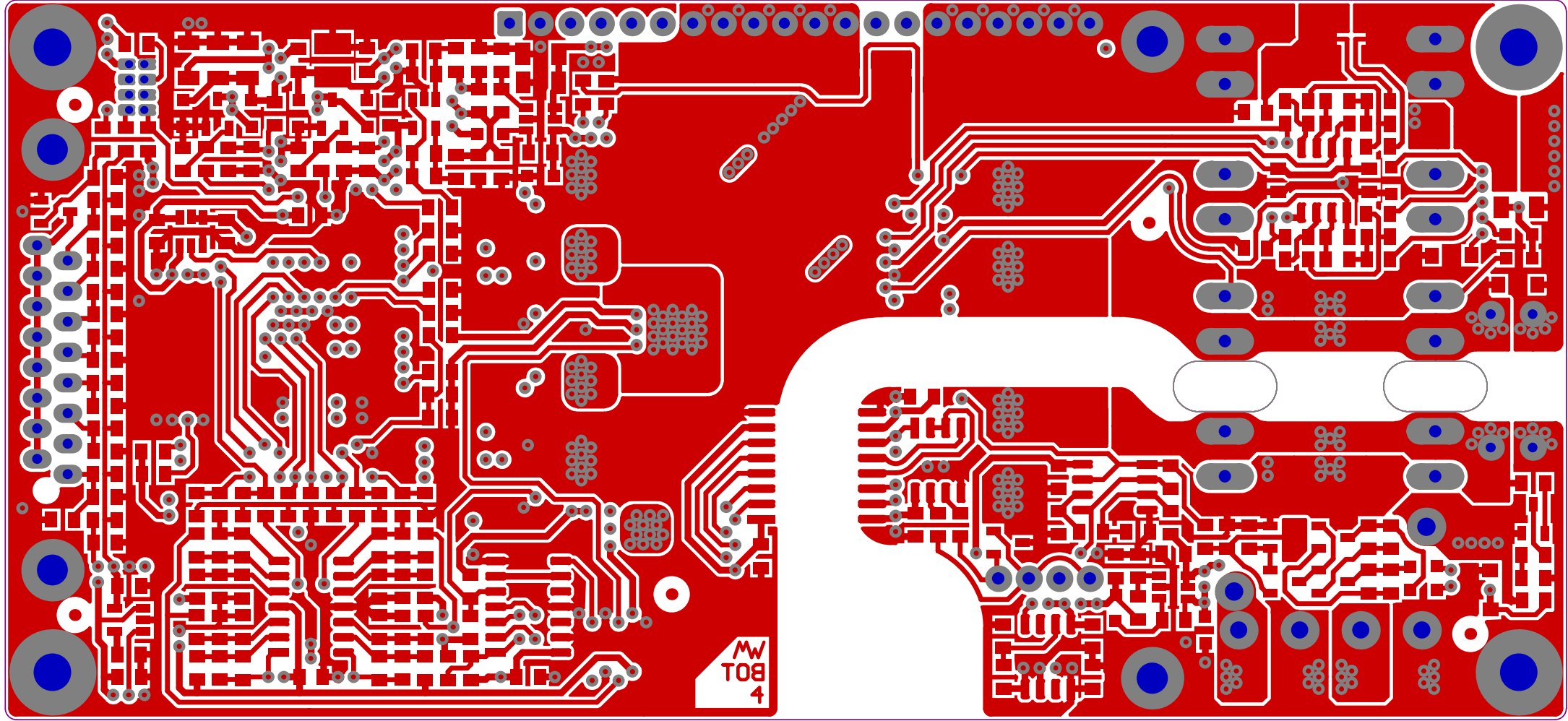
**Silberfuchs
Platinfuchs IIa
Maier Wenzel 14.7.2015**



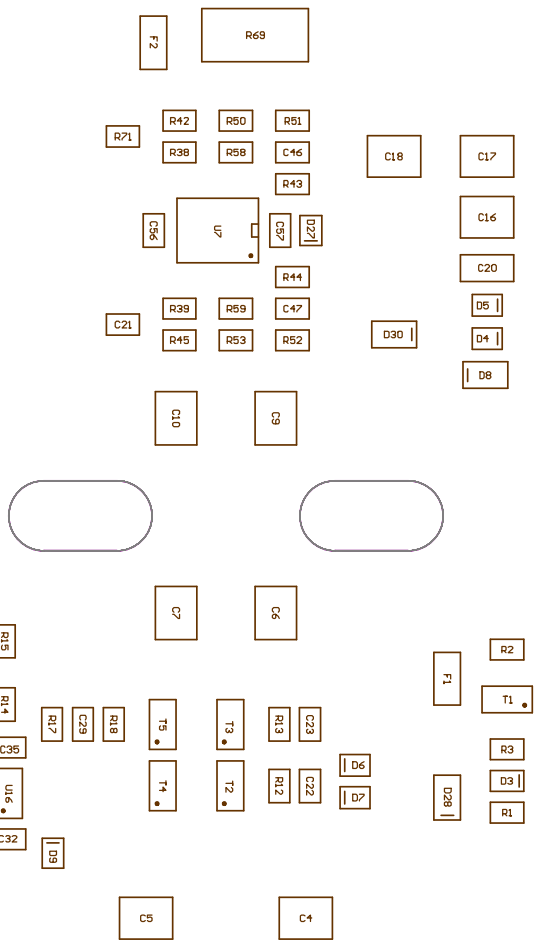








M/
BOT
4



Silberfuchs IIa



14.7.2012
 Wenzel
 Maier

