



July 2014

2N4403 / MMBT4403 PNP General-Purpose Amplifier

Description

This device is designed for use as a general-purpose amplifier and switch for collector currents to 500 mA.

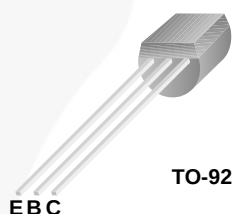


Figure 1. 2N4403 Device Package

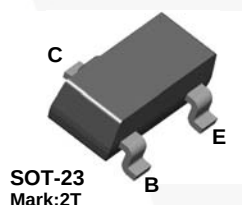


Figure 2. MMBT4403 Device Package

Ordering Information

Part Number	Marking	Package	Packing Method
2N4403BU	2N4403	TO-92 3L	Bulk
2N4403TF	2N4403	TO-92 3L	Tape and Reel
2N4403TFR	2N4403	TO-92 3L	Tape and Reel
2N4403TA	2N4403	TO-92 3L	Ammo
2N4403TAR	2N4403	TO-92 3L	Ammo
MMBT4403	2T	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings^{(1),(2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{CEO}	Collector-Emitter Voltage	-40	V
V_{CBO}	Collector-Base Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5.0	V
I_C	Collector Current - Continuous	-600	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

Notes:

1. These ratings are based on a maximum junction temperature of 150°C .
2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

Thermal Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Max.		Unit
		2N4403 ⁽³⁾	MMBT4403 ⁽⁴⁾	
P_D	Total Device Dissipation	625	350	mW
	Derate Above 25°C	5.0	2.8	mW/ $^\circ\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	$^\circ\text{C/W}$

Notes:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.
4. Device mounted on FR-4 PCB 1.6 inch x 1.6 inch x 0.06 inch.

Electrical Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Characteristics					
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage ⁽⁵⁾	I _C = -1.0 mA, I _B = 0	-40		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -0.1 mA, I _E = 0	-40		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -0.1 mA, I _C = 0	-5.0		V
I _{BL}	Base Cut-Off Current	V _{CE} = -35 V, V _{EB} = -0.4 V		-0.1	μA
I _{CEX}	Collector Cut-Off Current	V _{CE} = -35 V, V _{EB} = -0.4 V		-0.1	μA
On Characteristics					
h _{FE}	DC Current Gain	I _C = -0.1 mA, V _{CE} = -1.0 V	30		
		I _C = -1.0 mA, V _{CE} = -1.0 V	60		
		I _C = -10 mA, V _{CE} = -1.0 V	100		
		I _C = -150 mA, V _{CE} = -2.0 V ⁽⁵⁾	100	300	
		I _C = -500 mA, V _{CE} = -2.0 V ⁽⁵⁾	20		
V _{CE(sat)}	Collector-Emitter Saturation Voltage ⁽⁵⁾	I _C = -150 mA, I _B = -15 mA		-0.40	V
		I _C = -500 mA, I _B = -50 mA		-0.75	
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -150 mA, I _B = -15 mA ⁽⁵⁾	-0.75	-0.95	V
		I _C = -500 mA, I _B = -50 mA		-1.30	
Small Signal Characteristics					
f _T	Current Gain - Bandwidth Product	I _C = -20 mA, V _{CE} = -10 V, f = 100 MHz	200		MHz
C _{cb}	Collector-Base Capacitance	V _{CB} = -10 V, I _E = 0, f = 140 kHz		8.5	pF
C _{eb}	Emitter-Base Capacitance	V _{BE} = -0.5 V, I _C = 0, f = 140 kHz		30	pF
h _{ie}	Input Impedance	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	1.5	15.0	kΩ
h _{re}	Voltage Feedback Ratio	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	0.1	8.0	x10 ⁻⁴
h _{fe}	Small-Signal Current Gain	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	60	500	
h _{oe}	Output Admittance	I _C = -1.0 mA, V _{CE} = -10 V, f = 1.0 kHz	1	100	μmhos
Switching Characteristics					
t _d	Delay Time	V _{CC} = -30 V, I _C = -150 mA, I _{B1} = -15 mA		15	ns
t _r	Rise Time			20	ns
t _s	Storage Time	V _{CC} = -30 V, I _C = -150 mA, I _{B1} = I _{B2} = -15 mA		225	ns
t _f	Fall Time			30	ns

Note:

5. Pulse test: pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2.0\%$.

Typical Performance Characteristics

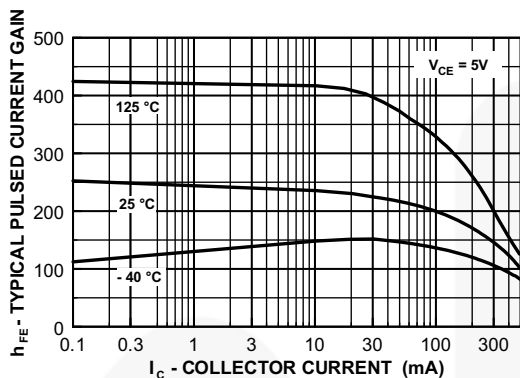


Figure 3. Typical Pulsed Current Gain vs. Collector Current

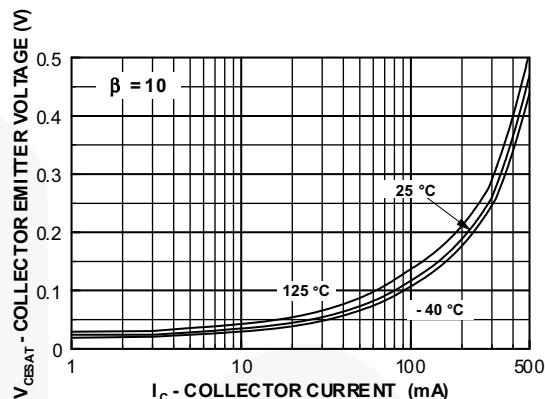


Figure 4. Collector-Emitter Saturation Voltage vs. Collector Current

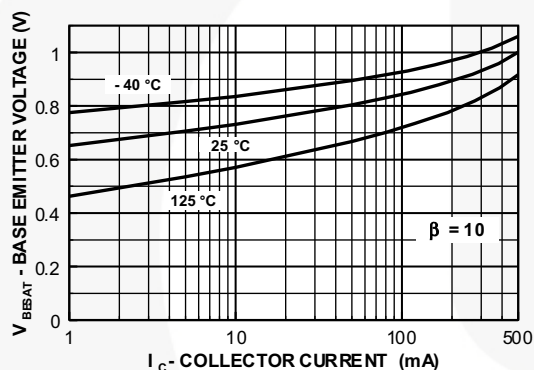


Figure 5. Base-Emitter Saturation Voltage vs. Collector Current

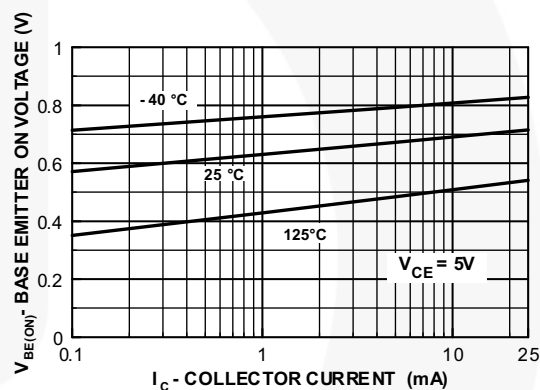


Figure 6. Base-Emitter On Voltage vs. Collector Current

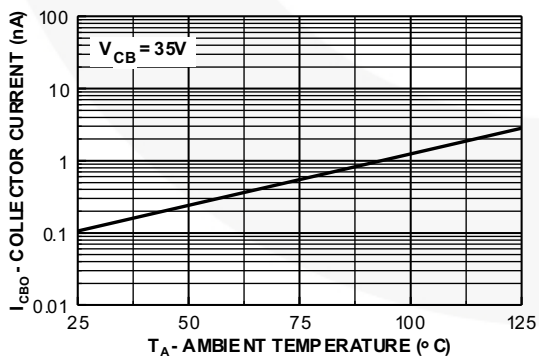


Figure 7. Collector Cut-Off Current vs. Ambient Temperature

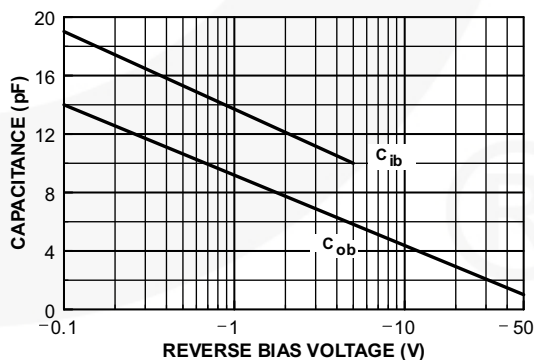


Figure 8. Input and Output Capacitance vs. Reverse Bias Voltage

Typical Performance Characteristics (Continued)

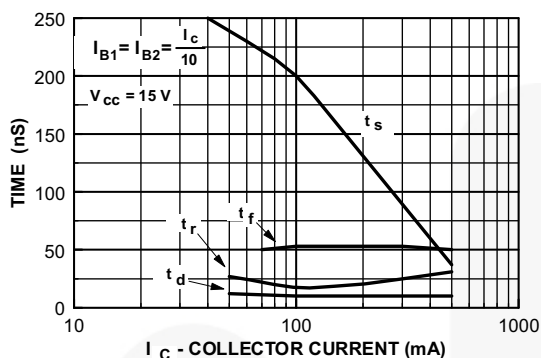


Figure 9. Switching Times vs. Collector Current

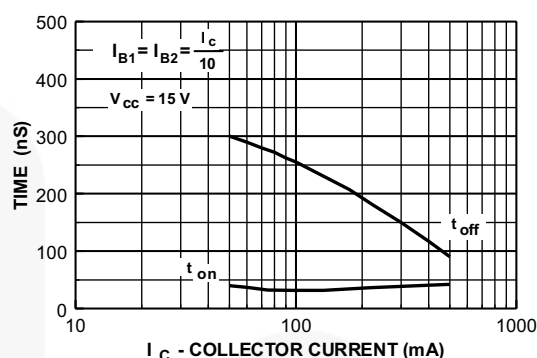


Figure 10. Turn-On and Turn-Off Times vs. Collector Current

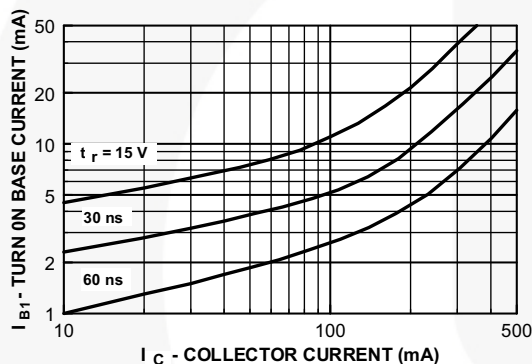


Figure 11. Rise Time vs. Collector and Turn-On Base Currents

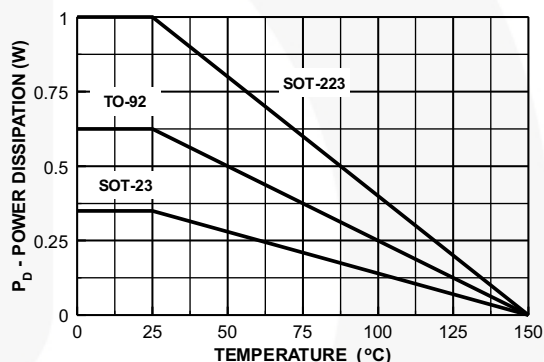


Figure 12. Power Dissipation vs. Ambient Temperature

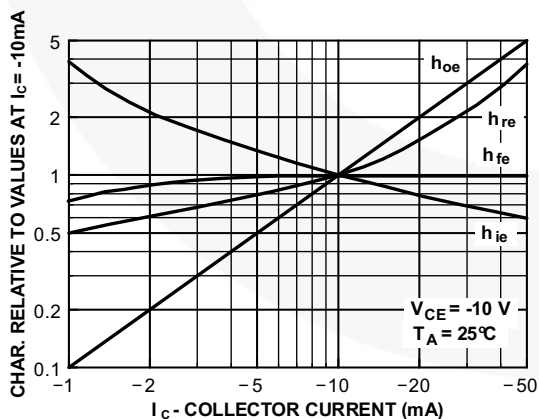


Figure 13. Common Emitter Characteristics

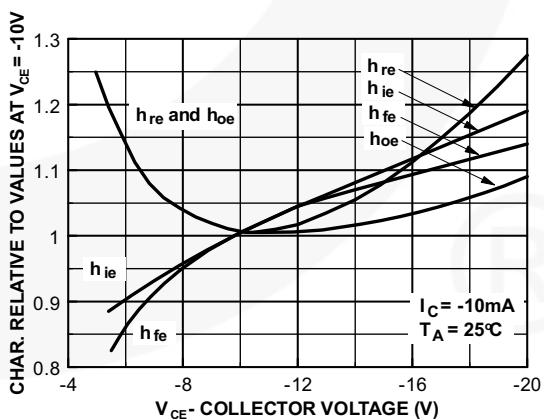


Figure 14. Common Emitter Characteristics

Typical Performance Characteristics (Continued)

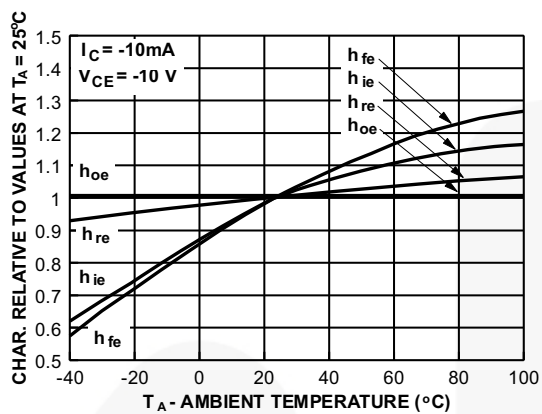
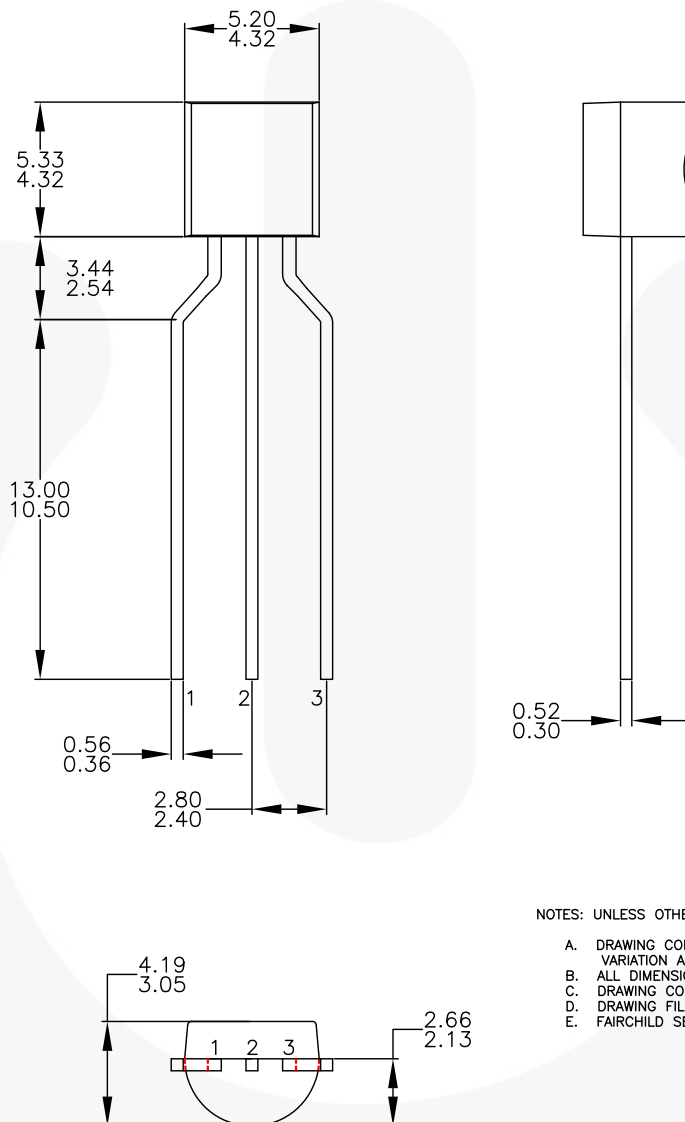


Figure 15. Common Emitter Characteristics

Physical Dimensions

TO-92 3L (Tape and Reel, Ammo)



NOTES: UNLESS OTHERWISE SPECIFIED

- A. DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5M-2009.
- D. DRAWING FILENAME: MKT-ZA03FREV3.
- E. FAIRCHILD SEMICONDUCTOR.

Figure 16. 3-LEAD, TO-92, MOLDED 0.200 IN LINE SPACING LD FORM (J61Z OPTION) (ACTIVE)

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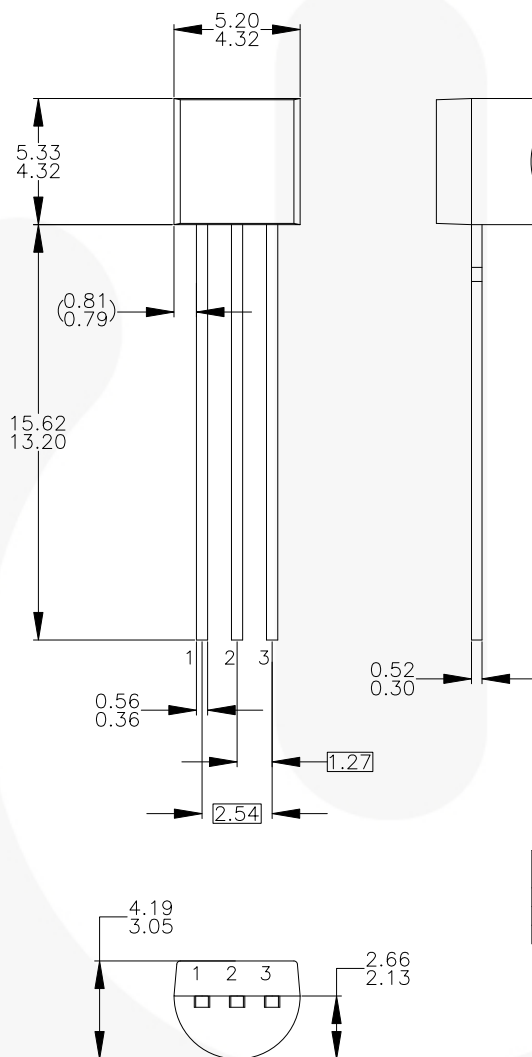
<http://www.fairchildsemi.com/dwg/ZA/ZA03F.pdf>.

For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area:

http://www.fairchildsemi.com/packing_dwg/PKG-ZA03F_BK.pdf.

Physical Dimensions (Continued)

TO-92 3L (Bulk)



NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DRAWING CONFORMS TO ASME Y14.5M-1994.
 D) TO-92 (92,94,96,97,98) PIN CONFIGURATION:

PIN	92			94			96			97			98		
	P	F	M	P	F	M	P	F	M	P	F	M	P	F	M
1	E	S	S	E	S	S	B	D	G	C	G	D	C	G	D
2	B	D	G	C	G	D	E	S	S	B	D	G	E	S	S
3	C	G	D	B	D	G	C	G	D	E	S	S	B	D	G

LEGEND:

P — BIPOLAR E — EMITTER D — DRAIN
 F — JFET B — BASE S — SOURCE
 M — DMOS C — COLLECTOR G — GATE

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEABLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZA03DREV3.

Figure 17. 3-LEAD, JEDEC TO-92 COMPLIANT STRAGHIT LEAD CONFIGURATION (OLD TO92AM3)

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For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area:

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Physical Dimensions (Continued)

SOT-23 3L

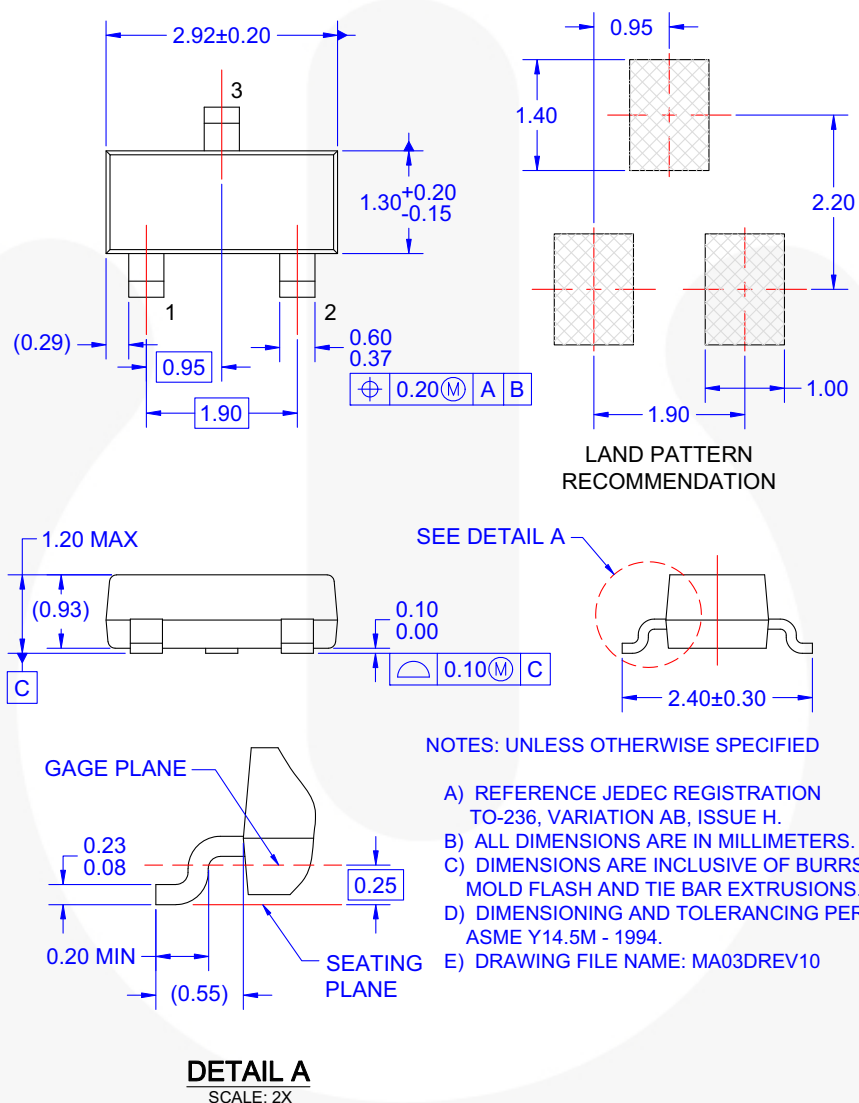


Figure 18. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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