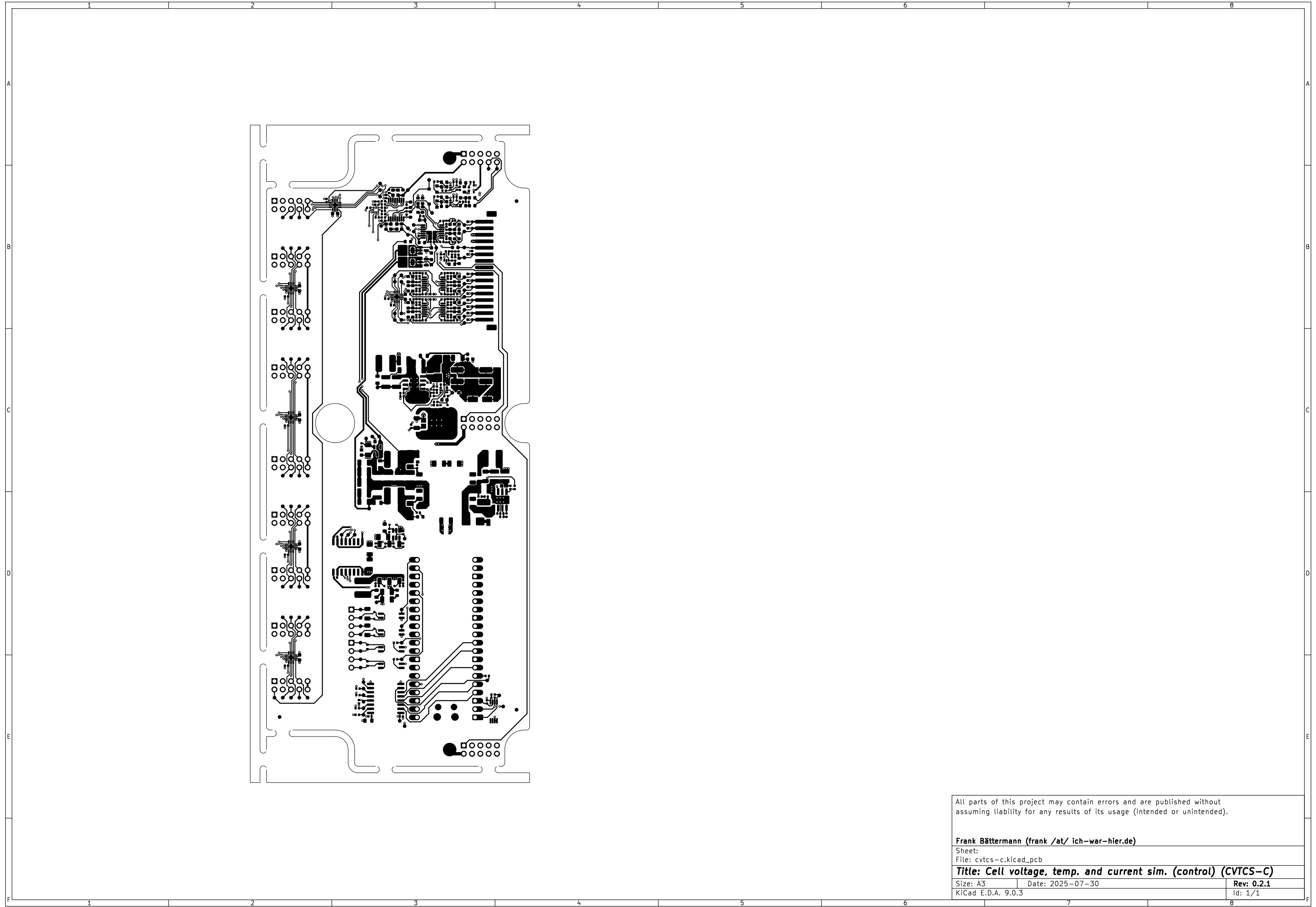
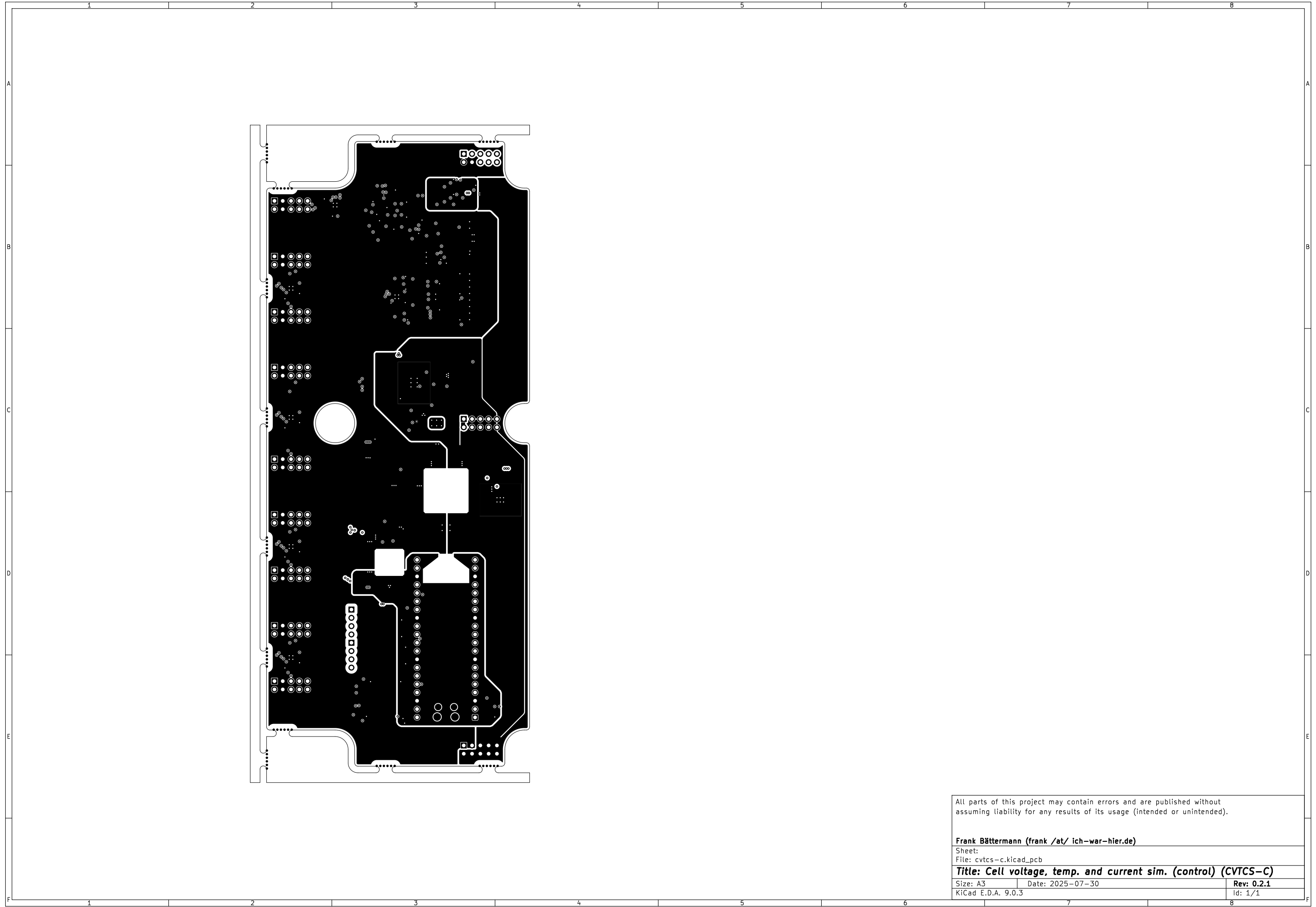




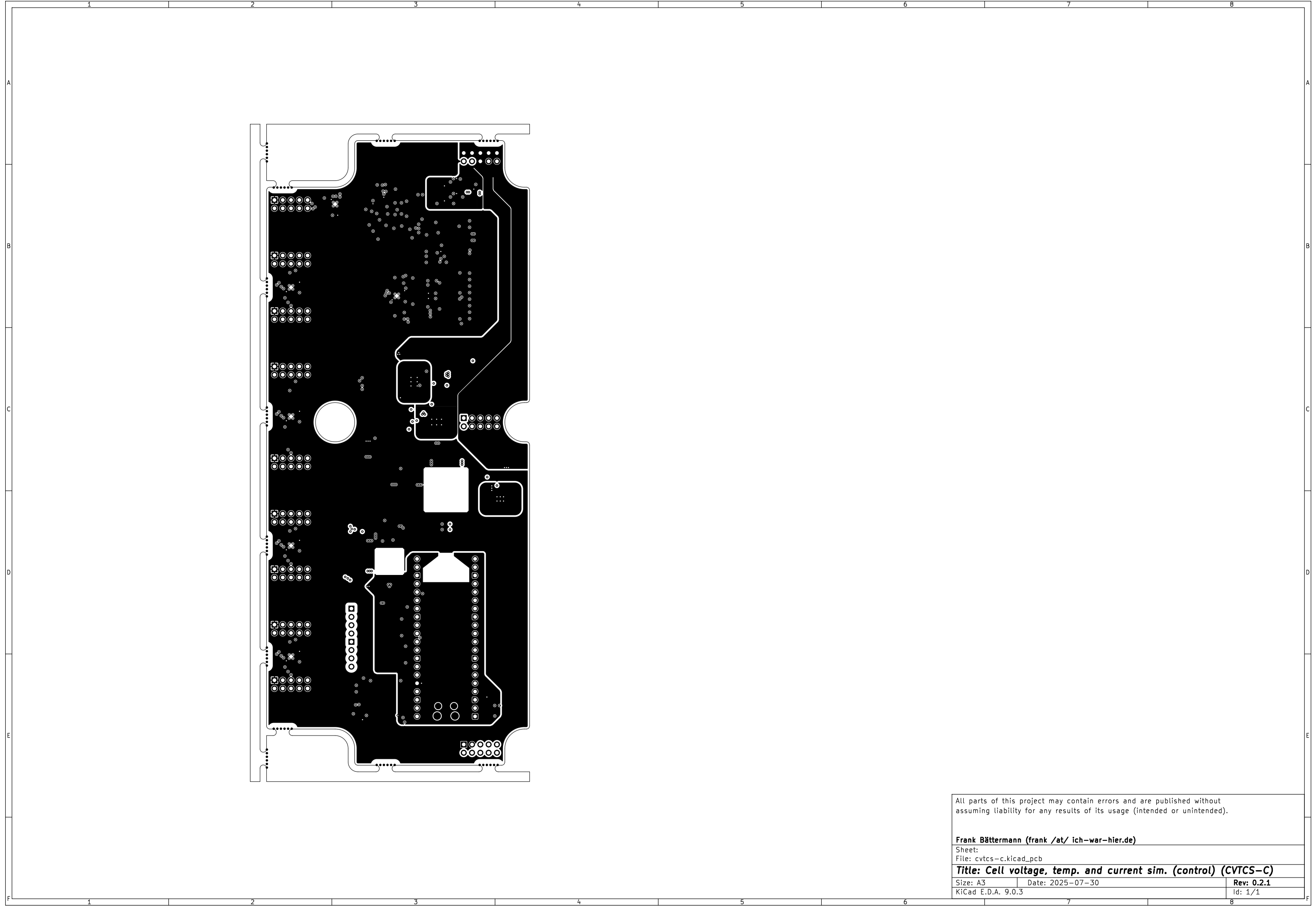
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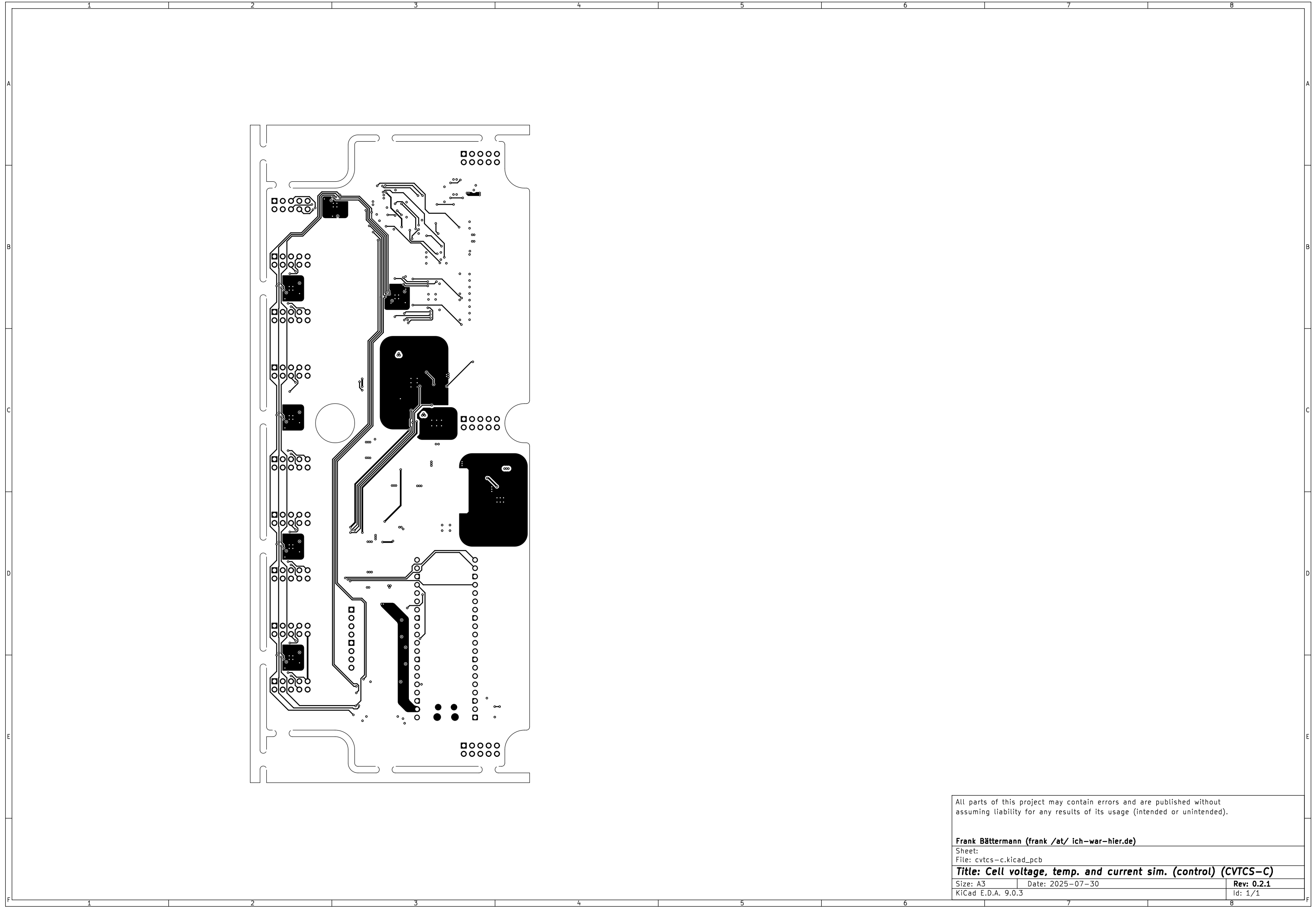


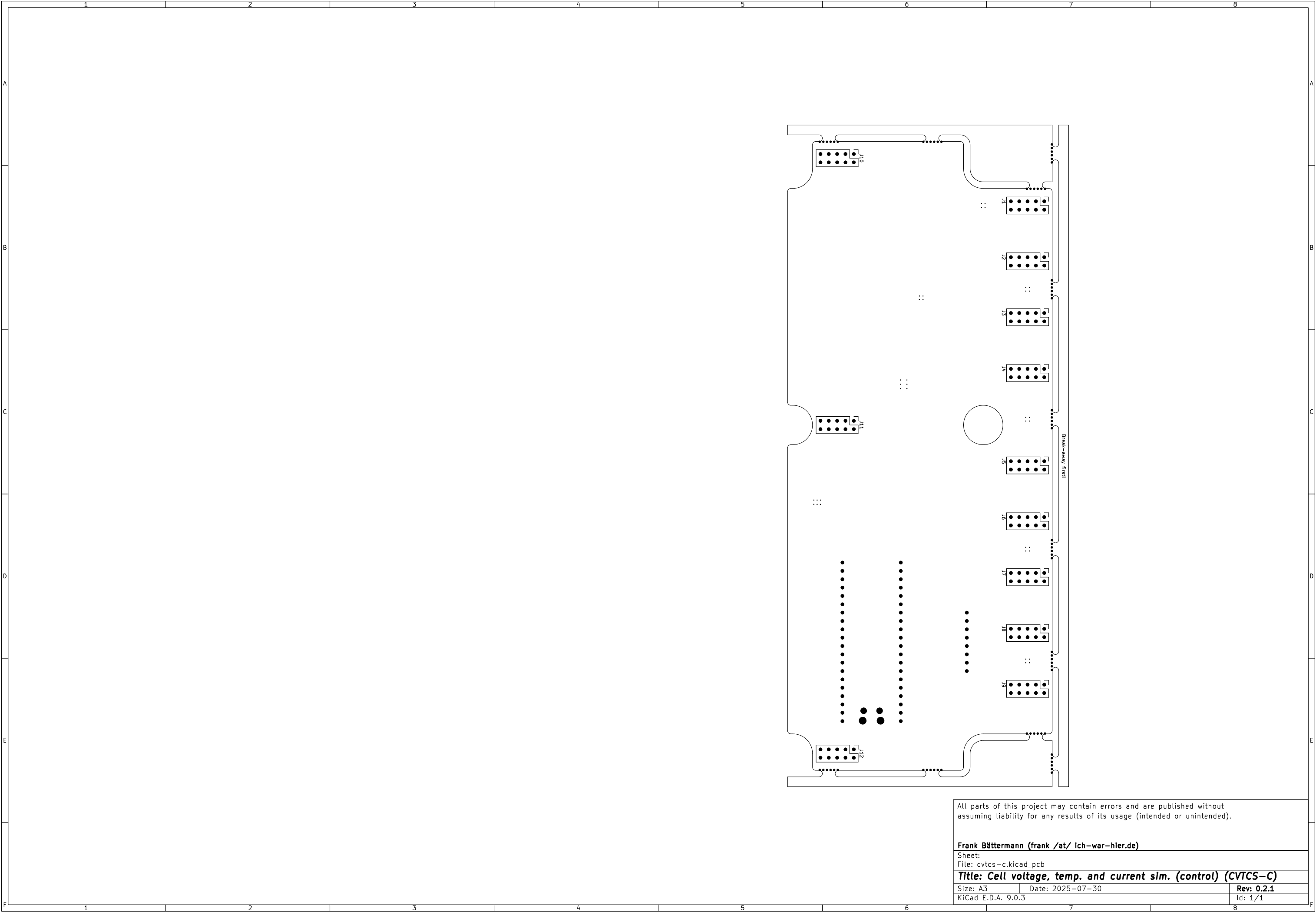
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Sheet: File: cvtcs-c.kicad_pcb		
Title: Cell voltage, temp. and current sim. (control) (CVTCS-C)		
Size: A3	Date: 2025-07-30	Rev: 0.2.1
KiCad E.D.A. 9.0.3	Id: 1/1	



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A								
B								
C								
D								
E								
F								

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