

CEVA

COM Express Evaluation Backplane

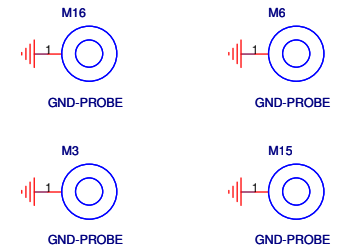
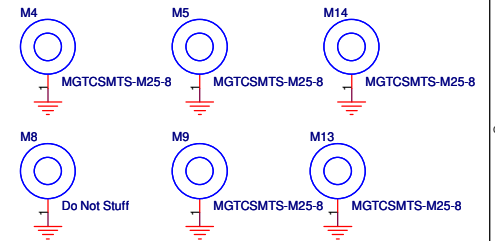
Rev. A.0

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Variants

Base Components used on all options



isolated frame GND for 3.3V DCDC
page 19

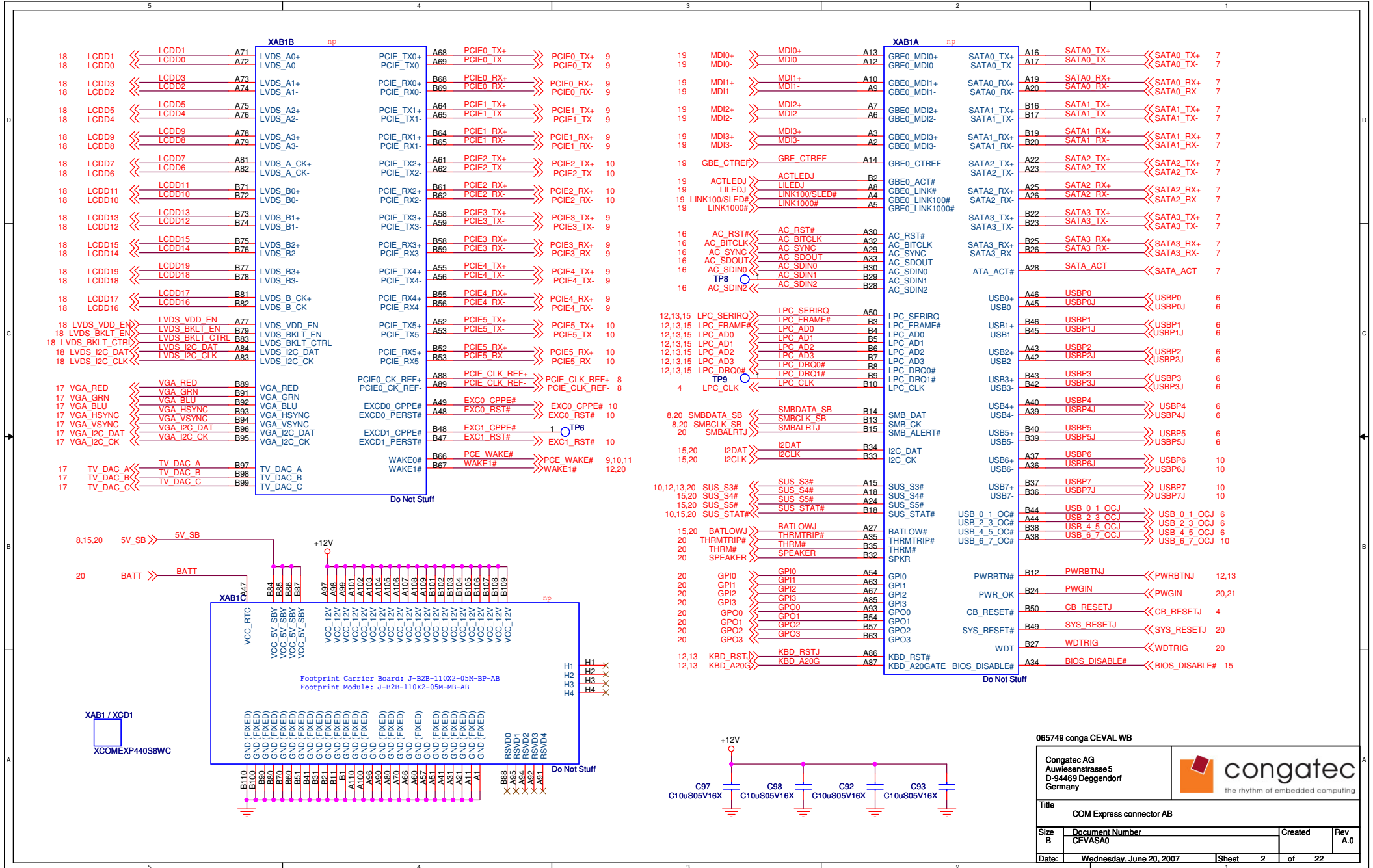


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Title CEVA - COM Express Evaluation Backplane			
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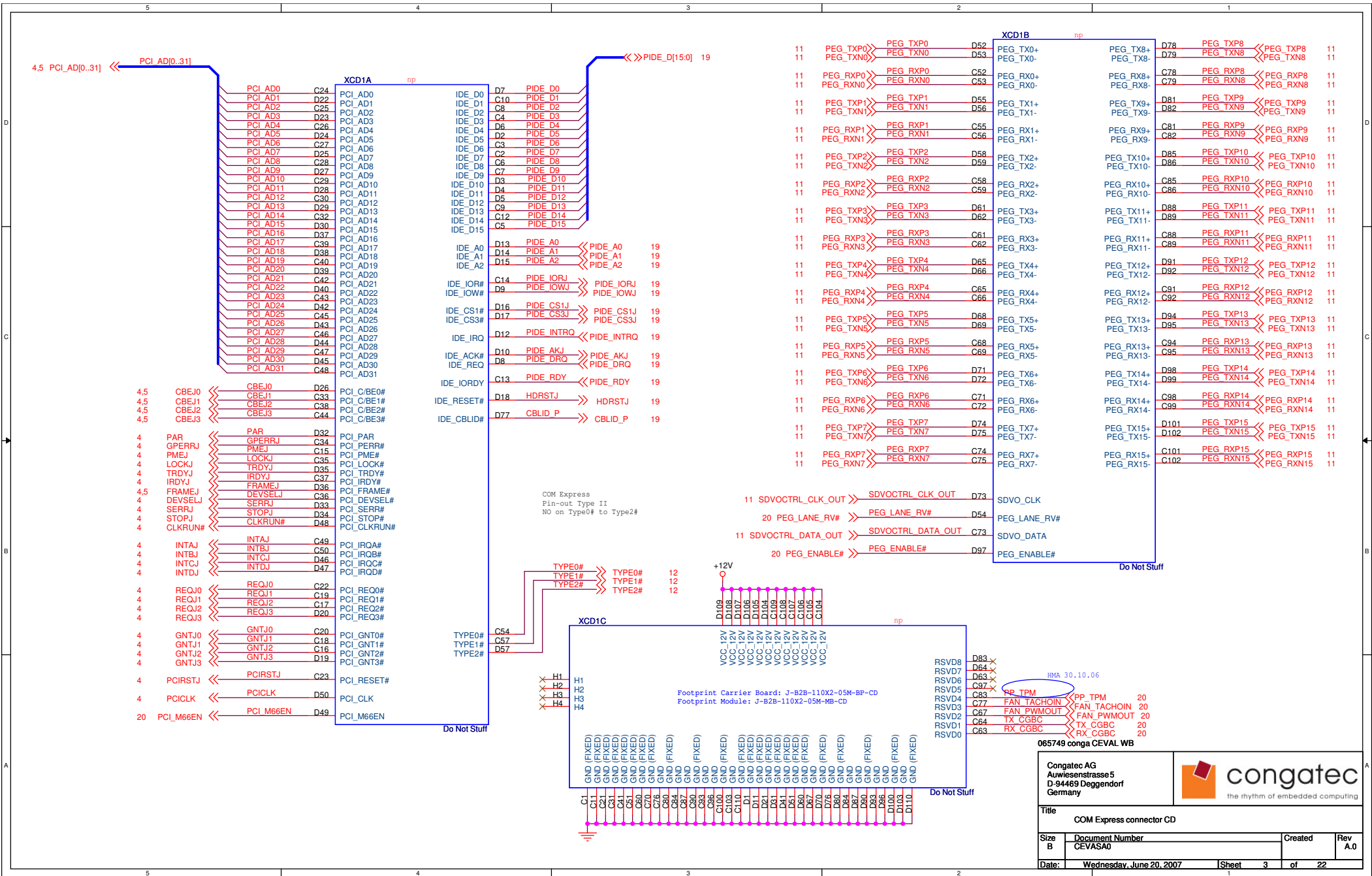
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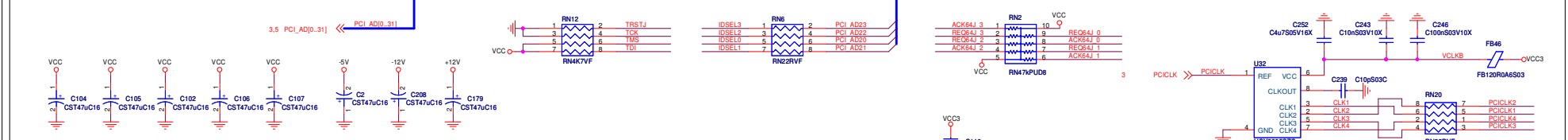
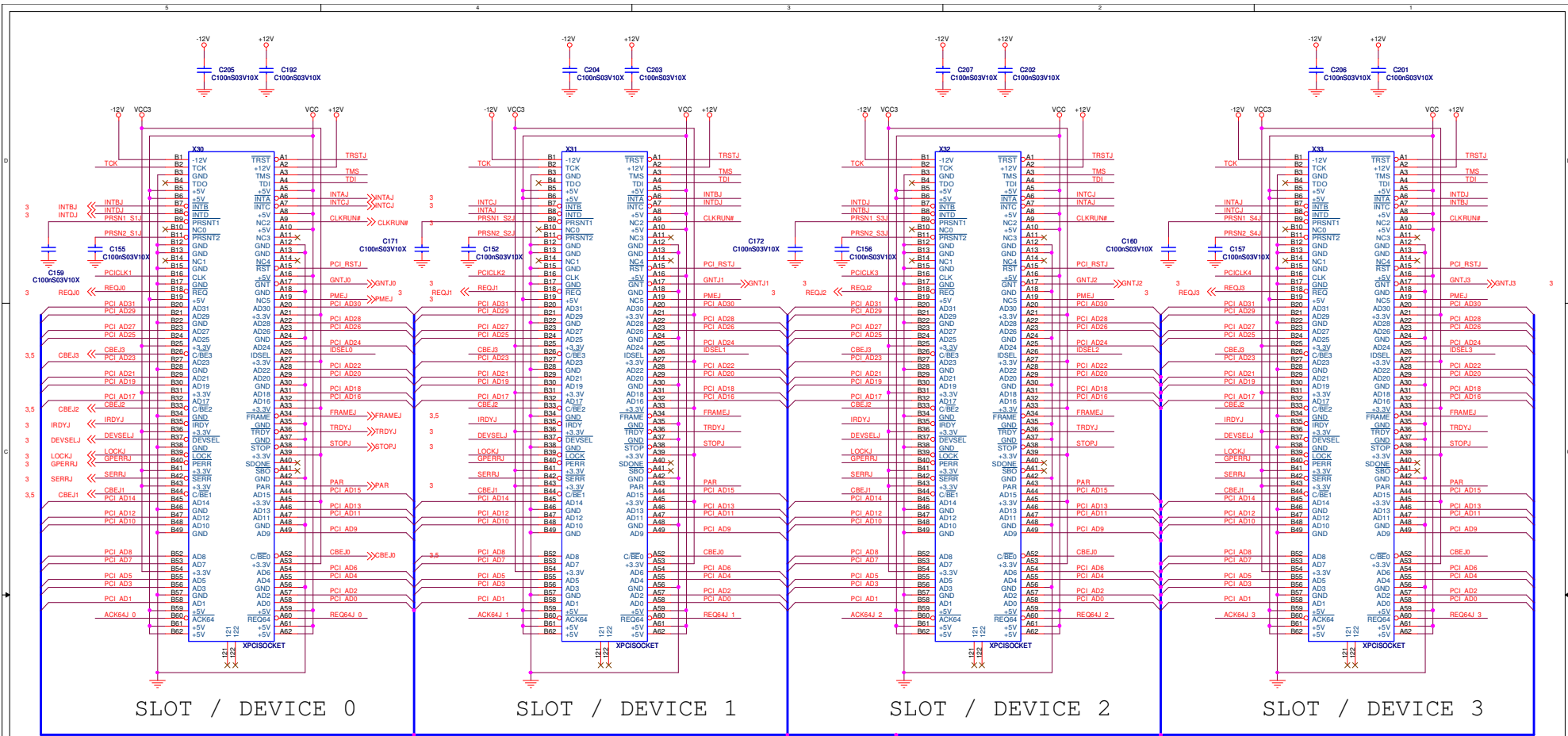
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Title: COM Express connector AB

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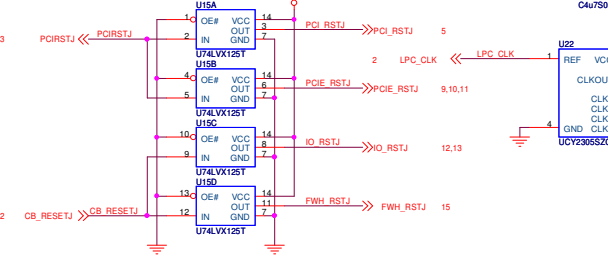
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COM Express - PCI interrupt routing and IDSELS

Slot / Device Signal	Slot / Device 0	Slot / Device 1	Slot / Device 2	Slot / Device 3
IDSEL	PCI_AD[20]	PCI_AD[21]	PCI_AD[22]	PCI_AD[23]
PCI Clock	PCI_CLK replica	PCI_CLK replica	PCI_CLK replica	PCI_CLK replica
INTA#	PCI_IRQ[A]#	PCI_IRQ[B]#	PCI_IRQ[C]#	PCI_IRQ[D]#
INTB# (if used)	PCI_IRQ[B]#	PCI_IRQ[C]#	PCI_IRQ[A]#	PCI_IRQ[D]#
INTC# (if used)	PCI_IRQ[C]#	PCI_IRQ[D]#	PCI_IRQ[A]#	PCI_IRQ[B]#
INTD# (if used)	PCI_IRQ[D]#	PCI_IRQ[A]#	PCI_IRQ[B]#	PCI_IRQ[C]#
REQ0# (if used)	PCI_REQ[0]#	PCI_REQ[1]#	PCI_REQ[2]#	PCI_REQ[3]#
REQ1# (if used)	PCI_REQ[1]#	PCI_REQ[2]#	PCI_REQ[0]#	PCI_REQ[3]#
REQ2# (if used)	PCI_REQ[2]#	PCI_REQ[3]#	PCI_REQ[0]#	PCI_REQ[1]#
REQ3# (if used)	PCI_REQ[3]#	PCI_REQ[0]#	PCI_REQ[1]#	PCI_REQ[2]#
GNT0# (if used)	PCI_GNT[0]#	PCI_GNT[1]#	PCI_GNT[2]#	PCI_GNT[3]#
GNT1# (if used)	PCI_GNT[1]#	PCI_GNT[2]#	PCI_GNT[0]#	PCI_GNT[3]#
GNT2# (if used)	PCI_GNT[2]#	PCI_GNT[3]#	PCI_GNT[0]#	PCI_GNT[1]#
GNT3# (if used)	PCI_GNT[3]#	PCI_GNT[0]#	PCI_GNT[1]#	PCI_GNT[2]#

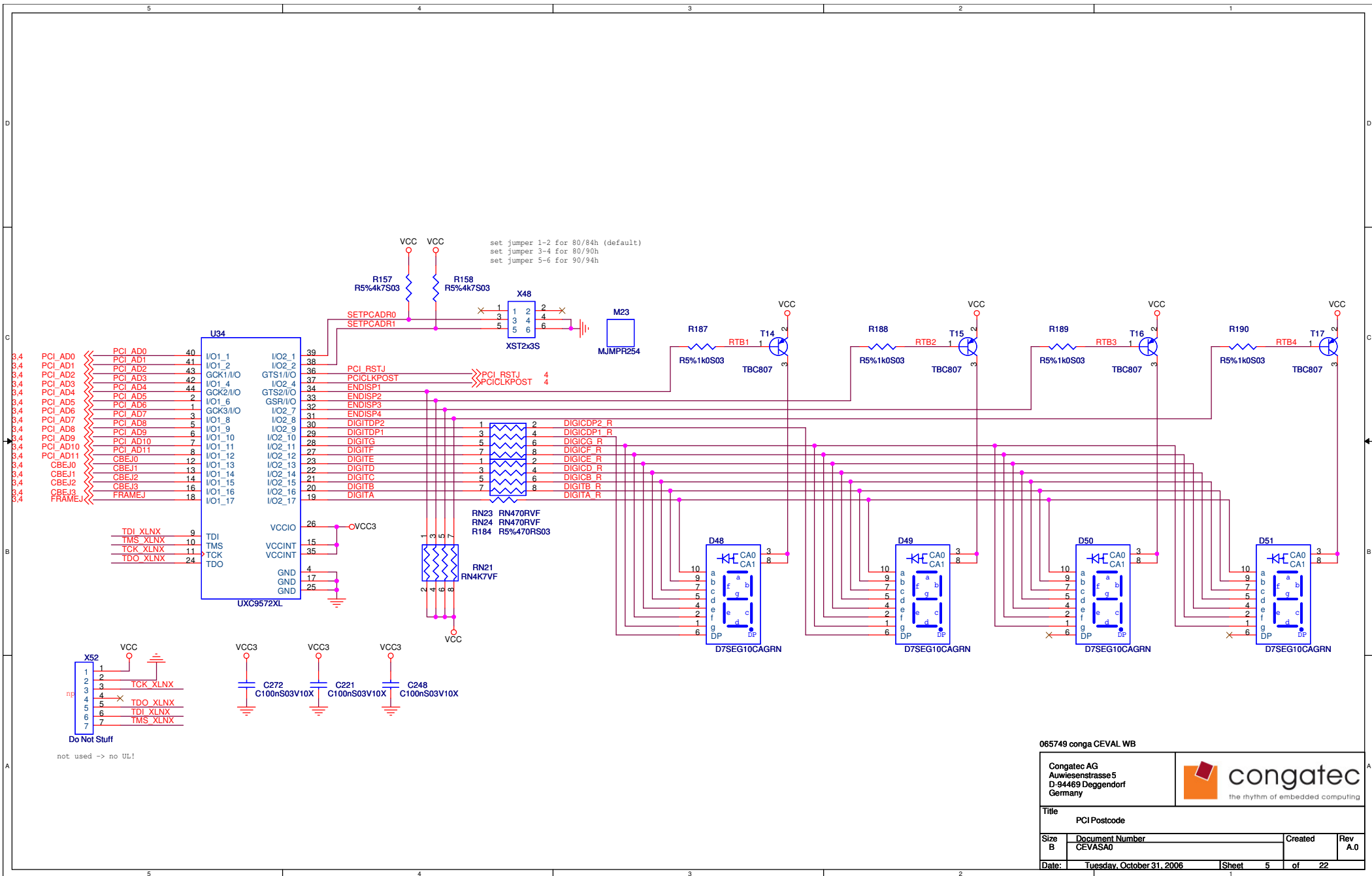


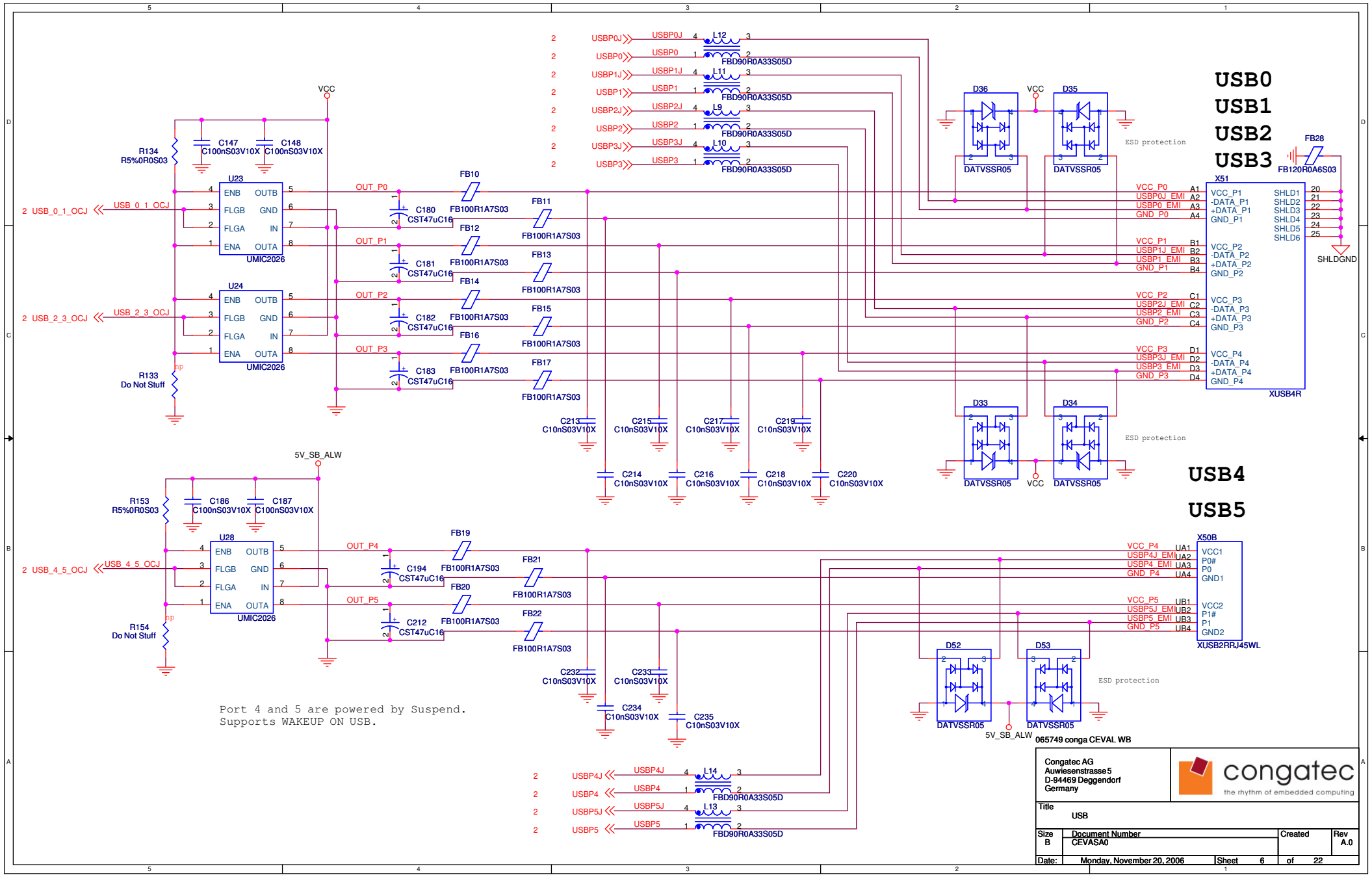
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Title	PCI Bus
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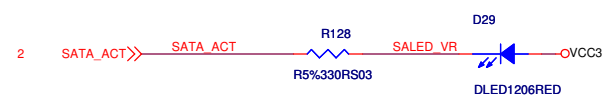
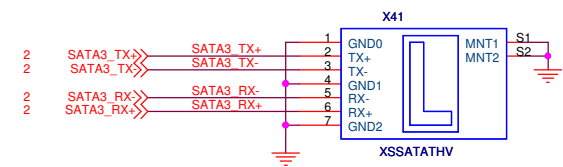
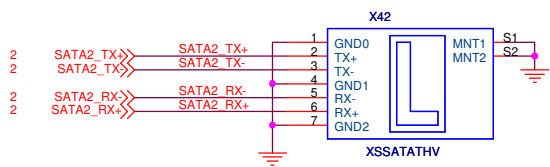
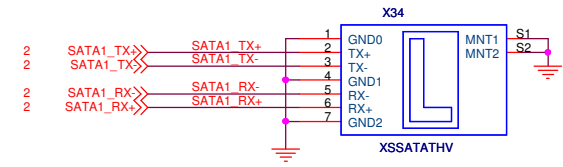
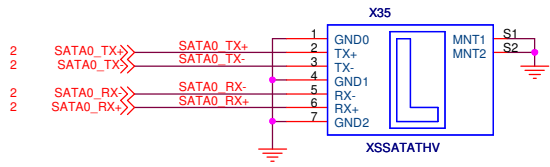


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USB			
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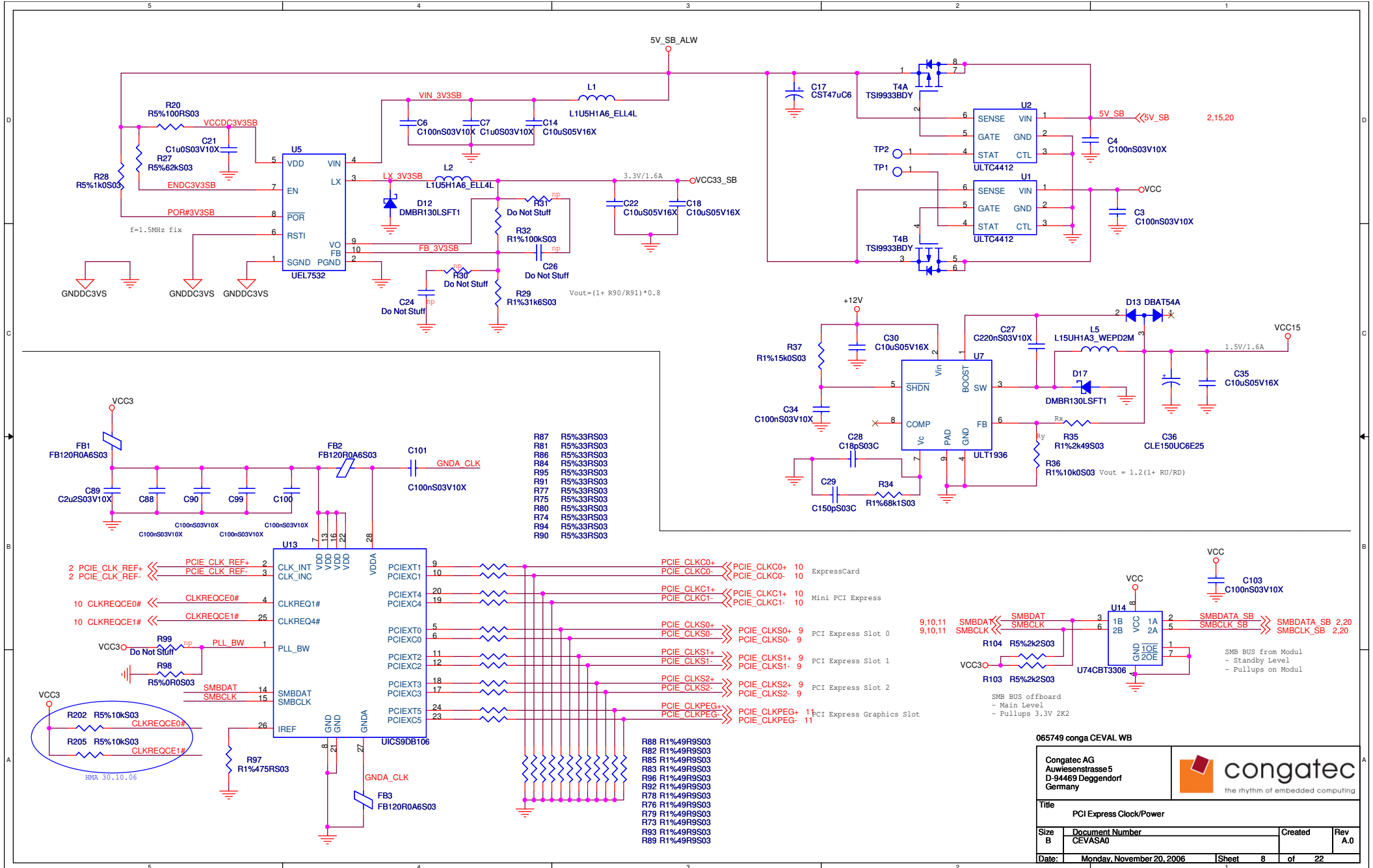


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SATA			
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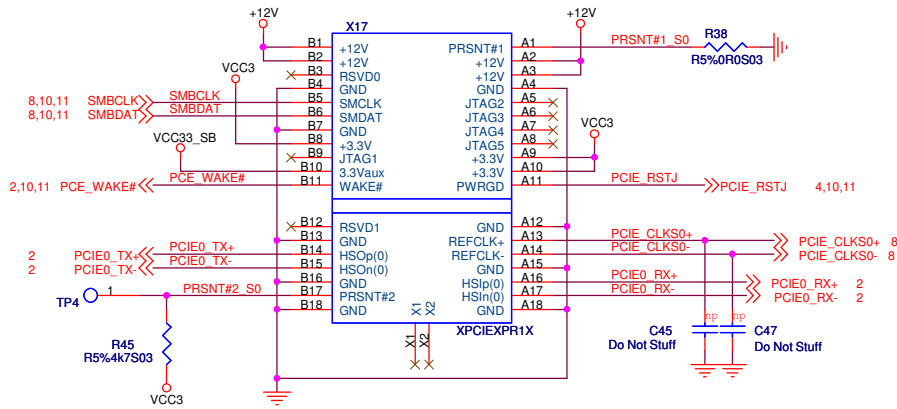
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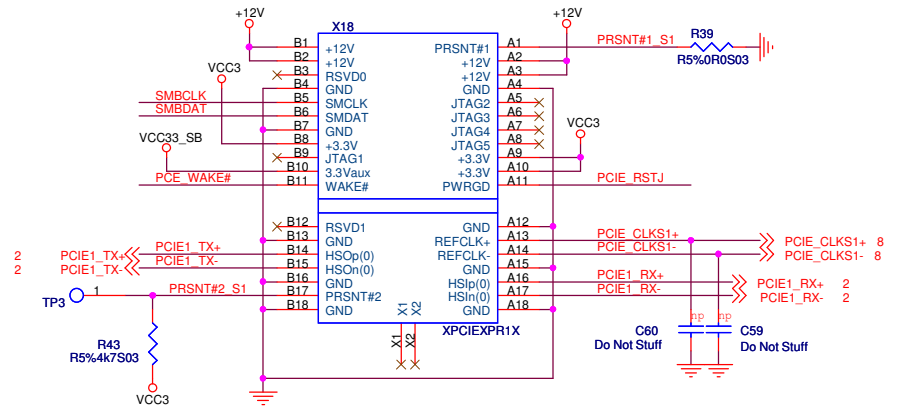
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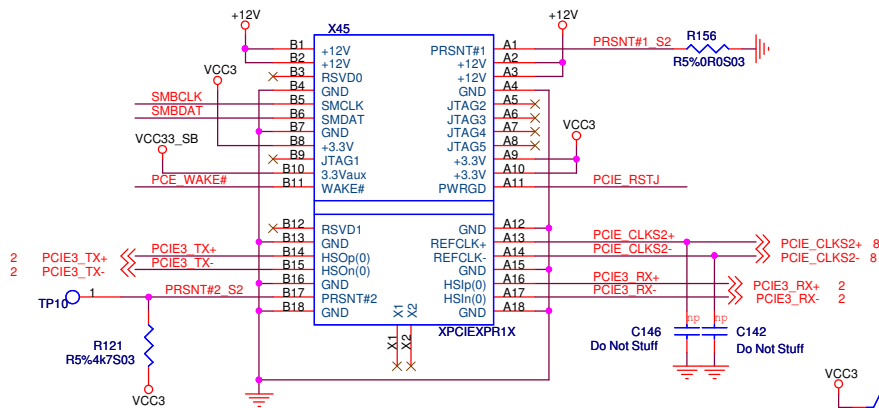
PCI Express SLOT 0 / Lane 0



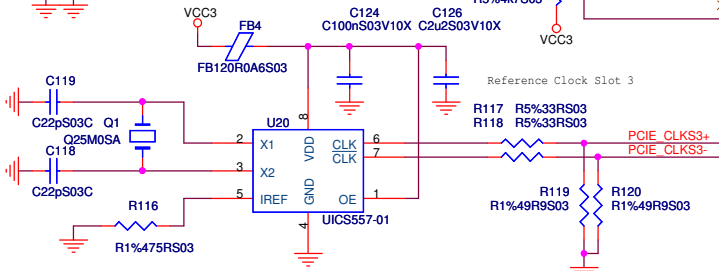
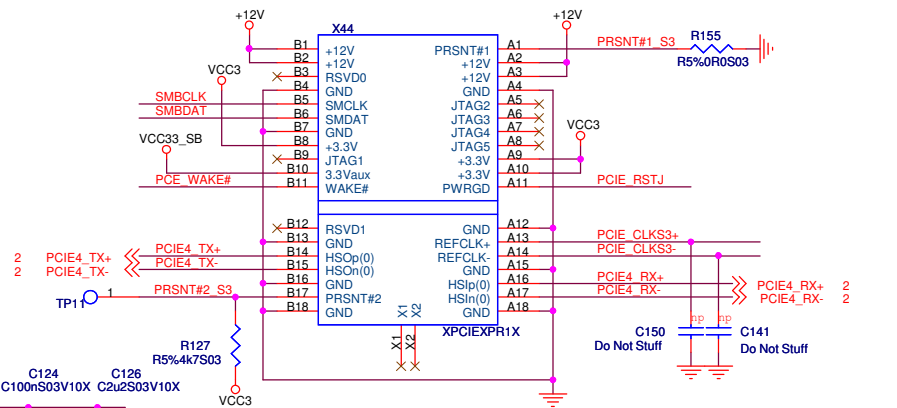
PCI Express SLOT 1 / Lane 1



PCI Express SLOT 2 / Lane 3



PCI Express SLOT 3 / Lane 4

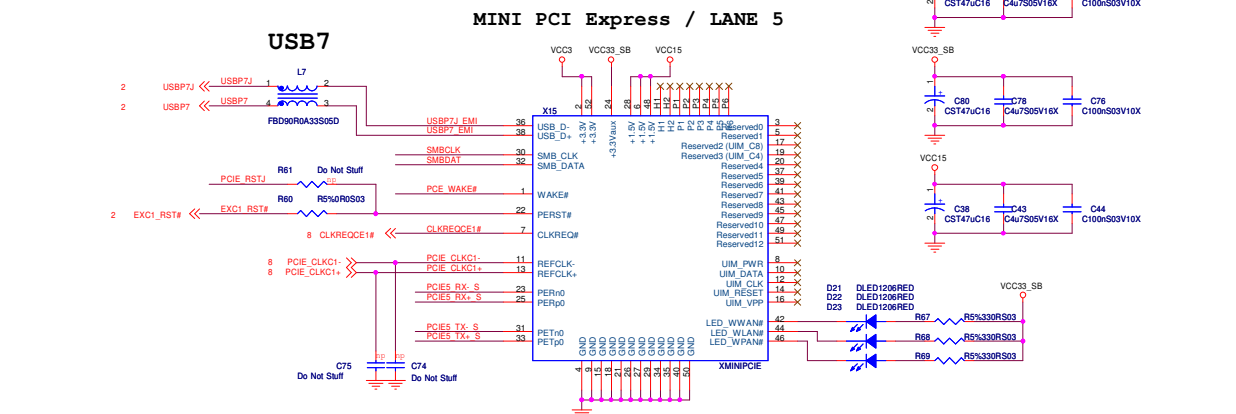
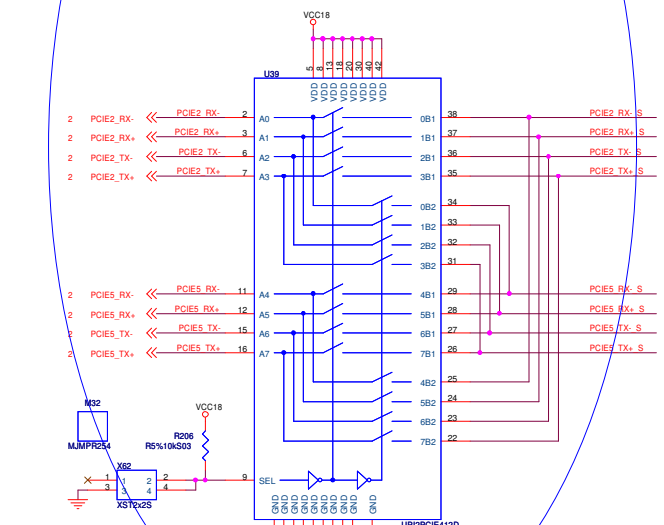
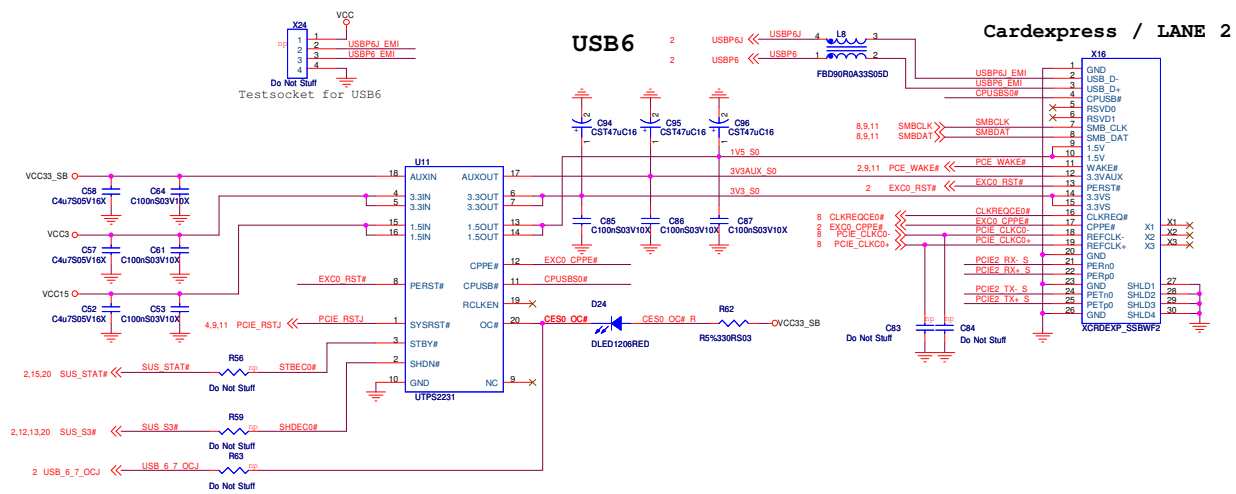
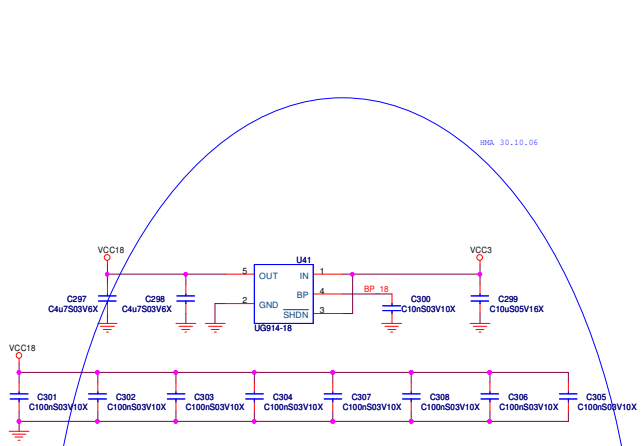


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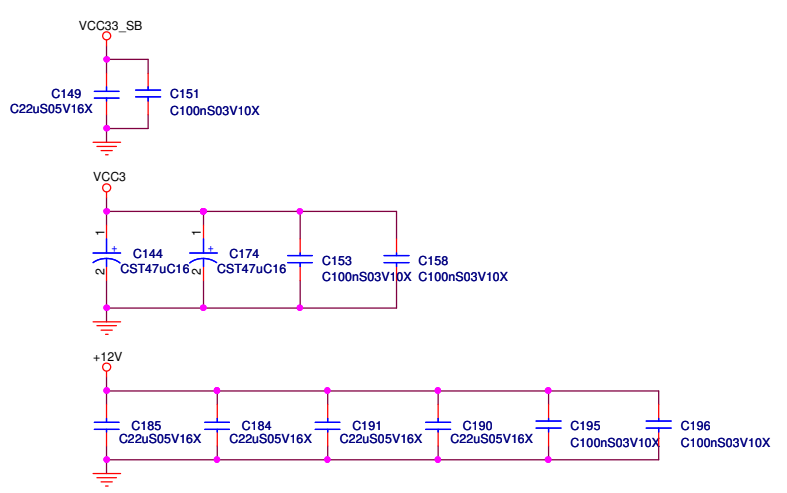
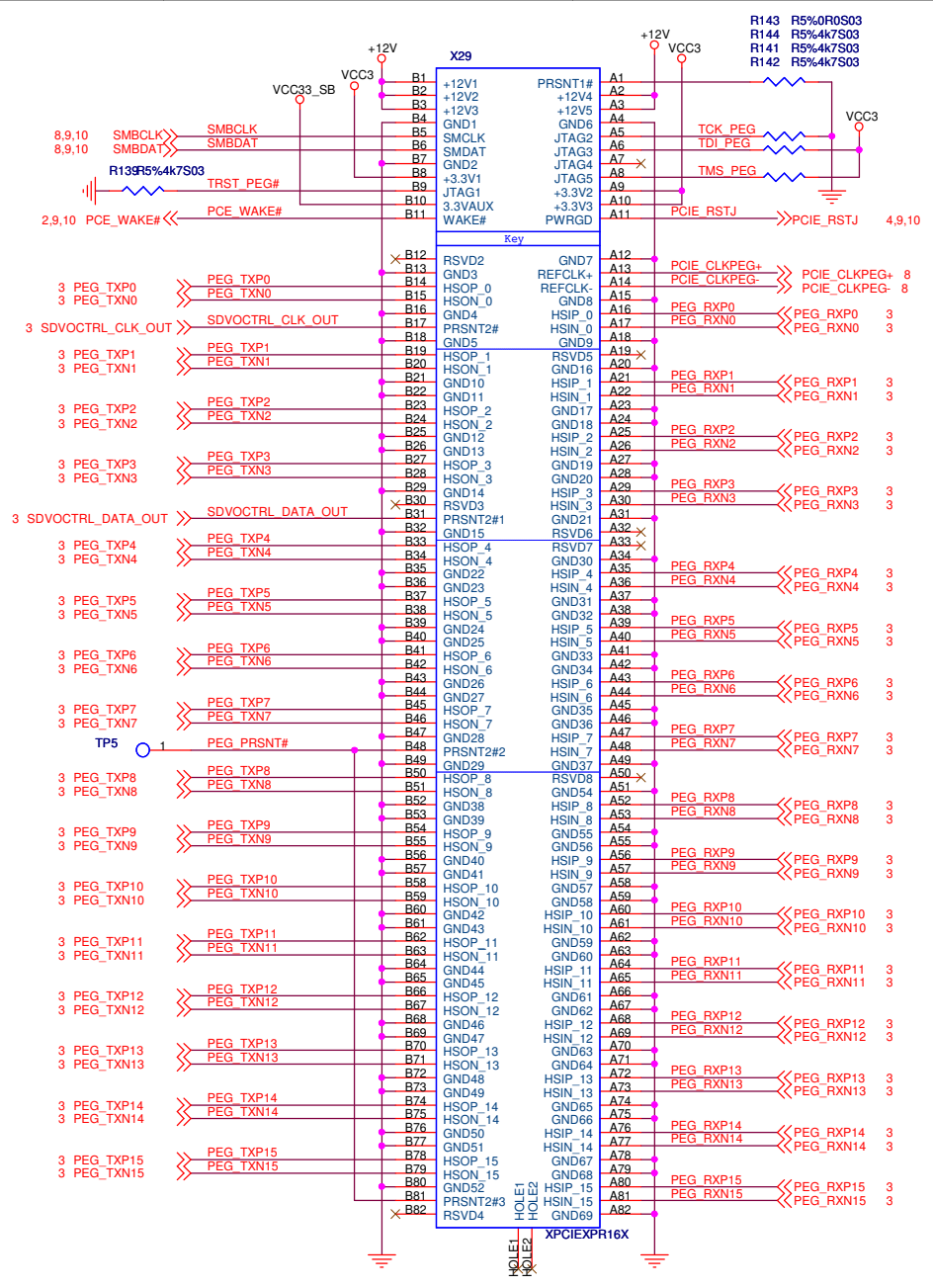
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set 1-2: PCIe lane 5 ExpressCard
 PCIe lane 2 Mini PCI Express
 set 3-4: PCIe lane 2 ExpressCard (default)
 PCIe lane 5 Mini PCI Express (default)

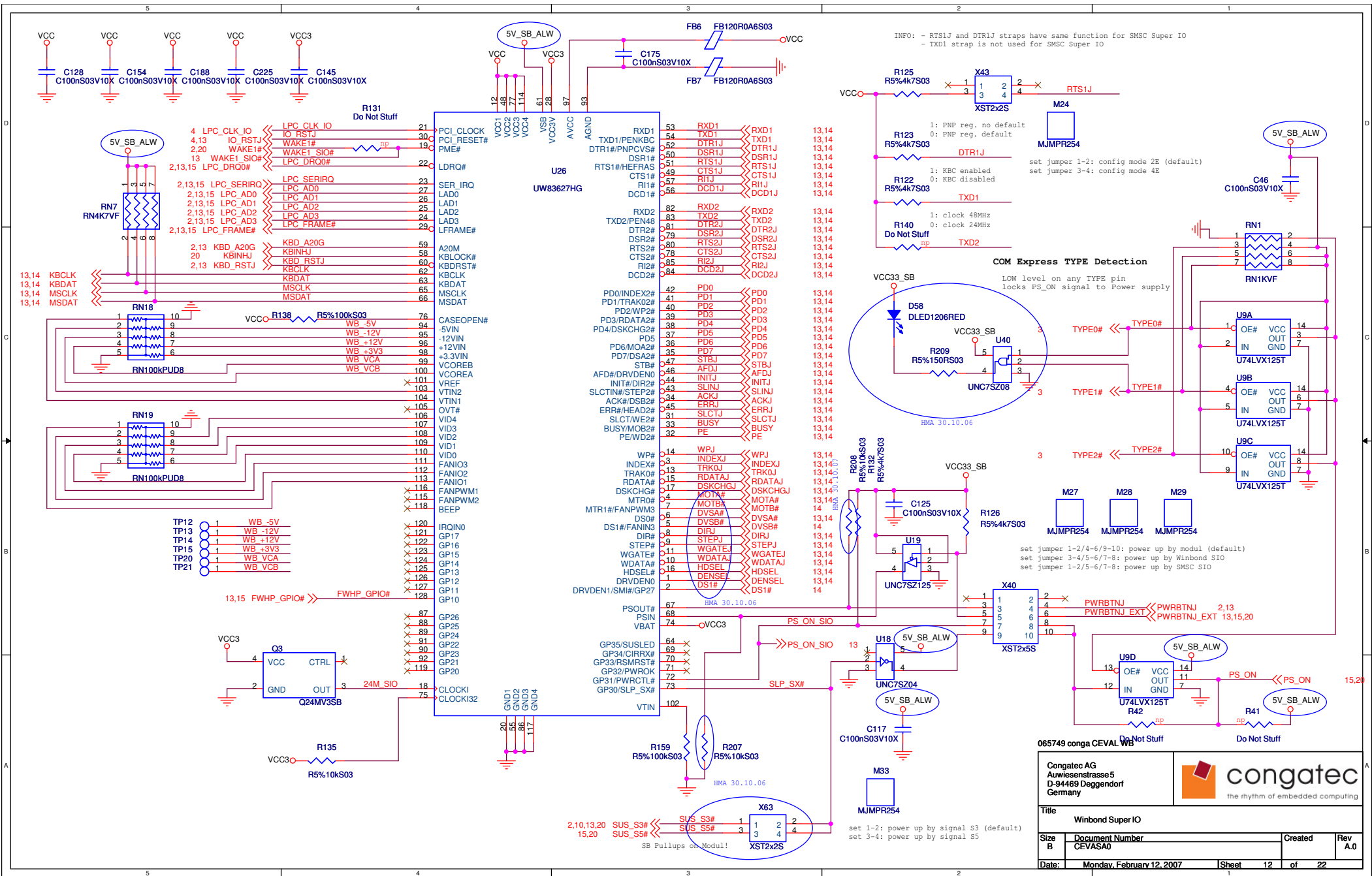


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Title			
PCI Express Graphic			
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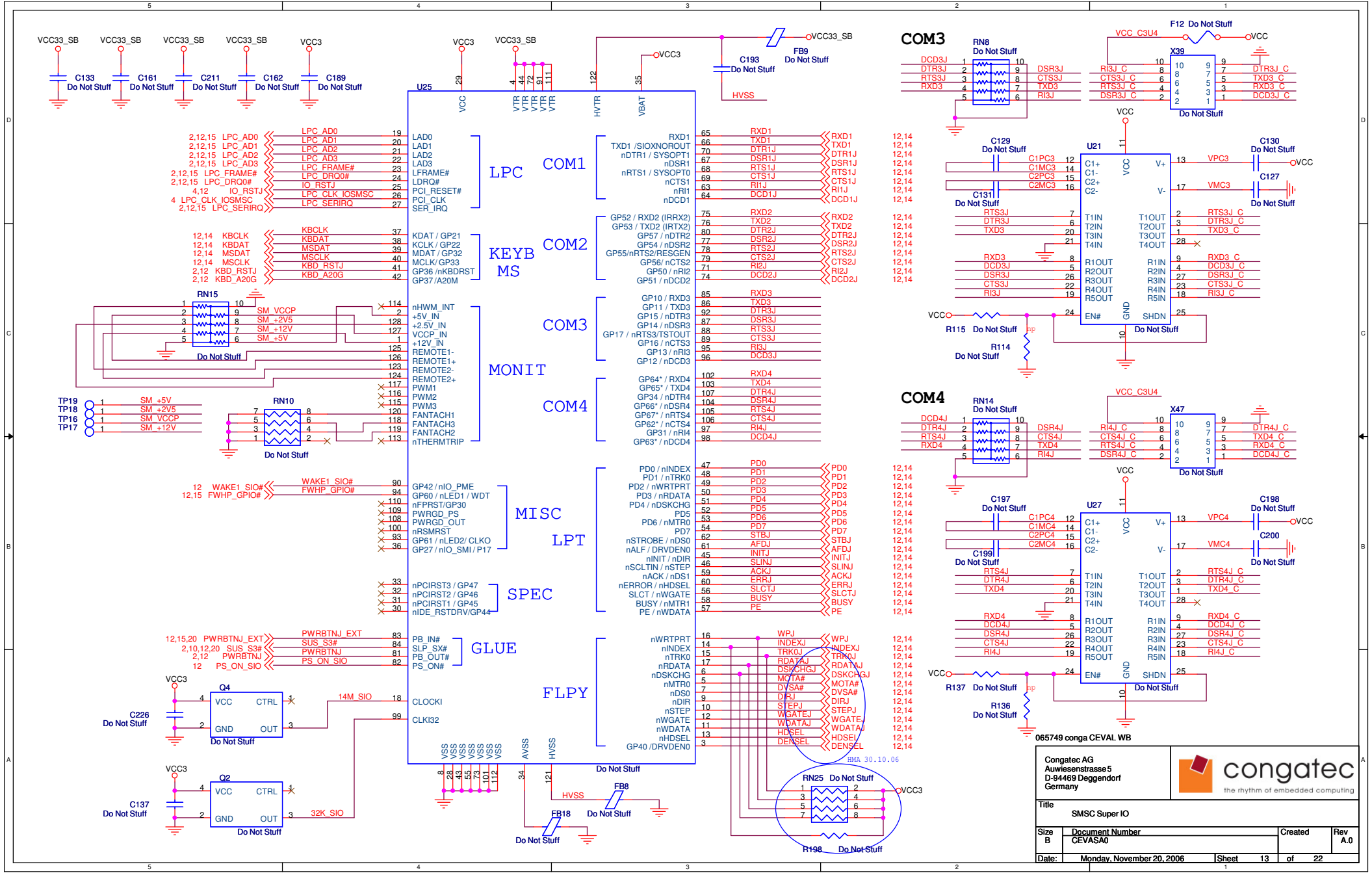
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Title: Winbond Super IO

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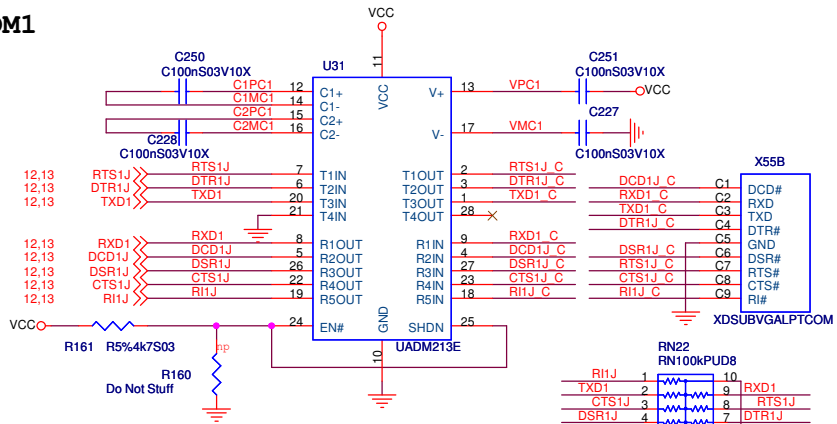
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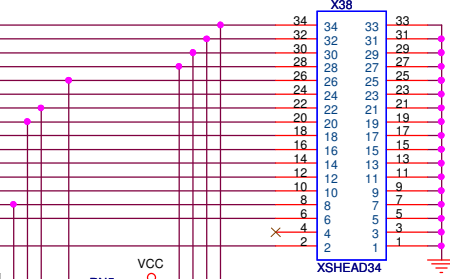
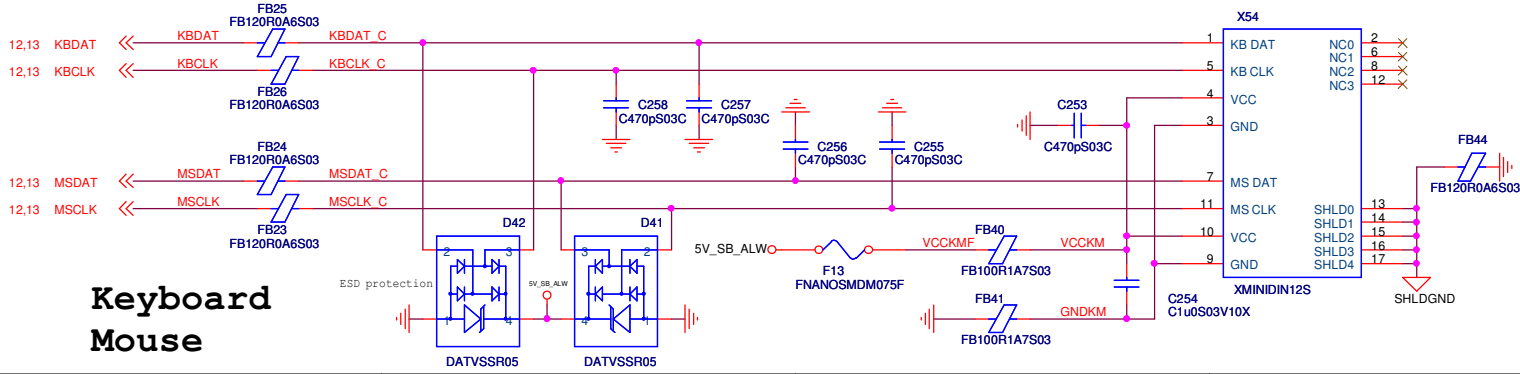
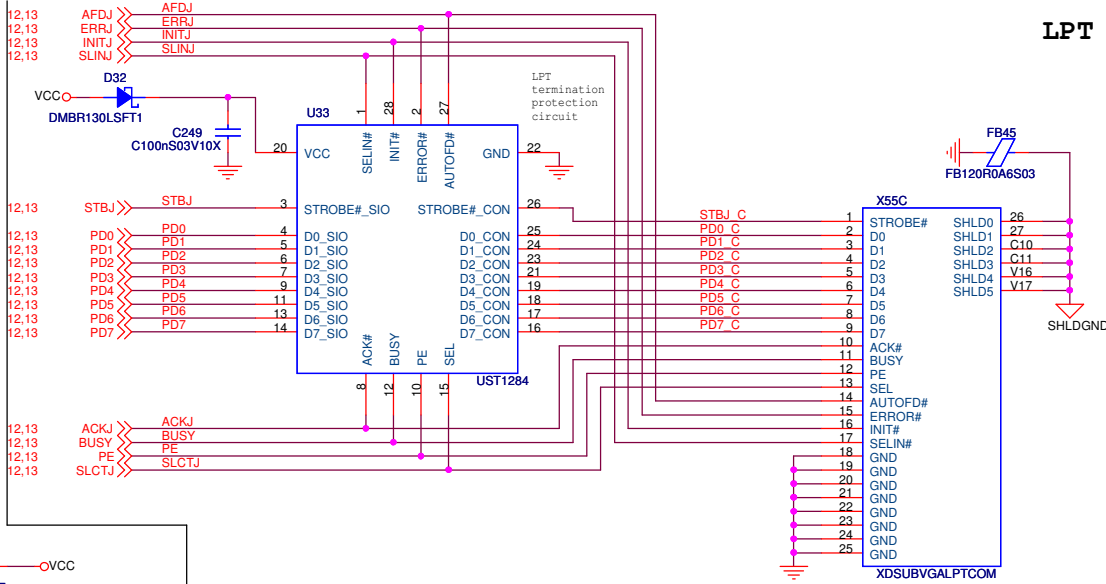
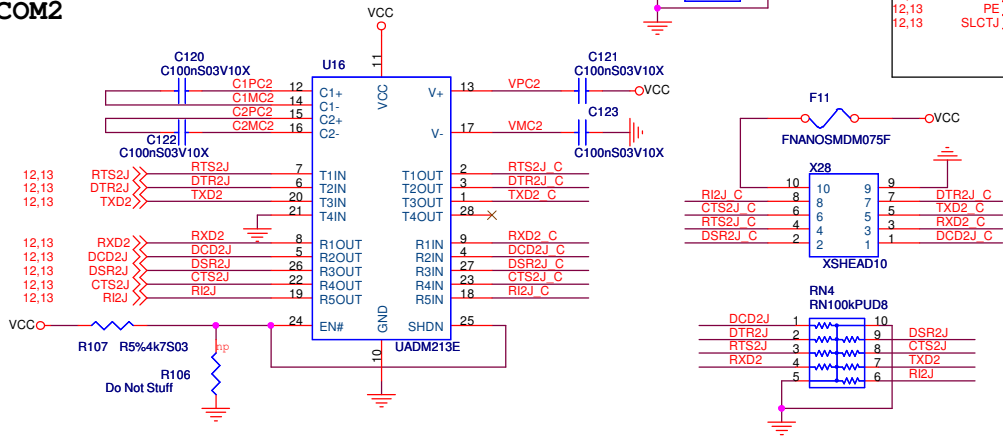
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COM1




COM2



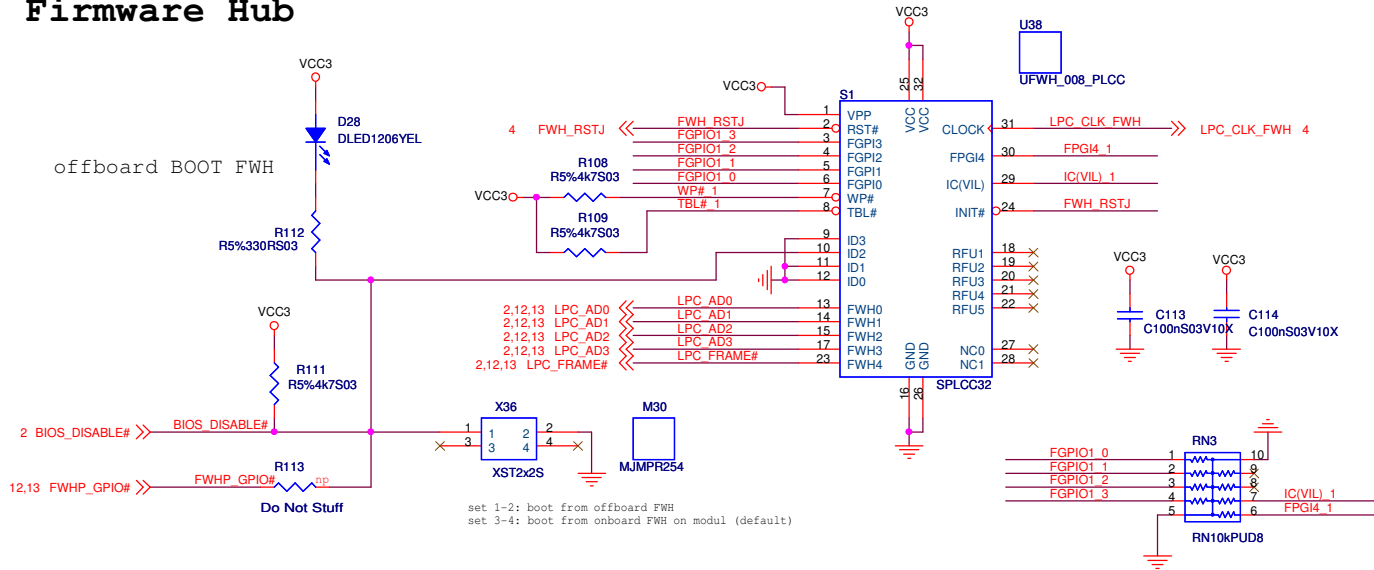
Floppy
 This connector supports only 1 drive (A or B)!
 2 drives destroy drives and interface!

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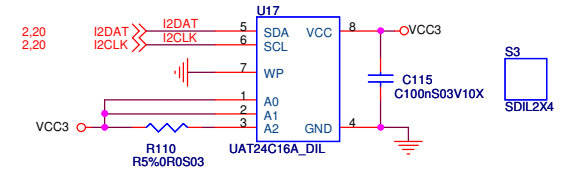
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Title: LPC FWHS			
Size: B	Document Number: CEVASA0	Created:	Rev: A.0
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**Keyboard
Mouse**

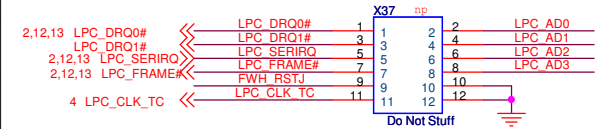
Firmware Hub



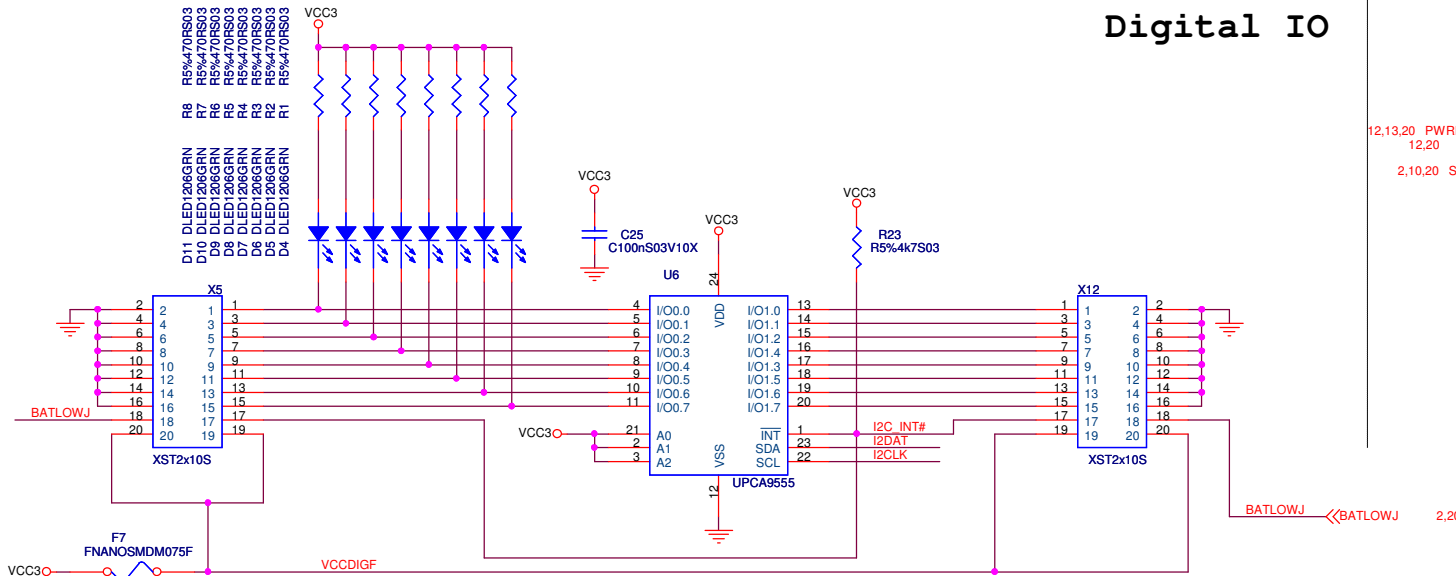
I2C Eeprom



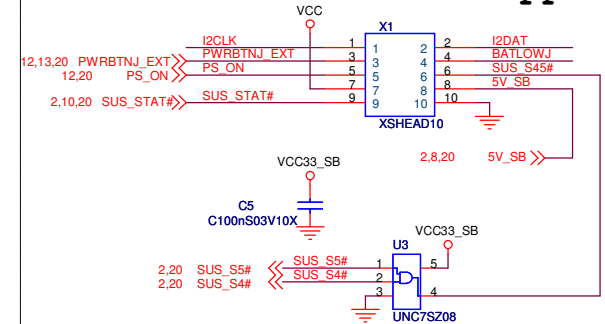
LPC Testconnector



Digital IO



Battery Support

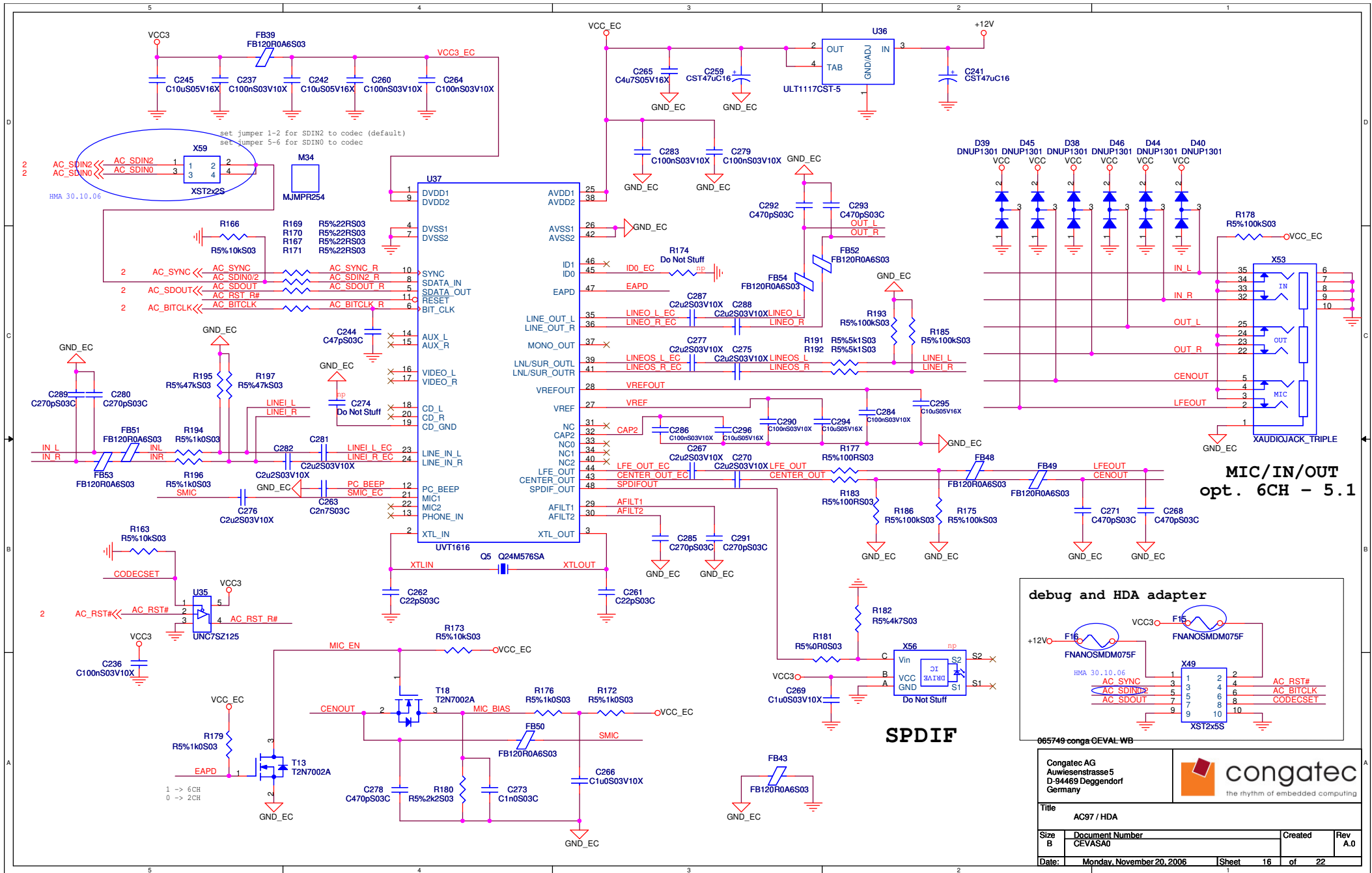



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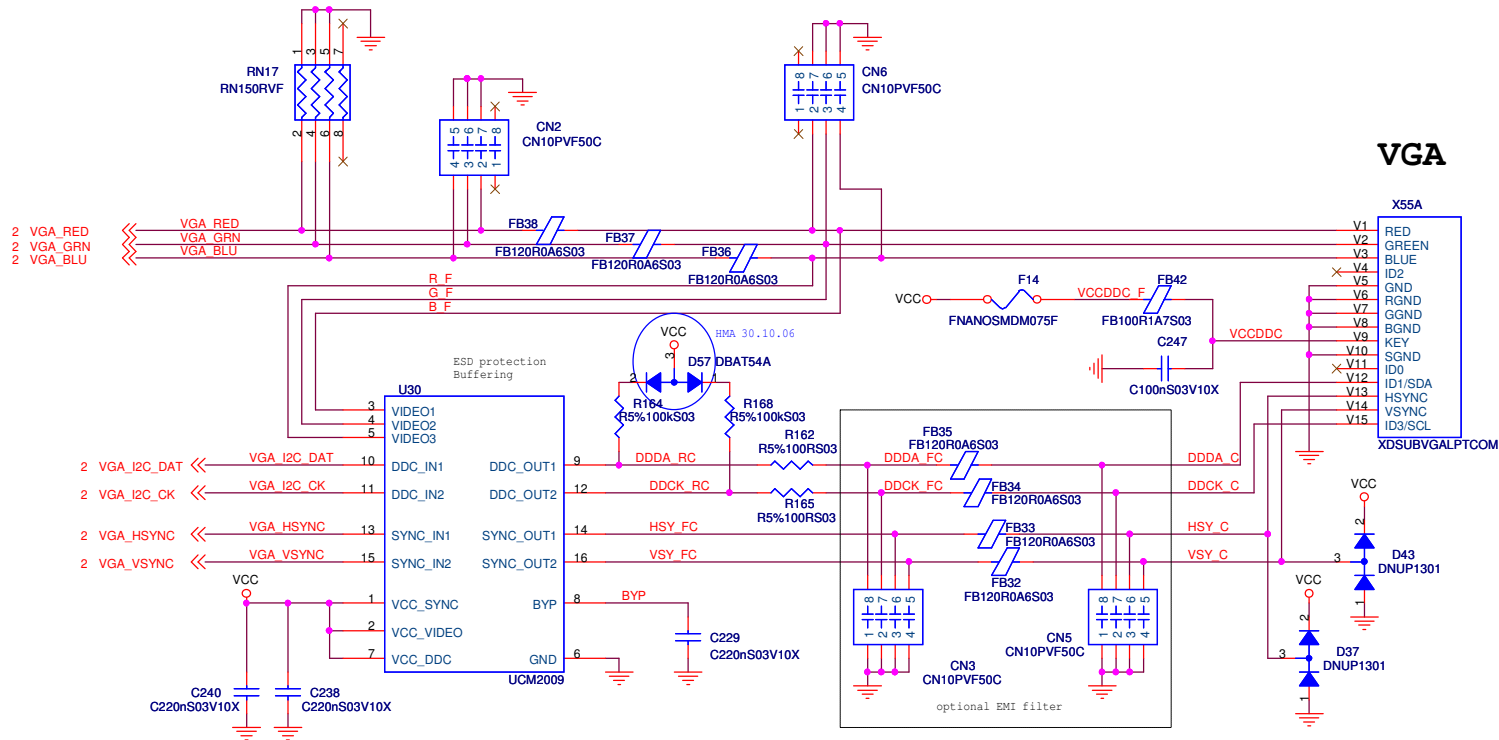
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Title			
LPC FW H / I2C			
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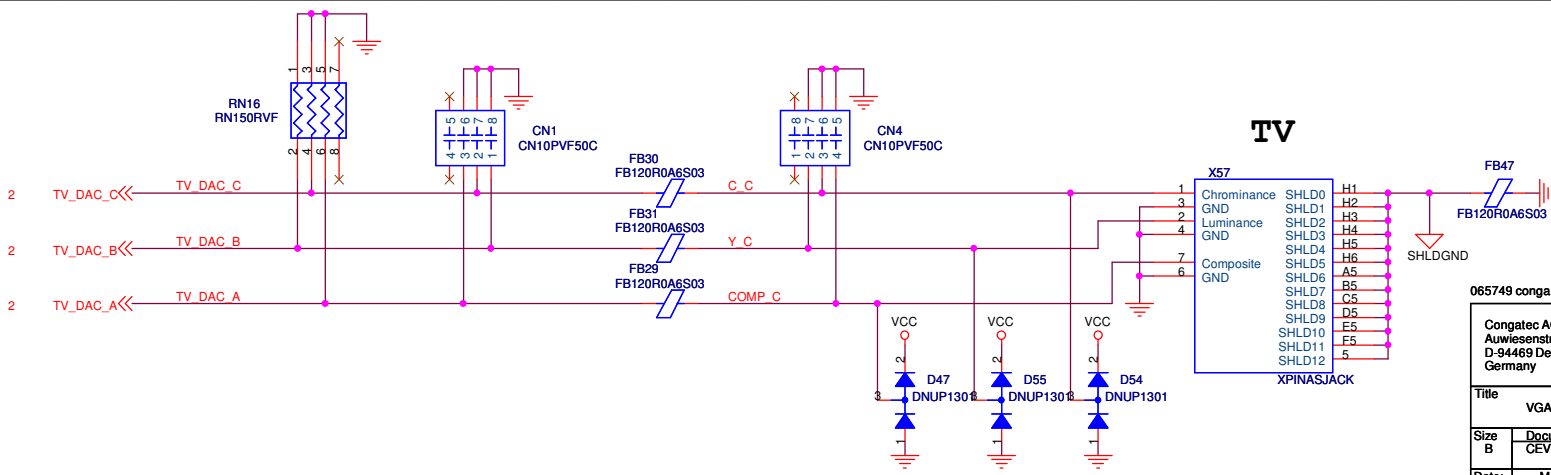


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Title: AC97 / HDA			
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VGA

- X55A**
- V1 RED
 - V2 GREEN
 - V3 BLUE
 - V4 ID2
 - V5 GND
 - V6 RGND
 - V7 GGND
 - V8 BGND
 - V9 KEY
 - V10 SGND
 - V11 ID0
 - V12 ID1/SDA
 - V13 HSYNC
 - V14 VSYNC
 - V15 ID3/SCL
- XDSUBVGA1PTCOM**
- VCC
 - D43 DNUP1301
 - VCC
 - D37 DNUP1301



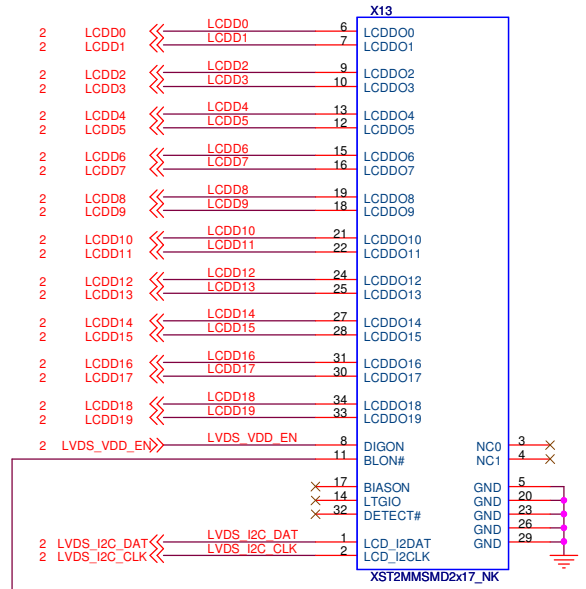
TV

- X57**
- 1 Chrominance SHLD0
 - 2 GND SHLD1
 - 3 Luminance SHLD2
 - 4 GND SHLD3
 - 5 Composite SHLD4
 - 6 GND SHLD5
 - 7 SHLD6
 - 8 SHLD7
 - 9 SHLD8
 - 10 SHLD9
 - 11 SHLD10
 - 12 SHLD11
 - 5 SHLD12
- XPINAJACK**
- H1
 - H2
 - H3
 - H4
 - H5
 - H6
 - A5
 - B5
 - C5
 - D5
 - E5
 - F5
 - 5

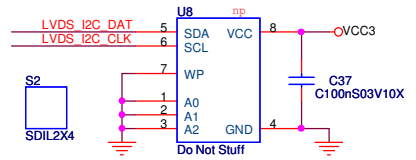
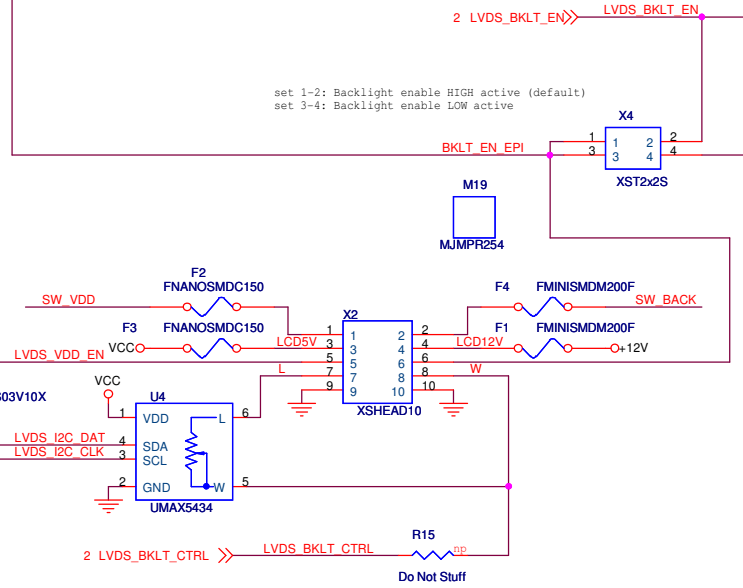
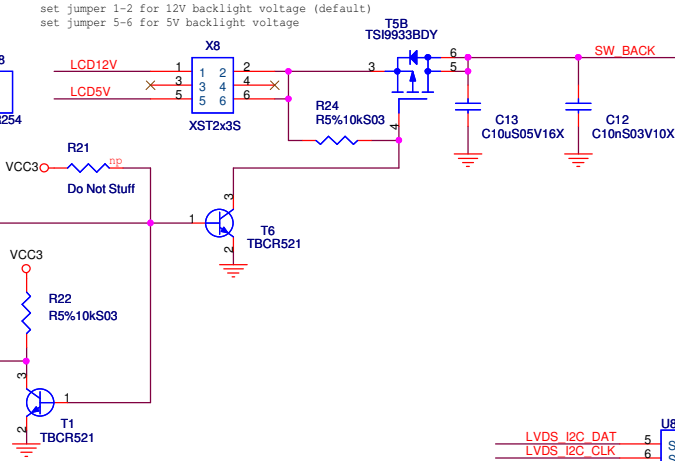
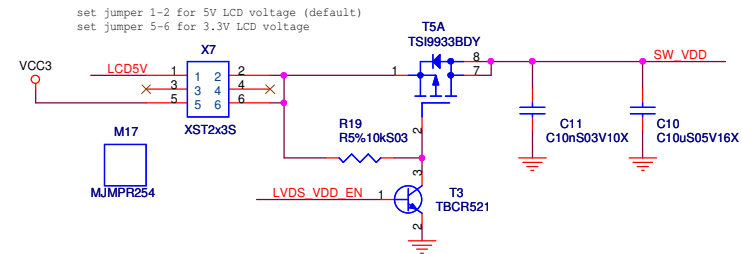
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VGA / TV			
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LCD



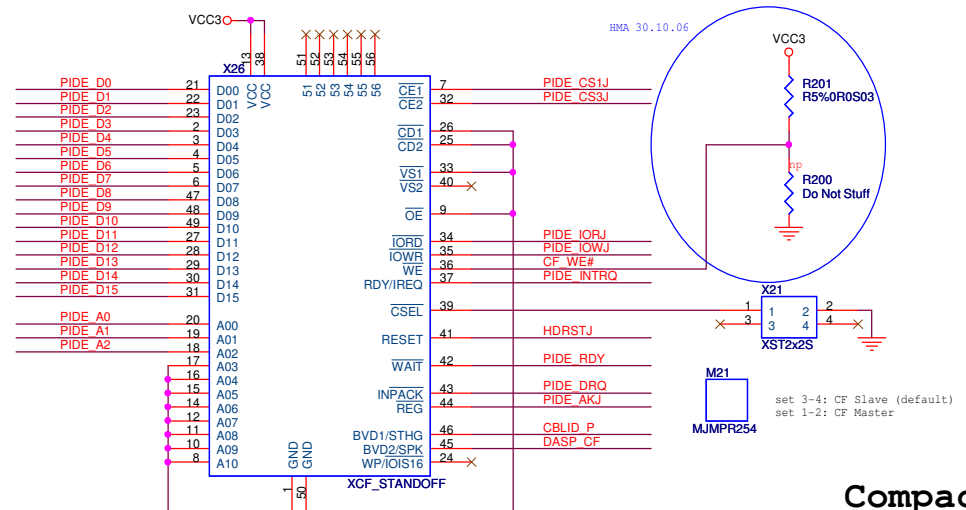
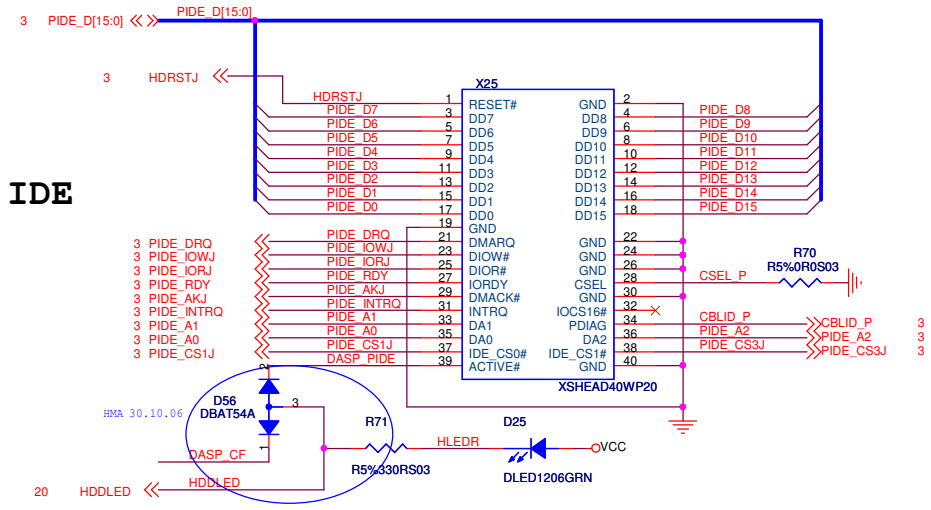
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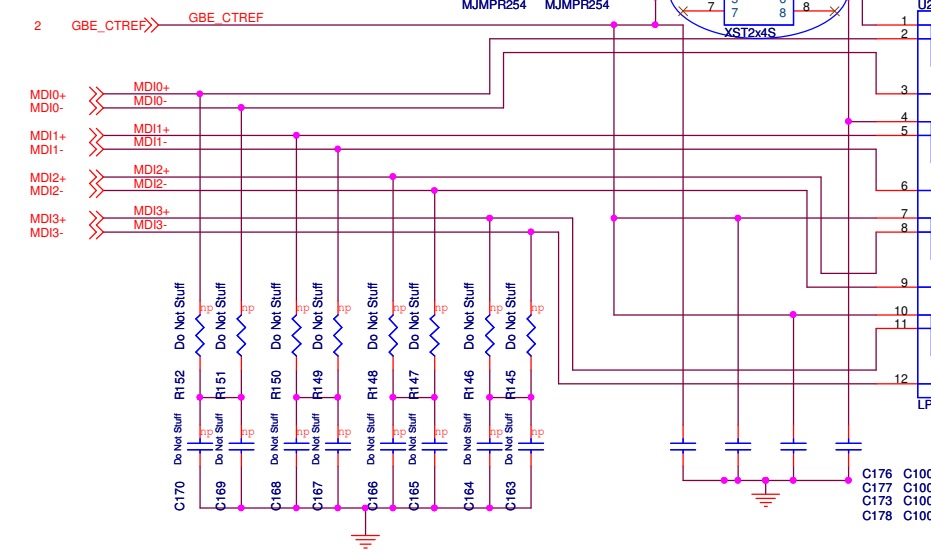
Title			
LCD			
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IDE



Compact Flash

set 3-4/7-8: 10/100
set 1-2/5-6: 10/100/1000 (default)



Ethernet

These three capacitors across the CHASSIS_GND to GND split, are located below the magnetics module. Place as close as possible to the magnetics module.

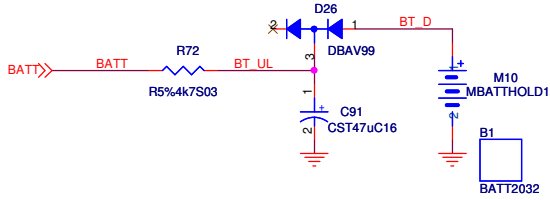
These three capacitors across the CHASSIS_GND to GND split, are located above the magnetics module. Place as close as possible to the magnetics module.

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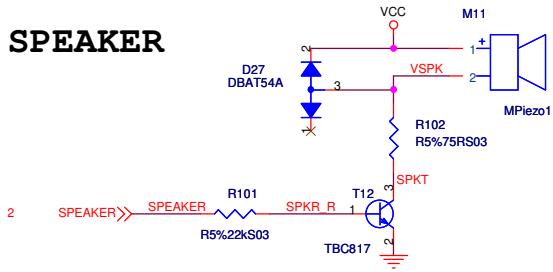
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Title IDE / CF / Ethernet			
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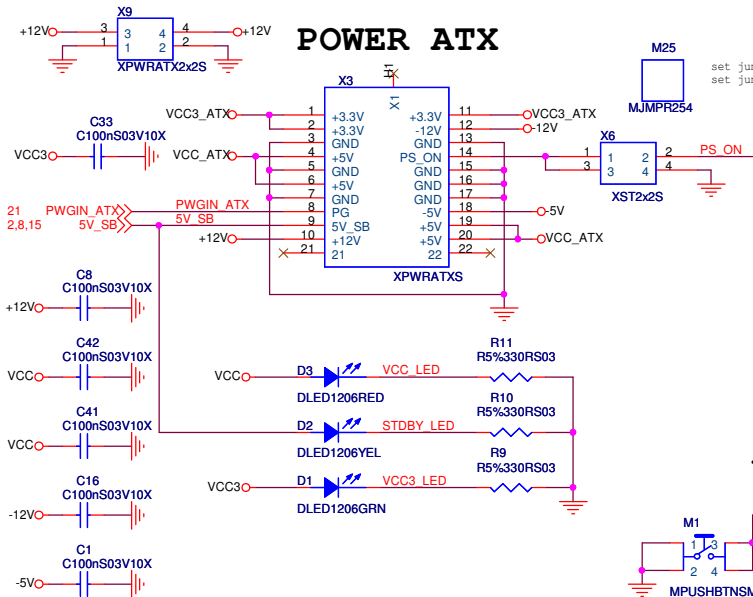
BATTERY



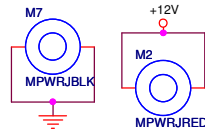
SPEAKER



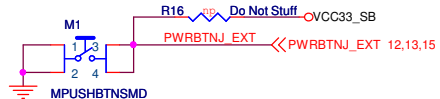
POWER ATX



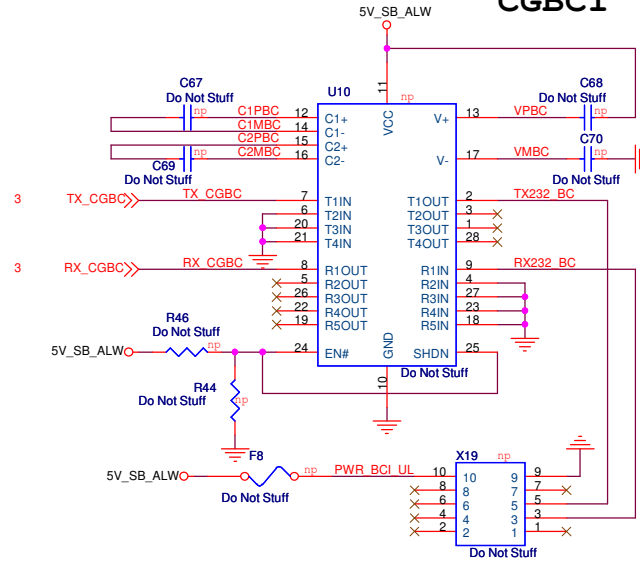
POWER AT



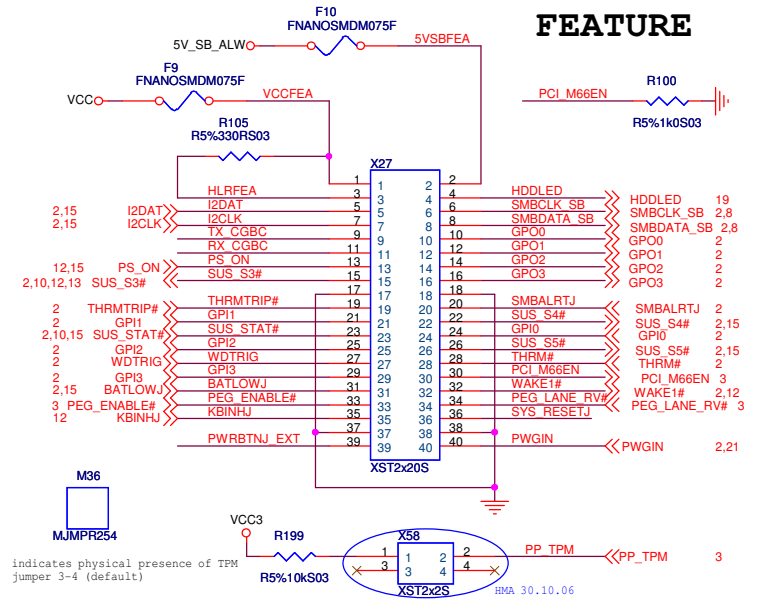
ATX ON



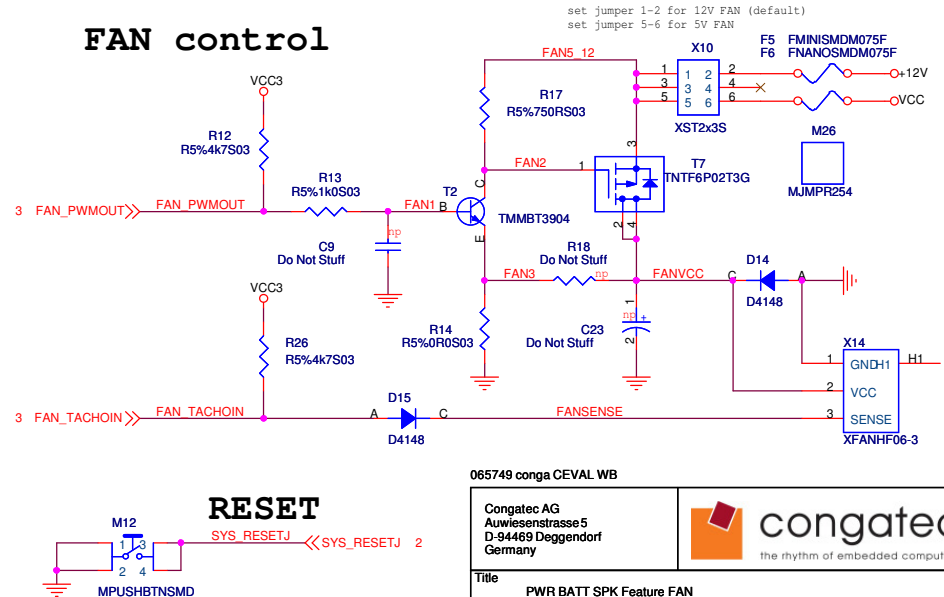
CGBCI



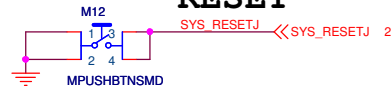
FEATURE



FAN control



RESET

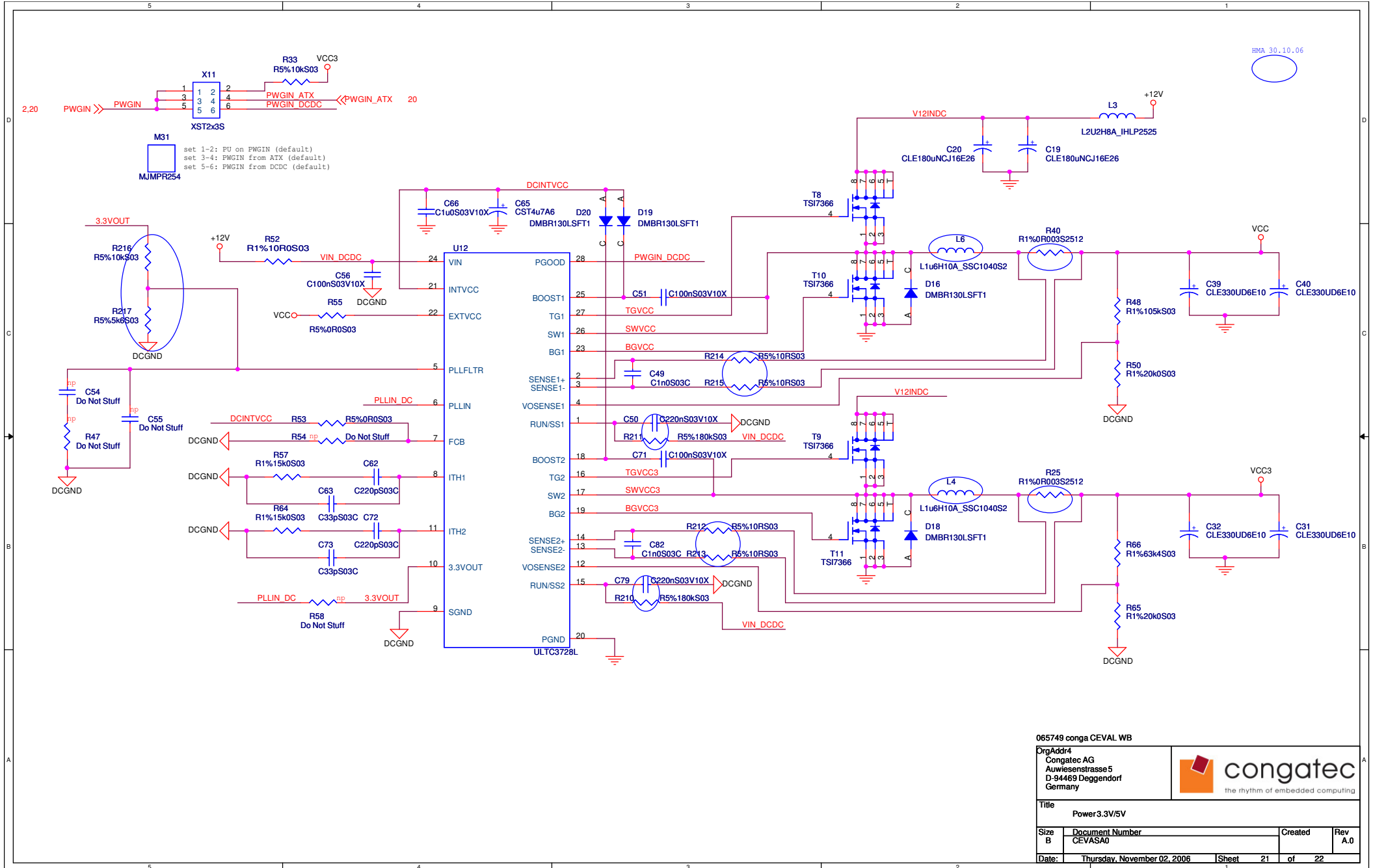


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PWR BATT SPK Feature FAN			
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Power 3.3V/5V			
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Revision History

Rev. X.0	17.05.06	HMA	0	design created
Rev. A.0	30.10.06	HMA	all	changed footprints to have in general 0603 only
			4	corrected C239 to 10p
			8	added pullups to CLKREQ#s of express clock buffer
			10	added MUXer to switch between PCIe lanes 2 and 5
			12	changed standby supply on Super IO to 5V_SB_ALW
				added PD to signal PSIN
				added jumper to switch between S3 and S5 for ATX power control
				added circuit to notify wrong module
			12,13,14	corrected floppy connection
			14	added 3.3V pullups for SMSC floppy connection
			16	added jumper to chose between SDIN0 and SDIN2 for codec
				added fuses to HDA connector to be consistent with UL
			17	corrected RN17 to 150R
				added diode to DDC PUs to prevent leakage
			3,20	signal PP_TPM added to COM express pinout and added feature jumper
			19	removed jumper for VCC and WE# from CF connector
				modified jumper setting for 10/100 Ethernet option
				corrected connection of IDE LED
			21	modified circuit to get better performance

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