

5. CIRCUIT DESCRIPTION

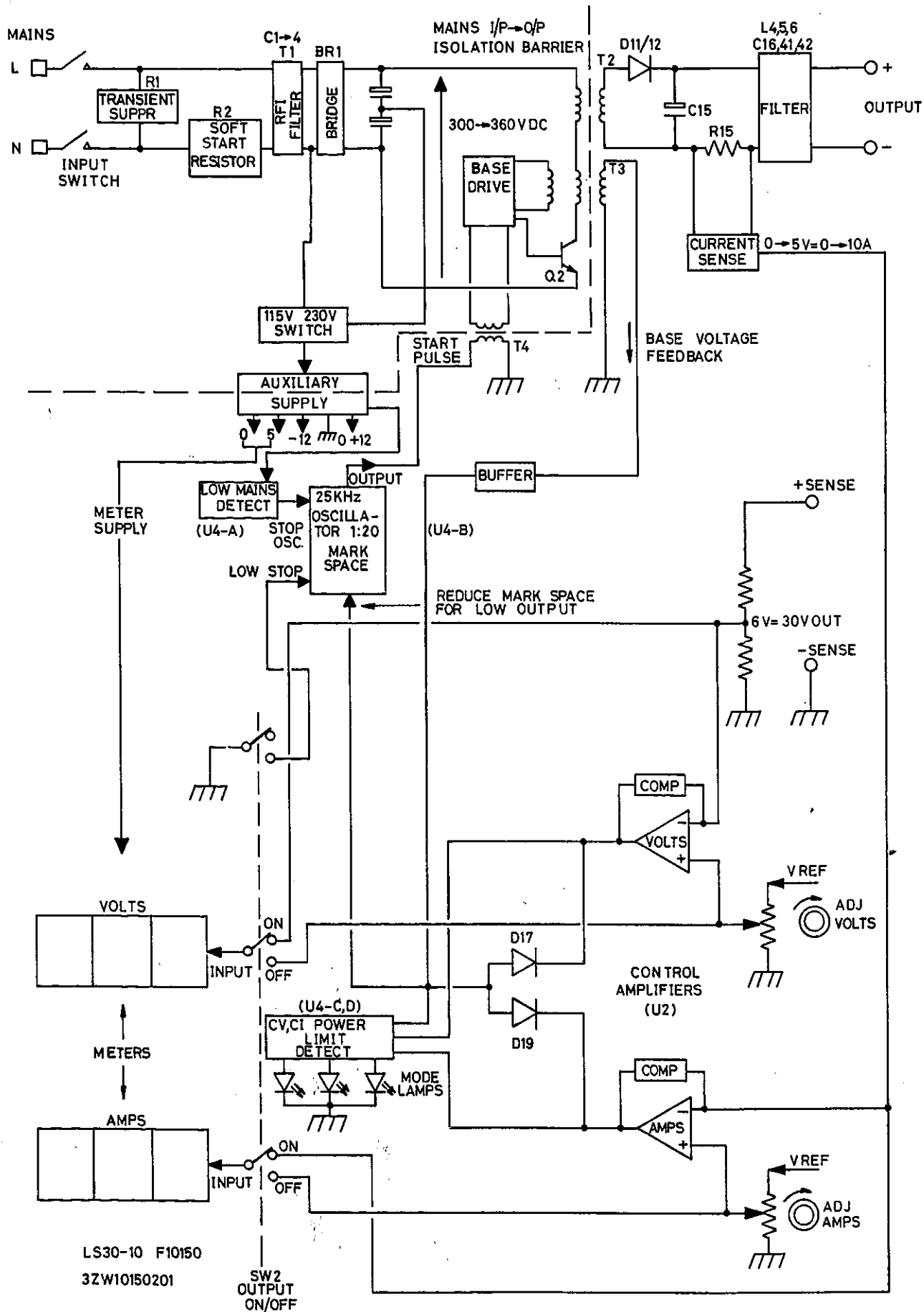


Fig 4 Block Schematic