Product Center

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+ Small household appliances non-isolated products
+ Small household appliances non-isolated products
+ Power Devices
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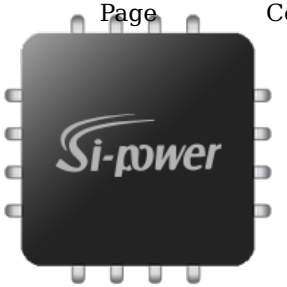
Audio/Wireless Products

Audio Series

Audio Decoding Master Control Series



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SP6007

It is a highly integrated BI-CMOS low-frequency receiving decoding chip with high sensitivity, low power consumption and other characteristics.

Detailed description | Documentation and tools | Samples and Purchases

The SP6007 is a highly integrated BI-CMOS low-frequency receiving decoding chip with high sensitivity, low power consumption and other characteristics. It can demonetize multi-country radio signals, including the United States (WWVB), Germany (DCF77), Japan (JJY40 and JJY60), the United Kingdom (MSF), and Switzerland (HBG). The SP6007 includes a unique dual-frequency crystal compensation function, single/dual frequency radio wave reception function, and AGC locking function, making SP6007 an extremely ideal radio signal receiving circuit.

Functions

Low power consumption (<90μA).
High sensitivity (0.4 μV).
● Dual-frequency crystal compensation pin, the circuit has the capacitance of the compensated crystal, can be used to achieve high selectivity.
• State of sleep.
● Few peripheral components required.
The AGC lock function.
Wide frequency band range (40-120 KHz).
● Strong anti-interference ability.

Radio clock series

+ Electronics in the automotive industry

Samples and Purchases

Sample application

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