







Kommunikationsvarianten der V24 (JP4 und JP8 auf AVR CP/M Board)

V	Beschreibung	Verbindungen	Jumperbelegung	Pinbelegung	
				JP4	JP8
1	CP/M auf VGA	Rx AVR – Tx VGA Tx AVR – Rx VGA	 JP4  JP8	1 Rx FTDI 2 Tx VGA 3 Tx FTDI 4 Rx AVR	1 Tx FTDI 2 Rx VGA 3 Rx FTDI 4 Tx AVR
2	CP/M auf USB	Rx AVR – Tx FTDI Tx AVR – Rx FTDI	 JP4  JP8	1 Rx FTDI 2 Tx VGA 3 Tx FTDI 4 Rx AVR	1 Tx FTDI 2 Rx VGA 3 Rx FTDI 4 Tx AVR
3	VGA auf USB	Tx VGA – Rx FTDI Rx VGA – Tx FTDI	 JP4  JP8	1 Rx FTDI 2 Tx VGA 3 Tx FTDI 4 Rx AVR	1 Tx FTDI 2 Rx VGA 3 Rx FTDI 4 Tx AVR