

Table 1. Connections required for ISP and debugWIRE

JTAGICE mkII probe	ISP6PIN header	ISP	debugWIRE
Pin 1 (TCK)	Pin 3 SCK	x	
Pin 2 (GND)	Pin 6 GND	x	x
Pin 3 (TDO)	Pin 1 MISO	x	
Pin 4 (VTref)	Pin 2 V _{CC}	x	x
Pin 6 (nSRST)	Pin 5 RESET	x	x
Pin 9 (TDI)	Pin 4 MOSI	x	